

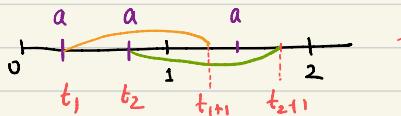
# TIMED AUTOMATA

## LECTURE 19

Clarification about the expressive power of 1-ATA:

Question:

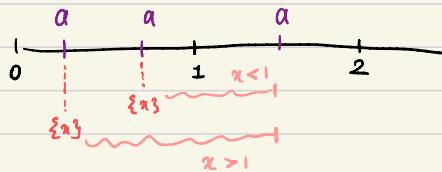
$$\text{Let } L_1 = \{ (aaa, t_1, t_2, t_3) \mid 0 < t_1 < t_2 < 1 \\ t_1 + 1 < t_3 < t_2 + 1 \}$$



Can you construct a 1-ATA for  $L_1$ ?

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Idea:



$$1\text{-ATA: } (q_0, a, 0 < x < 1) \longrightarrow (p_1, \{x3\}) \wedge (s_1, \phi)$$

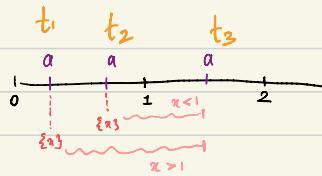
$$(p_1, a, \text{true}) \longrightarrow (p_2, \phi)$$

$$(s_1, a, x < 1) \longrightarrow (s_2, \{x3\})$$

$$(p_2, a, x > 1) \longrightarrow (f, \phi)$$

$$(s_2, a, x < 1) \longrightarrow (f, \phi)$$

$$(f, a, \text{true}) \mapsto (\text{reject}, \phi), \quad (\text{reject}, a, \text{true}) \mapsto (\text{reject}, \phi)$$



1-ATA:  $(q_0, a, 0 < x < 1) \mapsto (p_1, \exists x_3) \wedge (s_1, \phi)$

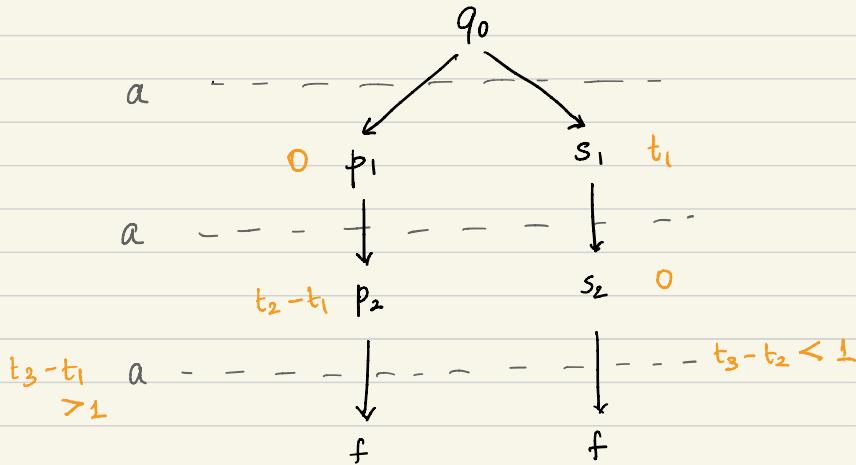
$(p_1, a, \text{true}) \mapsto (p_2, \phi)$

$(s_1, a, x < 1) \mapsto (s_2, \exists x_3)$

$(p_2, a, x > 1) \mapsto (f, \phi)$

$(s_2, a, x < 1) \mapsto (f, \phi)$

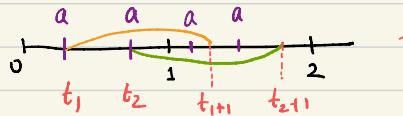
$(f, a, \text{true}) \mapsto (\text{reject}, \phi)$ ,  $(\text{reject}, a, \text{true}) \mapsto (\text{reject}, \phi)$



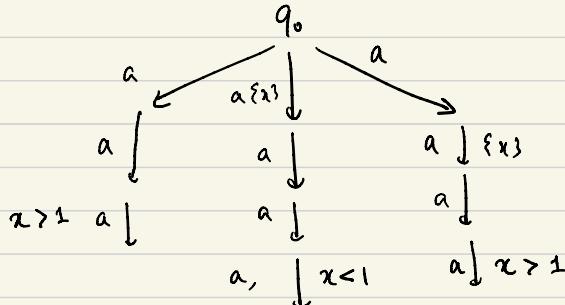
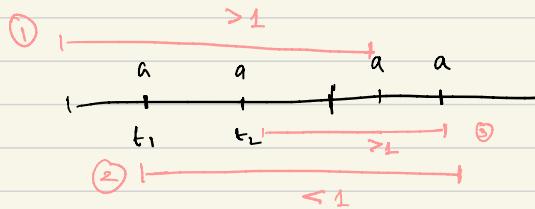
A small modification of the previous example:

Question:

$$\text{Let } L_2 = \{aaa, t_1 t_2 t_3 t_4 \mid 0 < t_1 < t_2 < 1 \\ 1 < t_3 < t_4 \\ t_1 + 1 < t_4 < t_2 + 1\}$$



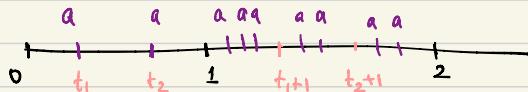
Can you construct a 1-ATA for  $L_2$ ?



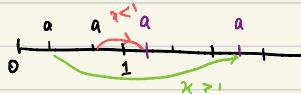
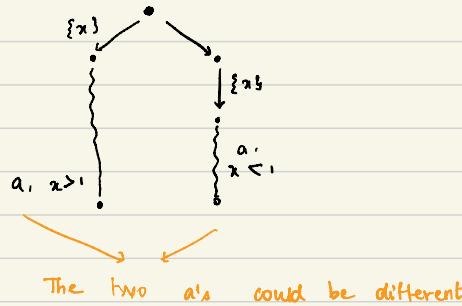
Question:

$$\lambda_3 = \{ (a^k, t_1, t_2, \dots, t_k) \mid k \geq 3 :$$

$$\begin{aligned} 0 < t_1 < t_2 < 1 \\ \exists j \geq 3 \text{ s.t. } t_{j+1} < t_j < t_{j+1} \\ t_3 > 1 \end{aligned} \quad \}$$



Problem:

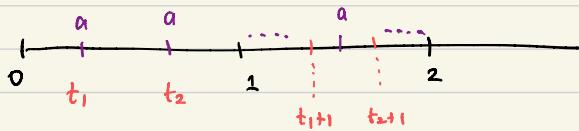


- This is an intuition that  $\lambda_3$  cannot be accepted by a 1-ATA.
- However, proving that a language cannot be accepted by a 1-ATA is difficult.
- We will see another example given in the paper, for which there is a proof that it cannot be accepted by a 1-ATA.

$$L = \{ (a^k, t_1 t_2 \dots t_k) \mid 0 < t_1 < t_2 < 1$$

$$1 < t_3, \dots, t_k < 2$$

there is exactly one  $a$  between  
 $t_1 + 1$  and  $t_2 + 1$



- $L$  can be accepted by a deterministic T-A with 2 clocks.

Goal: To prove that  $L$  cannot be accepted by a 1-ATA.

Step 1: Understand some property of DFAs

Step 2: How Step 1 translates to untimed alternating finite automata

Step 3: Any 1-ATA accepting  $L$  behaved like an untimed AFA in the interval  $(1, 2)$ , where clocks are useless.

Step 4: Use Step 1 and 2 in 3 to get a contradiction.

## Step 1: Understanding a property of DFA.

- Consider a unary alphabet  $\{a\}$ , and DFA  $\mathcal{B} = (\mathbb{Q}, q_0, \delta, F)$
- For each  $a^k$ , the DFA gives rise to a function

$$f_k^{\mathcal{B}} : \mathbb{Q} \rightarrow \mathbb{Q}$$



- The number of functions from  $\mathbb{Q} \rightarrow \mathbb{Q}$  is finite.
- therefore, if we look at the sequence :

$$f_1^{\mathcal{B}}, f_2^{\mathcal{B}}, f_3^{\mathcal{B}}, \dots$$

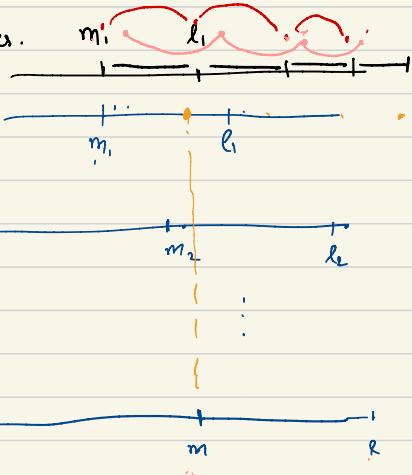
there exist  $m, l$ , s.t.



$$f_m^{\mathcal{B}} = f_{m+l}^{\mathcal{B}}$$

- Moreover:  $f_{m+i}^{\mathcal{B}} = f_{m+l+i}^{\mathcal{B}} \quad \forall i \geq 0$

Consider all DFA with **at most**  $n$  states.



$\mathcal{B}_1$	-	$m_1, l_1$
$\mathcal{B}_2$	-	$m_2, l_2$
$\vdots$	$\vdots$	$\vdots$
$\vdots$	$\vdots$	$\vdots$
$\vdots$	$\vdots$	$\vdots$
$\mathcal{B}_j$	-	$m_j, l_j$

- Let  $m = \max(m_1, \dots, m_j)$

$$l = l_1 \cdot l_2 \cdot l_3 \cdots l_j$$

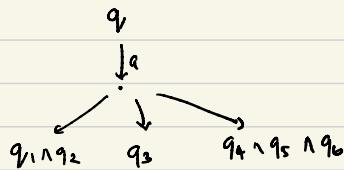
Then for every DFA  $\mathcal{B}$  with  $\leq n$  states, we have:

$$f_{m+i}^{\mathcal{B}} = f_{m+l+i}^{\mathcal{B}} \quad \forall i \geq 0$$

Step 2: Translating Step 1 to alternating finite automata.

AFA:  $(Q, q_0, \delta, F)$

$$\delta: Q \times \Sigma \rightarrow \mathcal{P}^+(Q)$$



Syntax and semantics similar to ATA: with no guards, no events

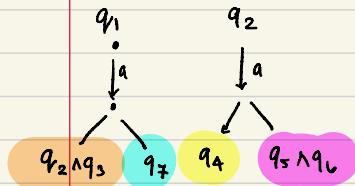
Claim: Every AFA can be converted into an equivalent DFA.

### Modified subset construction:

Each node: a set of subsets of Q

$$\{ \{q_1, q_2\}, \{q_1, q_3, q_4\}, \{q_2, q_5\}, \{q_3\} \}$$

↓ a



?

$$\{q_1, q_2\}$$

↓ a

$$\{q_2, q_3, q_4\}, \{q_2, q_3, q_5, q_6\}, \{q_7, q_4\}, \{q_7, q_5, q_6\}$$

- Perform the above operation on each set from the set of subsets.
- Node is accepting if there is a subset containing only accepting states.

Theorem: Every AFA with 'n' states can be

converted into a DFA with  $\leq 2^n$  states.

Consider unary alphabet  $\{a\}$ .

An AFA  $A$  with state set  $\Delta$  gives a function:

$$f_A^k : 2^{\Delta^Q} \rightarrow 2^{\Delta^Q}$$

$$\begin{array}{ccc} \{ \{ \}, \{ \}, \{ \} \dots \{ \} \} & \xrightarrow{a^k} & \{ \{ \}, \{ \}, \{ \} \dots \{ \} \} \\ \vdots & & \vdots \\ \{ \{ \}, \{ \} \} & \xrightarrow{a^k} & \{ \{ \}, \{ \}, \{ \} \}. \end{array}$$

- Similar to the DFA case, let 'm', 'l' be numbers s.t.

$$f_{m+i}^A = f_{m+l+i}^A \quad \text{if } i \geq 0$$

for all AFA  $A$  with at most  $2n$  states

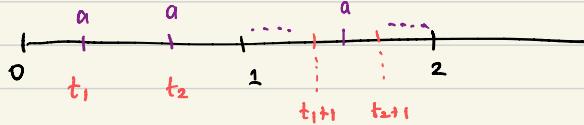
- Starting from some  $\{q_i\}$ ,  $a^{m+i}$  goes to an accepting node iff  $a^{m+l+i}$  goes to an accepting node.

Recall:

$$\mathcal{L} = \{ (a^k, t_1 t_2 \dots t_k) \mid 0 < t_1 < t_2 < 1$$

$$1 < t_3, \dots, t_k < 2$$

there is exactly one  $a$  between  
 $t_1 + 1$  and  $t_2 + 1$  } ?



Step 1: Understand some property of DFA's

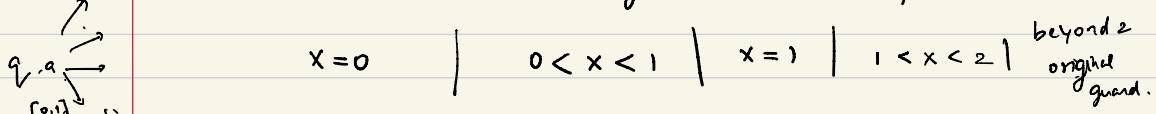
Step 2: How Step 1 translates to untimed alternating finite automata

Step 3: Any 1-ATA accepting  $\mathcal{L}$  behaved like an untimed AFA in the interval  $(1, 2)$ , where clocks are useless.

Step 4: Use Step 1 and 2 in 3 to get a contradiction.

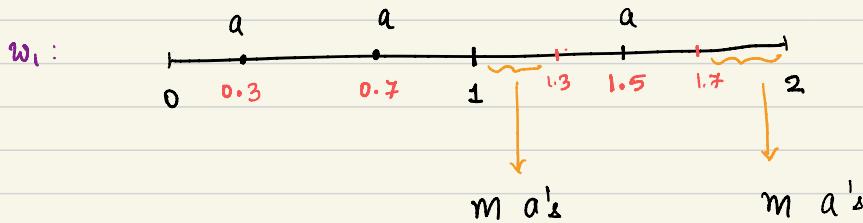
Suppose  $A$  is a 1-ATA with ' $n$ ' states accepting '2'.

- We can assume that every transition is partitioned as:



- For the moment, let us ignore all transitions with  $x=0$ . We will see later why we can do this.

Construct two timed words  $w_1$  and  $w_2$  as follows:



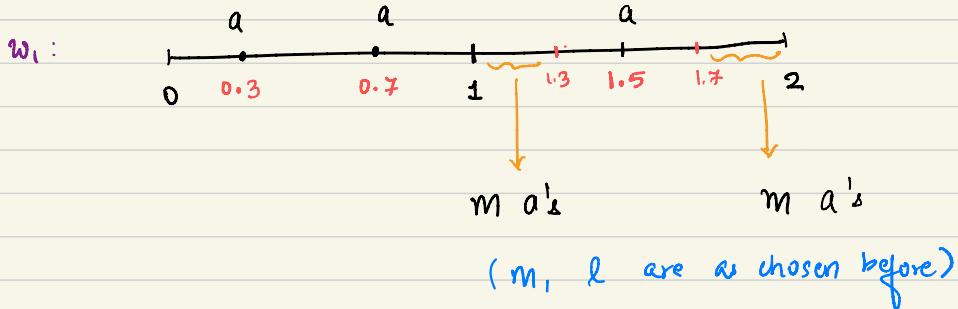
( $m, n$  are as chosen before)

$w_2$ : On top of  $w_1$ , add ' $l$ ' a's in the interval

(1.3, 1.7), but not at 1.5

$$w_1 \in L, \quad w_2 \notin L.$$

We will show that if  $A$  accepts  $w_1$ , it also accepts  $w_2$   
- a contradiction.



Consider the acceptance game for  $\mathcal{L}$  on  $w_1$ .

- Let  $(q, v)$  be a configuration reached at  $t = 1$ .

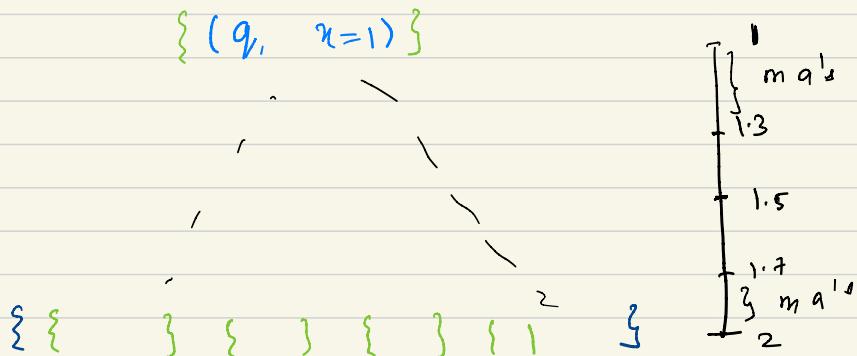
What are the possible values of  $x$  at  $t = 1$ ?

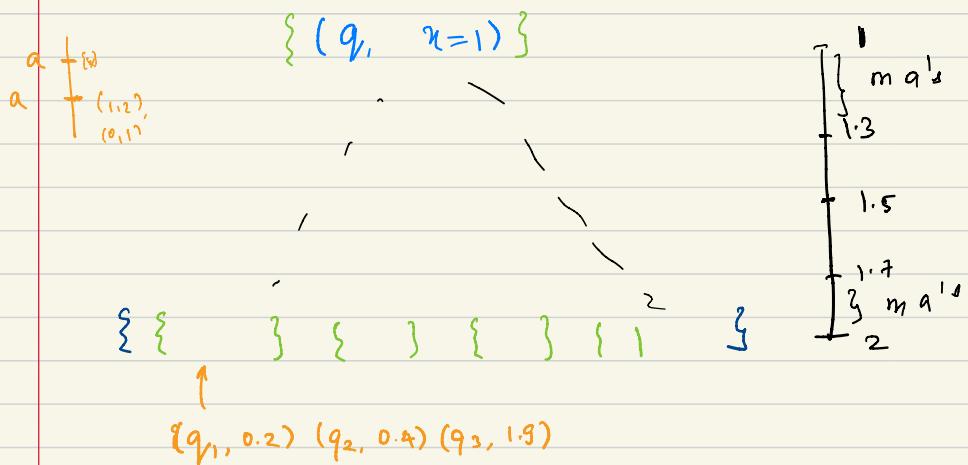
$(q, x=1)$

$(q, x=0.7)$

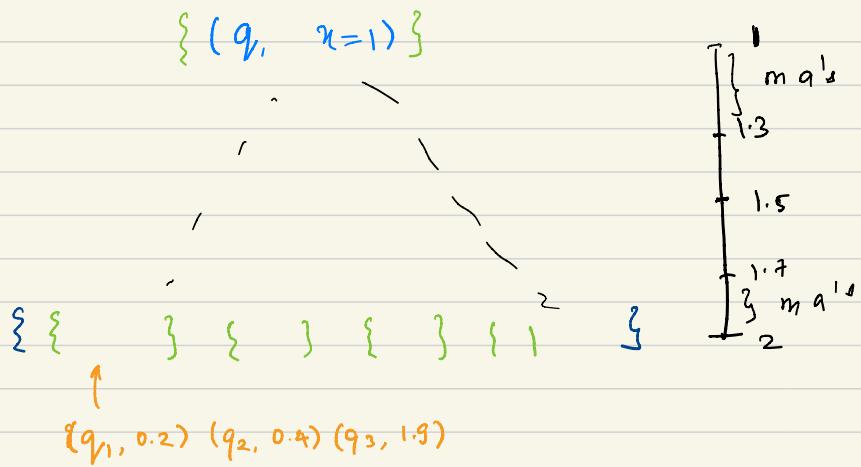
$(q, x=0.3)$

Pick  $(q, x=1)$  and investigate the set of sets of configs. reached from here after reading the entire word.

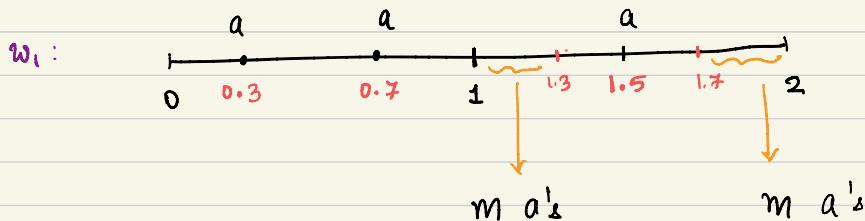




- In  $(1, 2)$  transitions with guard  $x=0$  are never used.
- In fact, only those transitions with either i)  $1 < x < 2$   
or ii)  $0 < x < 1$
- are used.
- i) is taken until ' $x$ ' is reset, (ii) is taken after ' $x$ ' is reset.
- Therefore, if we maintain an extra bit 0/1 in each state to mark whether ' $x$ ' has been reset until now, we can recover the behaviour of it in the interval  $(1, 2)$ .



- Therefore, starting from  $(q_1, x=1)$ , the rest of the accepting run is identical to the run of an (untimed) AFA with  $2n$  states, starting from  $(q_1, 0)$  → to denote not real.
- From our choice of 'm' and 'l', the same set of sets will be reached by this untimed AFA on the word  $w_2$ !
- Hence, from  $(q_1, x=1)$ ,  $w_2$  will also be accepted.



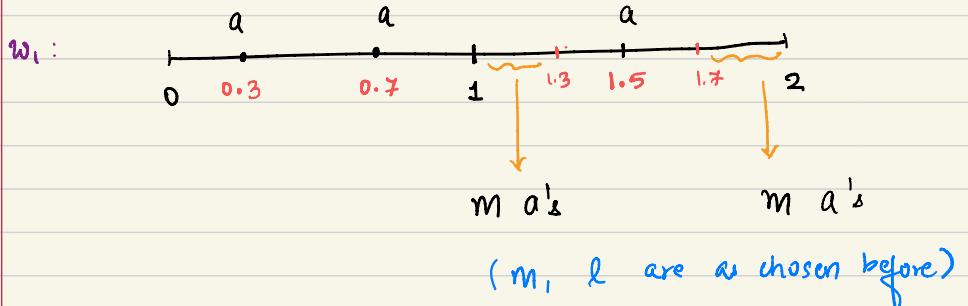
( $m, l$  are  $\alpha$  chosen before)

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Let us now focus on  $(q_j, x=0.7)$  at  $t=1$

- Upto  $t=1.3$  the word is the same in both  $w_1$  and  $w_2$  and hence the same set of configurations will be reached at  $t=1.3$
- Configurations at  $t=1.3$  are either  $(q_j, x=1)$  or  $(q_j, x < 0.3)$
- From  $(q_j, x=1)$ , apply same argument as before.
- From  $(q_j, x < 0.3)$ , only  $(0 < x < 1)$  transitions will be taken, so it behaves like an untimed AFA with ' $n$ ' states.
- By our choice of ' $m$ ' and ' $l$ ' the same set of set of states is reached after reading  $w_1$  and  $w_2$ .

Hence from  $(q_j, x=0.7)$  at  $t=1$ , if  $w_1$  is accepted,  $w_2$  is also accepted.



Finally consider  $(q_f, x=0.3)$  at  $t=1$ .

- upto  $t=1.7$ ,  $A$  will take only  $0 < x < 1$  edges.
- Hence the behaviour is similar to an AFA, and the same set of "states" will be reached for both  $w_1$  and  $w_2$  at  $t=1.7$ .  
The value of  $x$  may be different. However, it will either be  $x=1$  for both words, or some value with  $x < 1$  in both.
- From configurations with  $x < 1$  at  $t=1.7$ , the actual value remains  $0 < x < 1$  for the rest of the word. Hence the true value does not matter.
- This shows that the set of states reached after both  $w_1$  and  $w_2$  are the same!

If  $w_1$  is accepted by  $A$ ,  $w_2$  is also accepted by  $A$ .

- Contradiction

Summary:

Expressive power of 1-ATA vs many clock NTA

