

# TIMED AUTOMATA

## LECTURE 17

## GOALS OF TODAY'S LECTURE

- A partial algorithm for determinizing timed autl.
  - ↳ complete for several subclasses of T.A.

### When are Timed Automata Determinizable?

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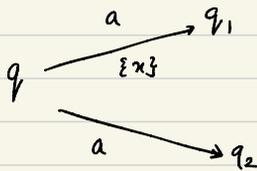
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ICALP '09

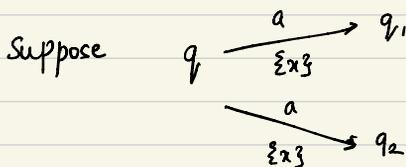
Main idea: How to make a subset-construction work?

Recall the problem with subset construction:



$$\{q\} \xrightarrow{a} \{q_1, q_2\}$$

How to track resets?

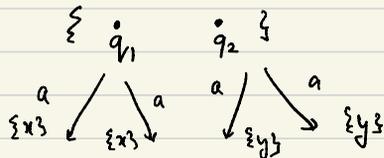


$$\{q\} \xrightarrow[\{x\}]{a} \{q_1, q_2\}$$

No problem

Goal: 1. Convert the given NFA into a language equivalent  $B$  so that the same clock is reset on every transition with the same letter 'a' for every state 'q'.

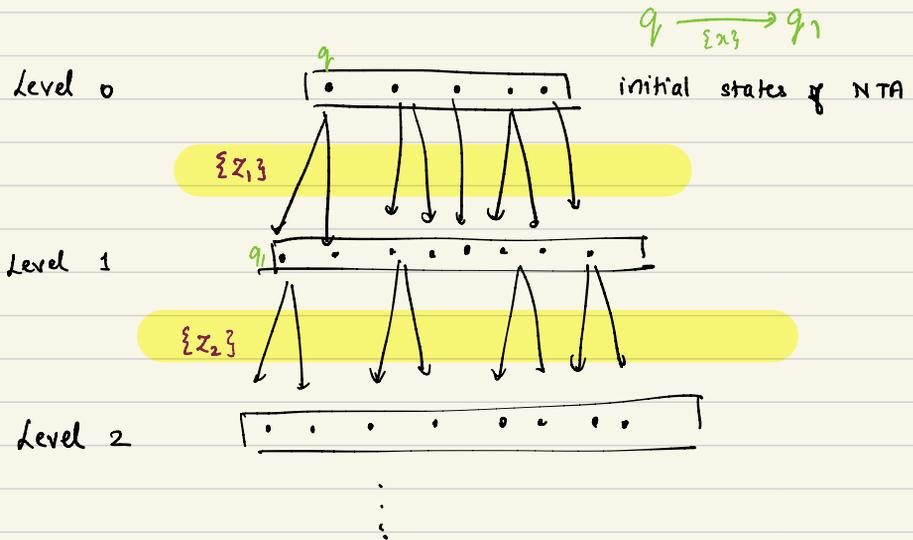
2. Suppose we try to do a subset construction



Even if same clock is reset out of  $q_1$  and out of  $q_2$ , still there is a problem for the "subset".

To tackle these two problems, BBBB'09 gives a construction where

for each level - a new clock is reset



- This is the basic idea. There are two challenges

- the constructed automaton should be language equivalent

- it has to be finite.

Automaton  $\mathcal{A}$ : (Running example)

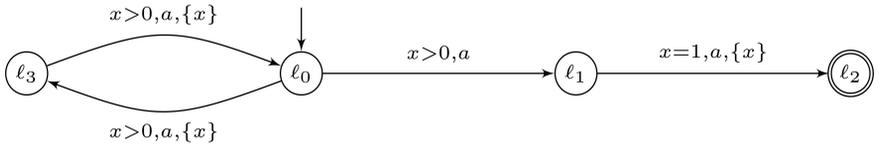
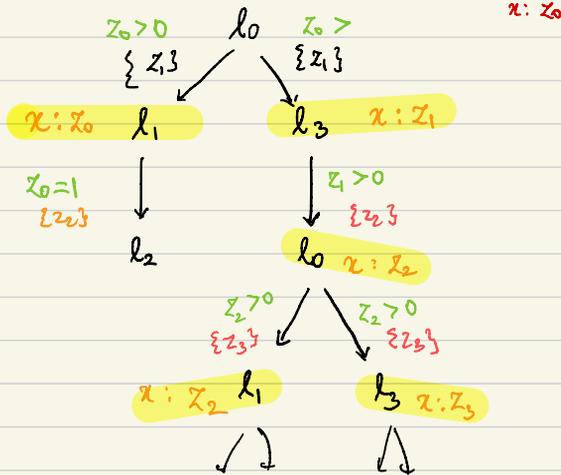


Fig. 1. A timed automaton  $\mathcal{A}$

Step 1:

$\mathcal{A}^\infty$ : Infinite tree given by the unfolding of  $\mathcal{A}$ :  
 + a new clock is reset at each level.



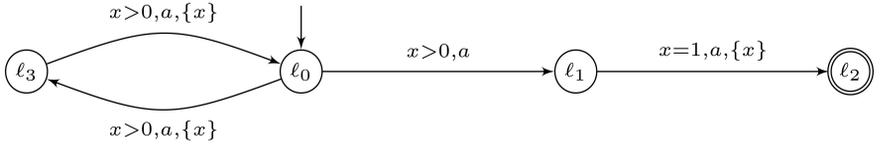
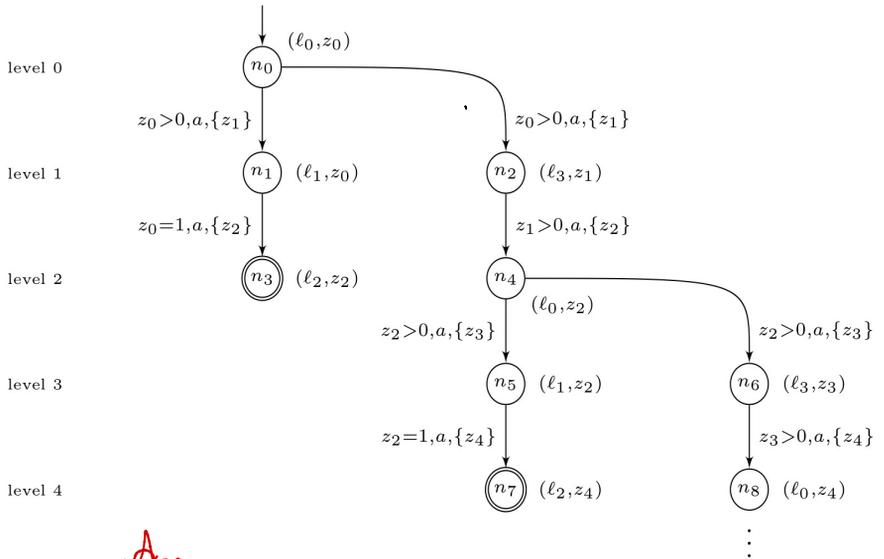


Fig. 1. A timed automaton  $\mathcal{A}$



$\mathcal{A}_\infty$

Advantages of  $\mathcal{A}_\infty$ : In a good form amenable to subset construction  
 Problems with  $\mathcal{A}_\infty$ : Infinitely many states.

Idea: If we know some  $z_i > M$  (max constant), we can reuse it.

$A \rightarrow A^\infty$ :

Advantages: It is in a good form for subset construction

Problem: Infinite.

- We want to be able to "reuse" clocks

$\left. \begin{array}{c} \{z_i\} \\ \vdots \\ \{z_i\} \end{array} \right\}$

Suppose we know that at a state, value of  $z_i > M$

( $M$ : maximal constant occurring in  $A$ )

$x: z_i$  and  $z_i > M$

↓

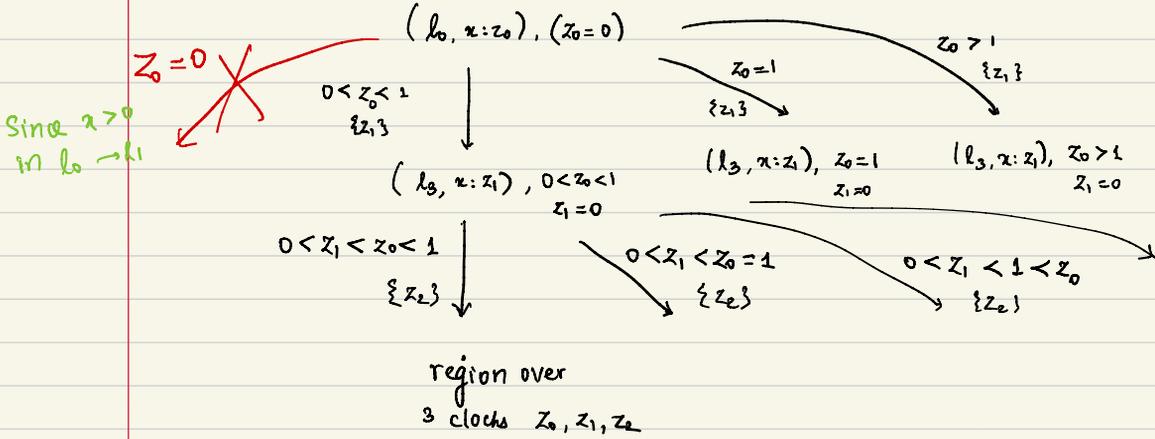
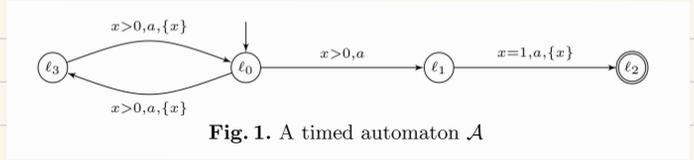
$x: \perp$  and make  $z_i$  free.

To detect  $z_i > M$ , we will make use of a region-style construction.

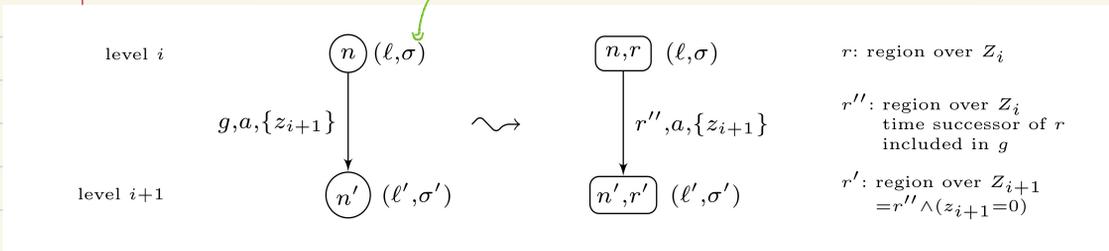
Step 2:

$R(d_{\infty})$ : infinite timed automaton where guards are regions over relevant clocks.

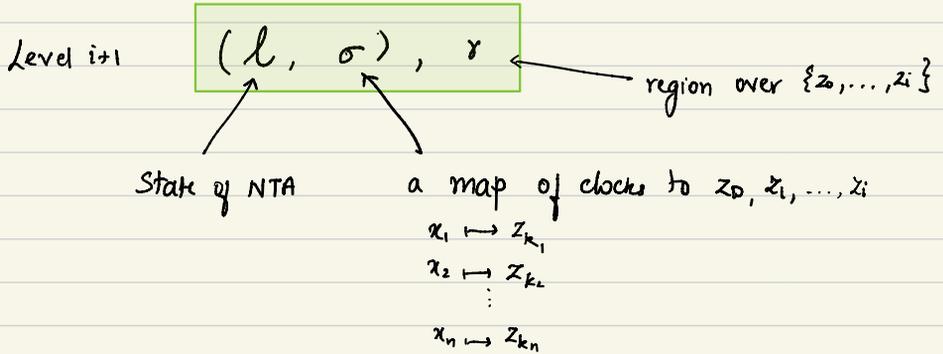
Illustration:



a map from  $X \rightarrow \{z_0, \dots, z_i\}$

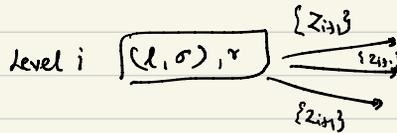


Currently we have an infinite tree whose nodes are:



Advantages:

- 1. Amenable to subset construction since same resets on all outgoing transitions.



- 2. Node contains information about which clocks are above  $M$ .

Problems:

- Still infinite

To make it finite we need a facility to reuse clocks. As a first step, we restrict region to "active clocks".

### Active clocks:

A clock  $z_i$  is active at node  $(l, \sigma)$ ,  $r$  if

-1. Some clock ' $\alpha$ ' is mapped to  $z_i$  AND

-2.  $z_i \leq M$  in the region ' $r$ '.

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- First of all, note that if some clock  $z_j$  is not mapped to any clock in  $\sigma$ , then its value is useless for the future. So it can be removed.

- Secondly, if  $\sigma(\alpha) = z_i$  and  $z_i > M$  in  $r$ , then we can maintain this information in  $\sigma$  itself.

$\sigma(\alpha) = \perp$  (to denote that its value  $> M$ )

Once again, such a  $z_i$  can be removed.

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We can therefore assume that the regions in  $R(\mathbb{A}^\infty)$  are only over active clocks!

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We now perform a subset construction on this region tree.

## Subset construction:

Node:

$$\{ (l_1, \sigma_1), (l_2, \sigma_2) \dots (l_k, \sigma_k) \}, \tau$$

a subset of state-map pairs

a region.  
over active clocks.

Transitions:

Look at all a-transitions from:

$$(l_1, \sigma_1), \tau$$

↓  $\tau'$

$$(l_2, \sigma_2), \tau \dots$$

↓  $\tau'$

$$(l_k, \sigma_k), \tau$$

↓  $\tau'$

- from the same time successor  $\tau'$ .

- Pick a fresh clock  $z$  which is not present in  $\tau$

$$\{ (l_1, \sigma_1), (l_2, \sigma_2) \dots (l_k, \sigma_k) \}, \tau$$

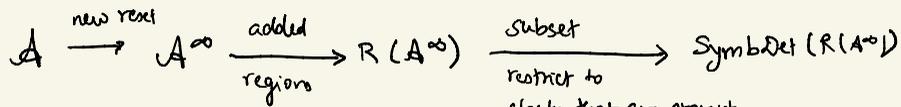
↓  $\tau'$

$$\{ (l_1', \sigma_1'), (l_2', \sigma_2') \dots (l_k', \sigma_k') \}, \tau''$$

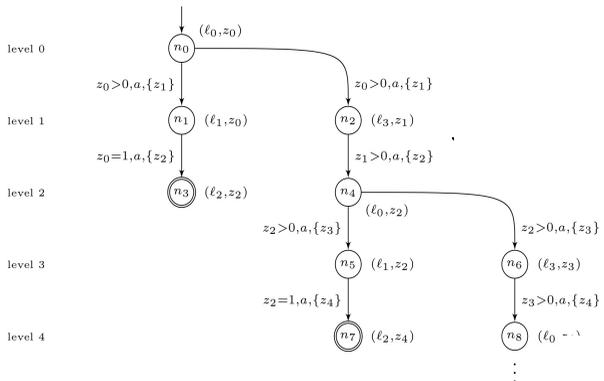
over active clocks.

# Illustration of Step 3:

Symbolic determinization of  $R(A^\infty)$ .

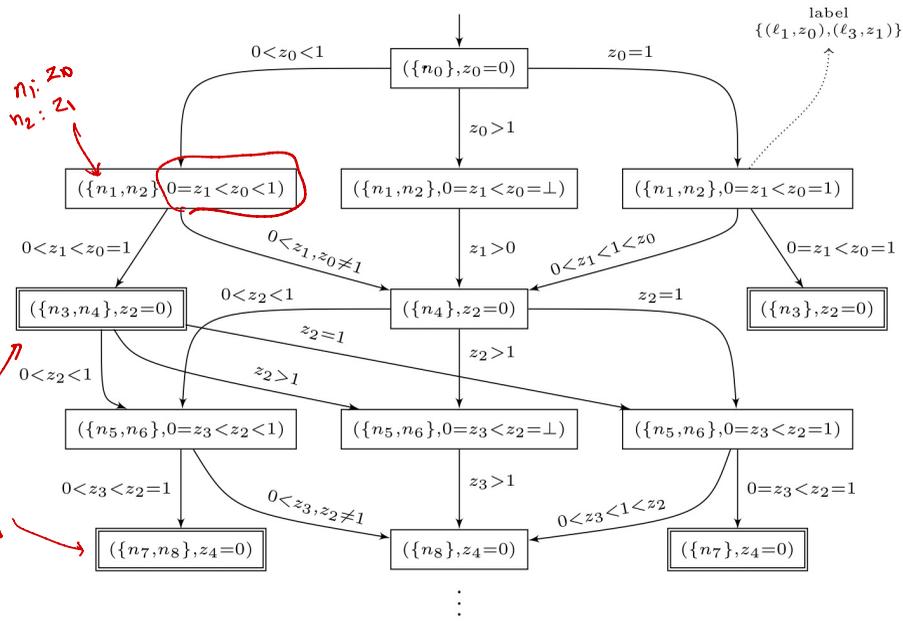


$R(A^\infty) \rightarrow \text{SymbolDet}(R(A^\infty))$



Restricted to clocks which appear in a map of the subset

for eg. only  $z_1$  appears in  $\{n_3, n_4\}$



Final observation: If the number of active clocks is bounded by  $\gamma$ , we can reset clocks and get a finite timed automaton.

$$X = \{x_1, x_2, \dots, x_\gamma\}$$

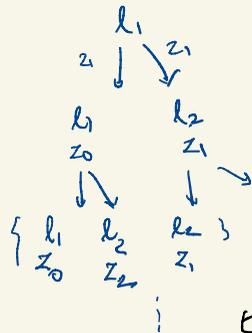
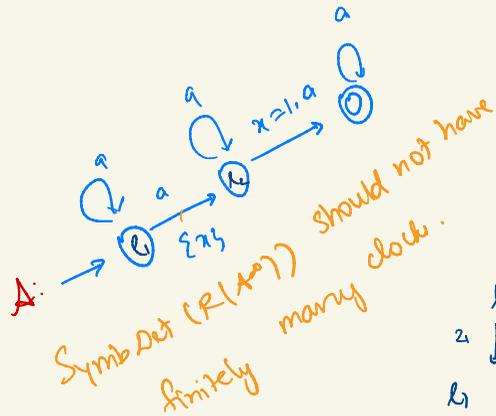
based on some deterministic policy.

One more optimization:

- If the successor is due to a "zero-time" successor of the region, then use the previously reset clock (in the transition that leads to this region).

$$\{ (l_1, \sigma_1) \dots (l_k, \sigma_k) \}, \gamma \quad r \neq z = 0$$

$r'$  [0-time successor, that is:  $r' = r$ ]  
 $\{z\}$



$A \{y_1, y_2, \dots, y_n\}$

$A^\infty \mid (l, y_1: z_1, y_2: z_2, \dots)$

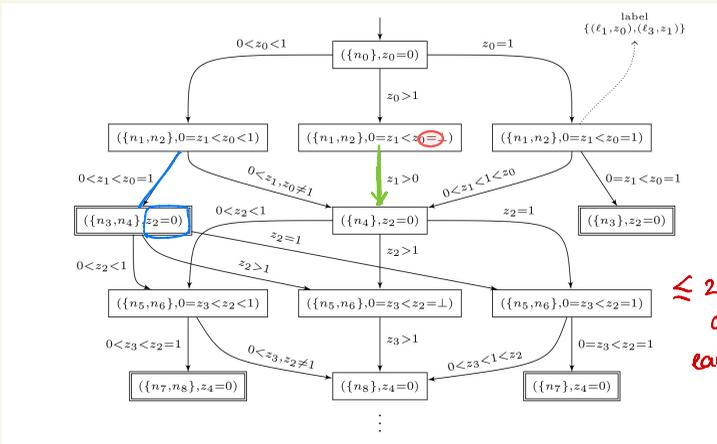
$R(A^\infty)$  | Every node is augmented with a region involving all clocks above its level.

**Symb Det ( $R(A^\infty)$ ):**

subset construction restricted to clocks that appear in the maps.

If the number of clocks present in each region is finite then there is a way in which we can extract an equivalent D.T.A. using a fresh set of clocks  $\{x_1, \dots, x_r\}$

using a fresh set of clocks  $\{x_1, \dots, x_r\}$



$\leq 2$  active clocks in each node.

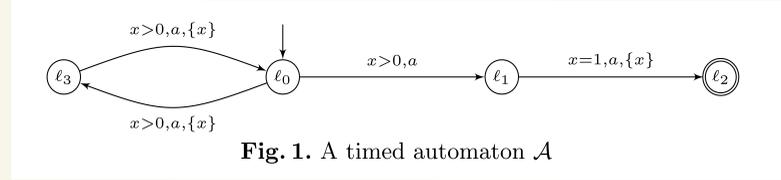
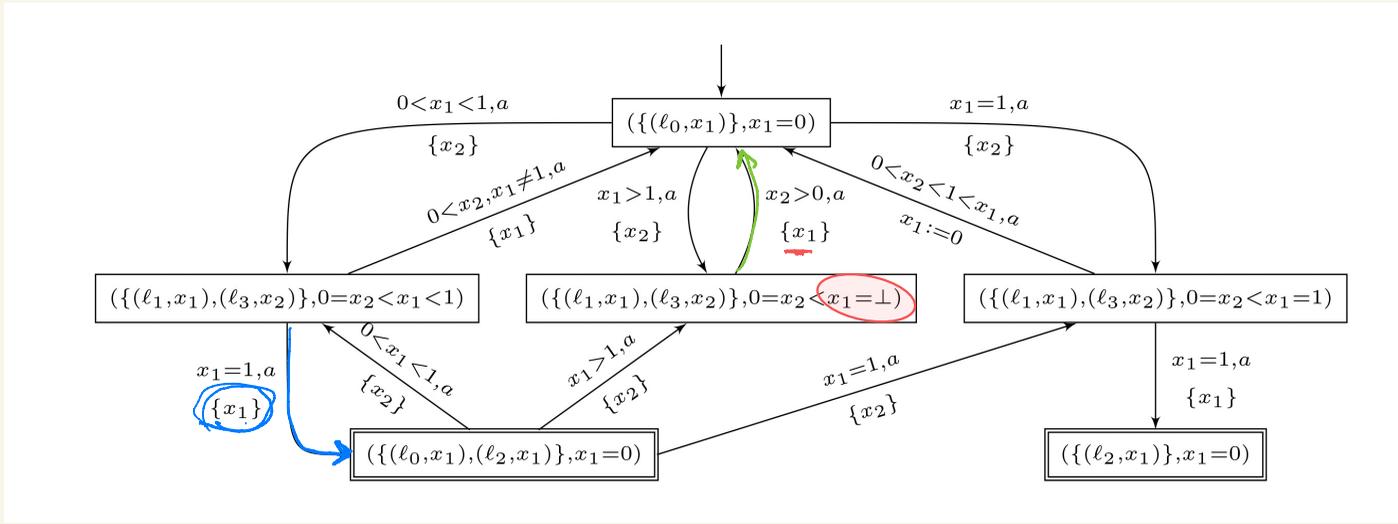


Fig. 1. A timed automaton  $\mathcal{A}$

$\{x_1, x_2\}$

with active clock reduction + reuse of clocks.

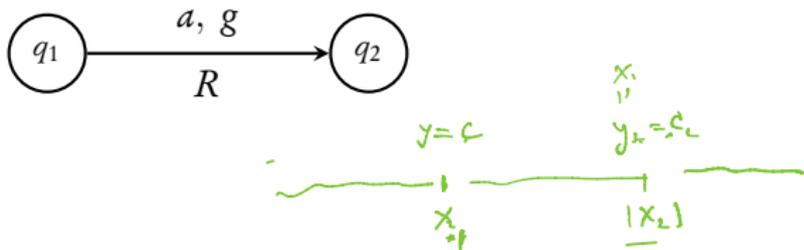


Question:

- What are some sufficient conditions for an automaton to have only finitely many active clocks? that is, finite  $\mathcal{R}$ .

Coming next: Two subclasses of NFA for which this construction works.

# Integer reset timed automata



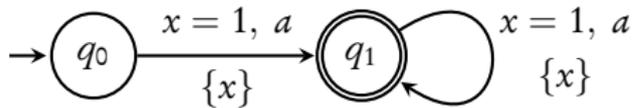
## Conditions:

- ▶  $g$  has **integer** constants
- ▶  $R$  is **non-empty**  $\Rightarrow$   $g$  has some constraint  $x = c$

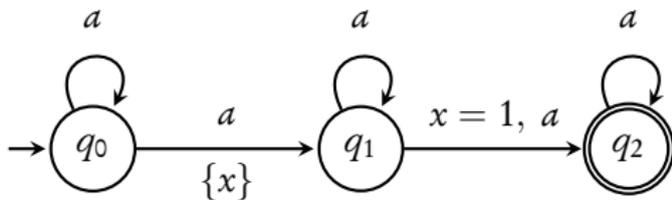
## Implication:

- ▶ Along a timed word, a **reset** of an IRTA happens only at **integer timestamps**

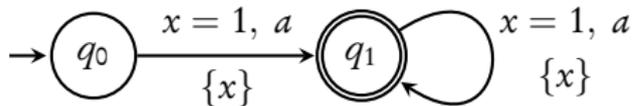
Timed automata with integer resets: Language inclusion and expressiveness



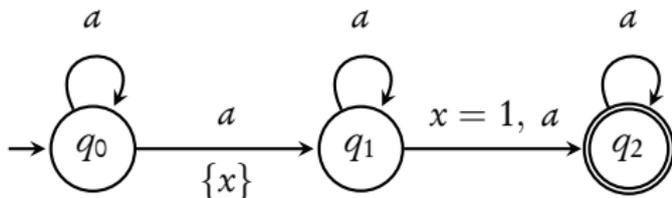
an IRTA



not an IRTA

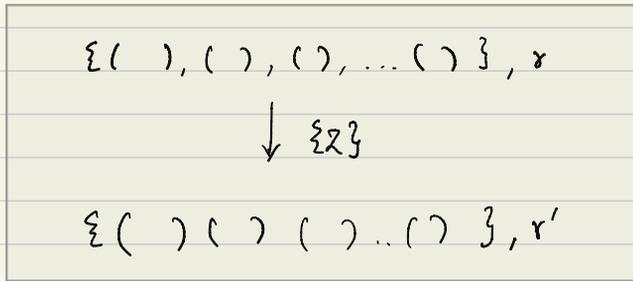


an IRTA



not an IRTA

Next: determinizing IRTA using the subset construction



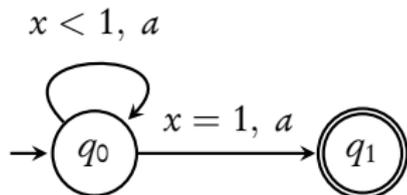
- Suppose a new clock  $z$  becomes active in the successor
  - This means <sup>some clock</sup> was reset in the previous transition
  - Moreover, due to the "zero-time" optimization, the new clock is added only for a non-zero delay.
  - Therefore  $> 0$  time should have elapsed from  $r'$ .
  - Due to property of IRTA, this non-zero delay should be an integer  $\geq 1$ .
  - If there are  $M+1$  active clocks in a node, then starting from the oldest active clock  $'y'$  in this node, at least  $M$  time has elapsed.
- Therefore  $'y'$  will become inactive ( $> M$ ) and can be reused.

$M+1$  active clocks are sufficient for IRTA.

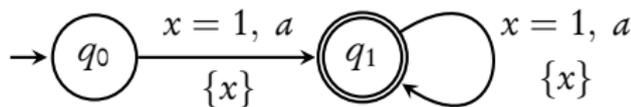
# Strongly non-Zeno automata

A TA is **strongly non-Zeno** if there is  $K \in \mathbb{N}$ :

every sequence of greater than  $K$  transitions **elapses** at least 1 time unit



not SNZ



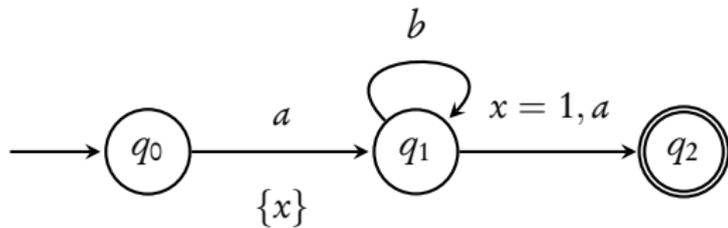
SNZ

$K=2$

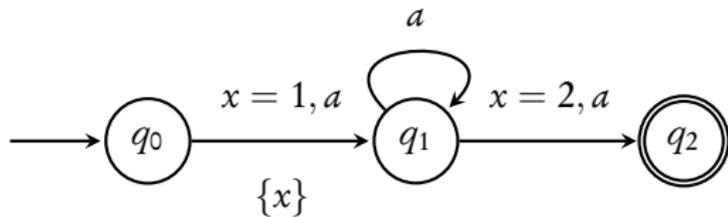


- In a SNZ automaton, every sequence of  $k(M+1)$  transitions elapses  $> M$  time units.

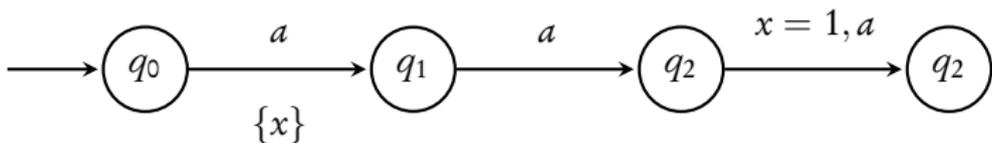
- Therefore when there  $k(M+1)$  active clocks in a node, the oldest entrant will become inactive and can be reused again.



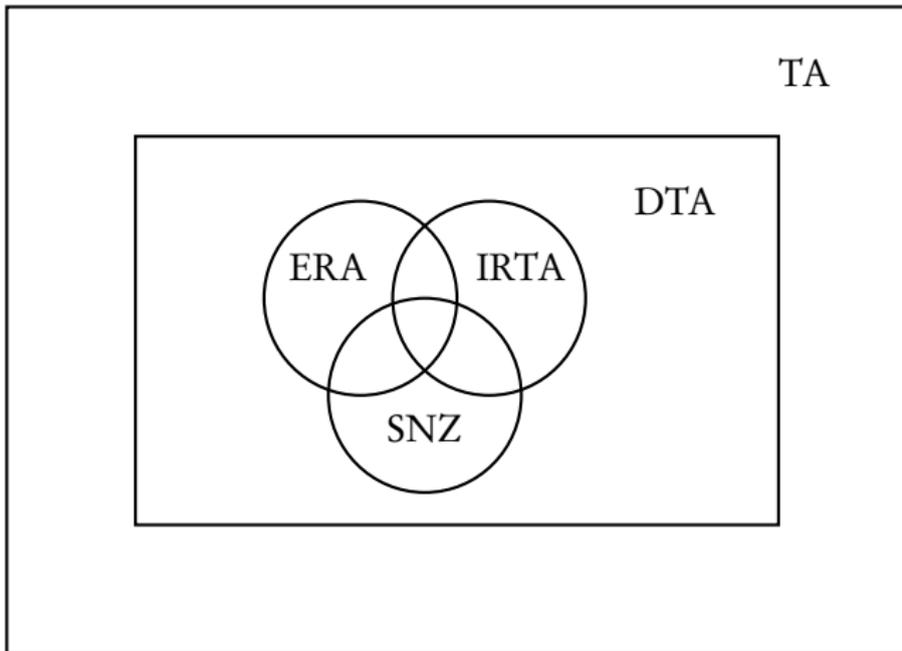
ERA ~~IRTA~~ ~~SNZ~~



~~ERA~~ IRTA ~~SNZ~~



~~ERA~~ ~~IRTA~~ SNZ



## Summary:

- A method to determinize NFA, that works for certain classes.
  - New subclasses seen: IRFA  
SNZ.
  - For a generic  $A$ , this algorithm may not work.
- 

Can we decide, given  $A$ , whether the no. of active clocks in  $\text{SymbSet}(R(A^*))$  is finite!

→ Given  $A$ , can we decide whether this algorithm will work for this automaton or not?

Given NFA  $A$ . does there exist a language equiv. DFA.

↳ Undecidable.