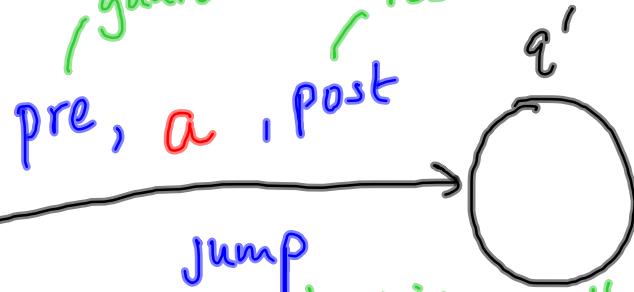


Hybrid Automata

Clocks \rightarrow arbitrary real valued variables

it depends on state

/ guard / reset - can be nondet



$\text{init}(q)$ - initial values
of each var

which variables
are reset $\subseteq 2^X$

$\text{inv}(q)$ - if q is initial state

- invariant conditions for values

$\text{flow}(q)$ - specifies \dot{x} for each x

What's decidable about hybrid automata?

Henzinger et al, JCSS

$$X = \{x_1, x_2, \dots, x_n\}$$

$\underbrace{}$

$$\subseteq \mathbb{R}^n$$

$$V: X \rightarrow \mathbb{R}$$

Rectangular hybrid automata

$R \subseteq \mathbb{R}^n$ is a rectangle if it is a product of intervals

Rectangular hybrid automaton

flow(α)
inv(q)
init(q)

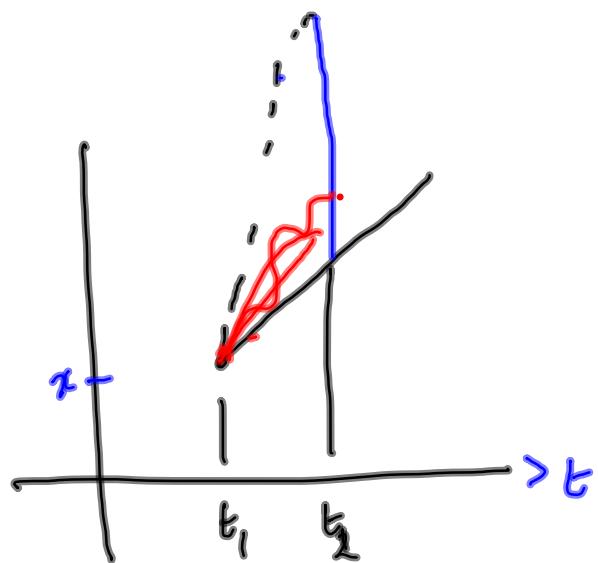
pre(t)
post(t)

} a conjunction of
one rectangular
constraint per variable

$$\text{post}(t) : \quad x = 3y + 7_2 + 8y^3 \quad x \\ x \in [l, u)$$

$$\text{flow}(x) \in [1, 5]$$

effectively assume
evolution is
piecewise linear



Initialized automaton

$q \xrightarrow{a} q'$ if $\text{flow}(q)(z) \neq \text{flow}(q')(z)$
 then $z \in \text{jump}(t)$

Whenever z changes, z is reset

Initialized + rectangular \Leftrightarrow positive

Deterministic reset

Reachability is decidable for initialized rectangular automata

reachability of (q, Z)

where Z is a "zone" or rectangle

Initialized Rectangular



Initialized Singular

- each i is a constant
deterministic resets.



Initialized Stopwatch

$i \in \{0,1\}$

deterministic resets



Timed Automata

with constant resets

Initialized Stopwatch $(q, x_1, x_2, \dots, x_n, \text{some } \dot{x}_i = 0)$

↓
Timed Automata

If $\dot{x}_i = 0$, x_i is post(t)
for last transition when
 x_i went from 1 to 0

$(q, x_1, \dots, x_n, f: x \rightarrow \mathbb{R} \cup \{\perp\})$

If $\dot{x}_i = 1$, same value as stopwatch and

If $\dot{x}_i = 0$, $f(x) = \perp$

$f(x) = \text{stopped value of } x$

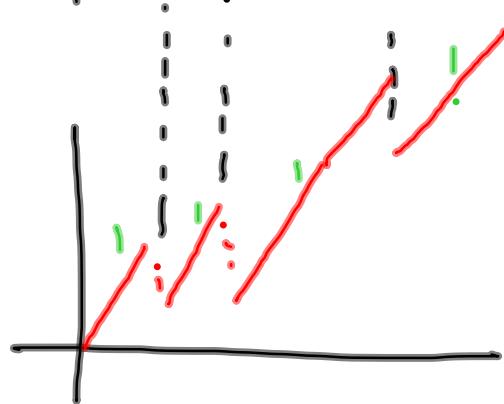
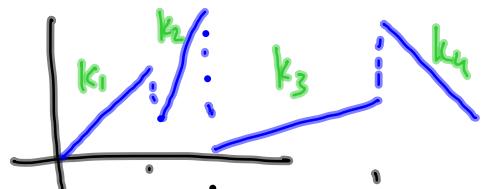
Initialized singular



Initialized stopwatch

Simple scaling
relates configurations

$\forall q \exists k \ x \in [k, k] \text{ in } q.$
det. jumps



Initialized Rectangle

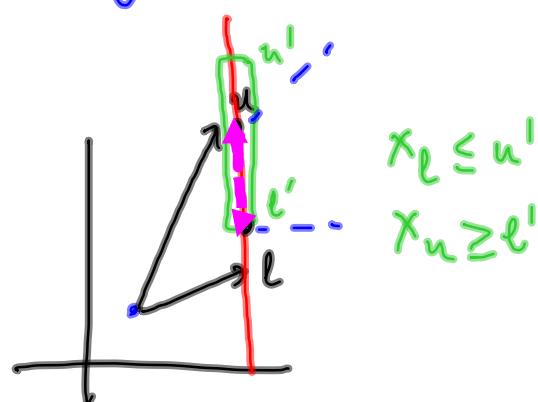


Initialized Singular

"Compact" rectangles
"bounded & closed"

$$x \in [l, u]$$

$$\begin{array}{c} x_l = l \\ x_u = u \end{array}$$



Relaxing conditions yields undecidability

Timed automata are decidable - one-slope variables
"initialized" by default , det-jumps

2 slope variables $\dot{x} = k_1$ or $\dot{x} = k_2$

but not initialized

"Simple" rectangular automaton

- exactly one x is not a clock
- initially all variables are 0, all resets to 0
- all rectangles are compact (bounded & closed)

Thm: Reachability is undecidable for simple automata with one two-slope variable.

$\exists y$ s.t. $y \in \{k_1, k_2\}$ in each state

2 counter machines

Instructions

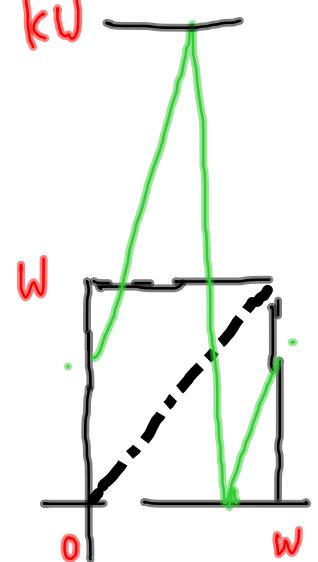
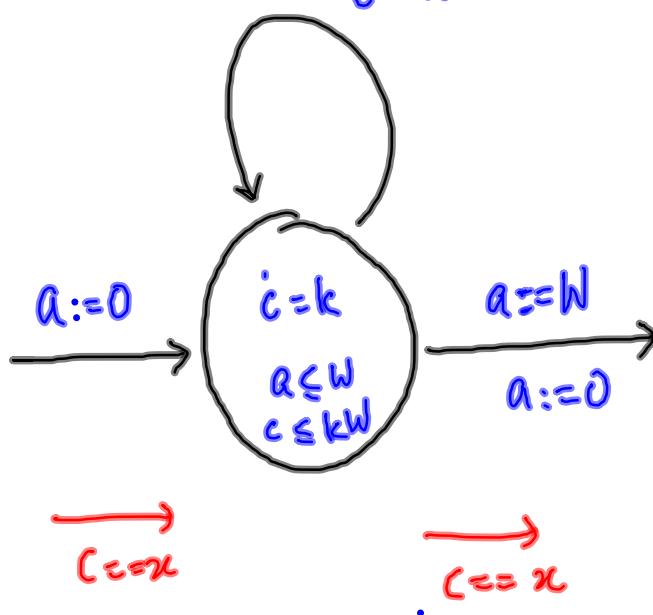
1:

2: $\leftarrow c_1++, c_1--$ · c_2++, c_2-- ⋮ if $c_i == 0$ goto ℓ n: \leftarrow halt

"Wrapping" lemma

W units of time kU

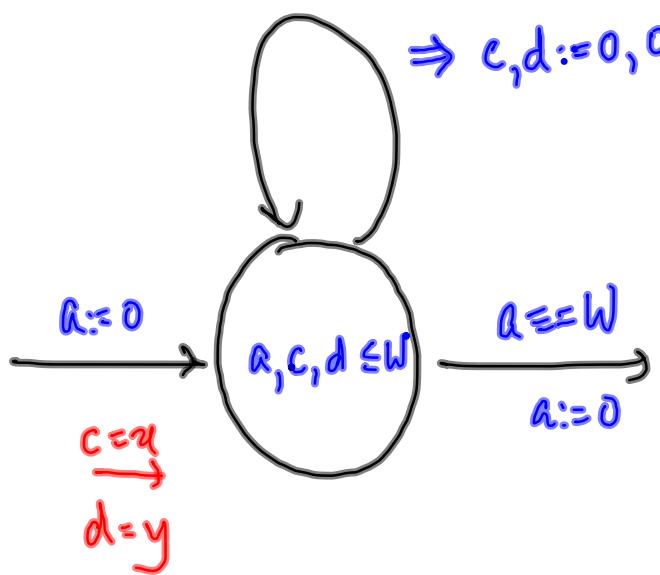
$$c = Wk \rightarrow c := 0$$



Testing $c=d$

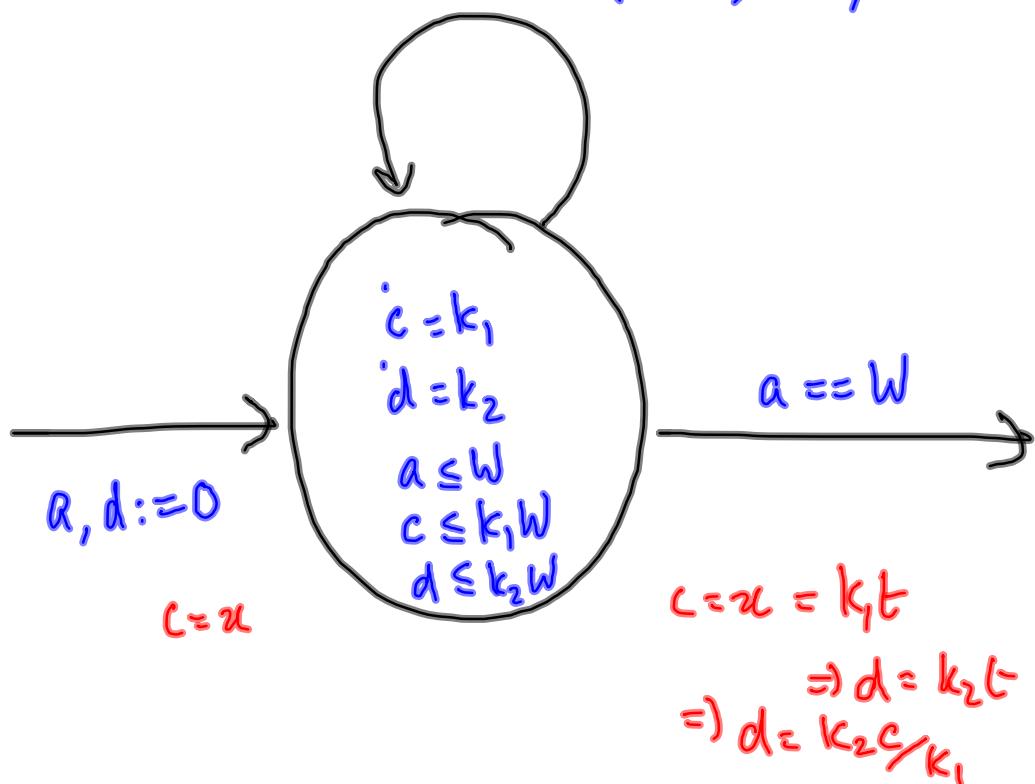
$$c=w \wedge d=w$$

$$\Rightarrow c, d := 0, 0$$



Two non clocks

$$c = k_1 W \Rightarrow c, d := 0$$



Simulation of 2 counter machines

Counters C — clock c
 D — clock d

3 more clocks a, b, b'

$k_1 > k_2 > 0$
 One non-clock z — slopes. k_1, k_2

Value of $C = u$ is encoded $c = k_1 \cdot \left(\frac{k_2}{k_1}\right)^u$

$D = v$ is encoded $d = k_1 \cdot \left(\frac{k_2}{k_1}\right)^v$

$$C = u \quad c = k_1 \left(\frac{k_2}{k_1} \right)^u$$

$$\text{if } C == 0$$

$$C != 0$$

$$C = 0 \quad C \geq 1$$

$$c \in [k_1, k_1]$$

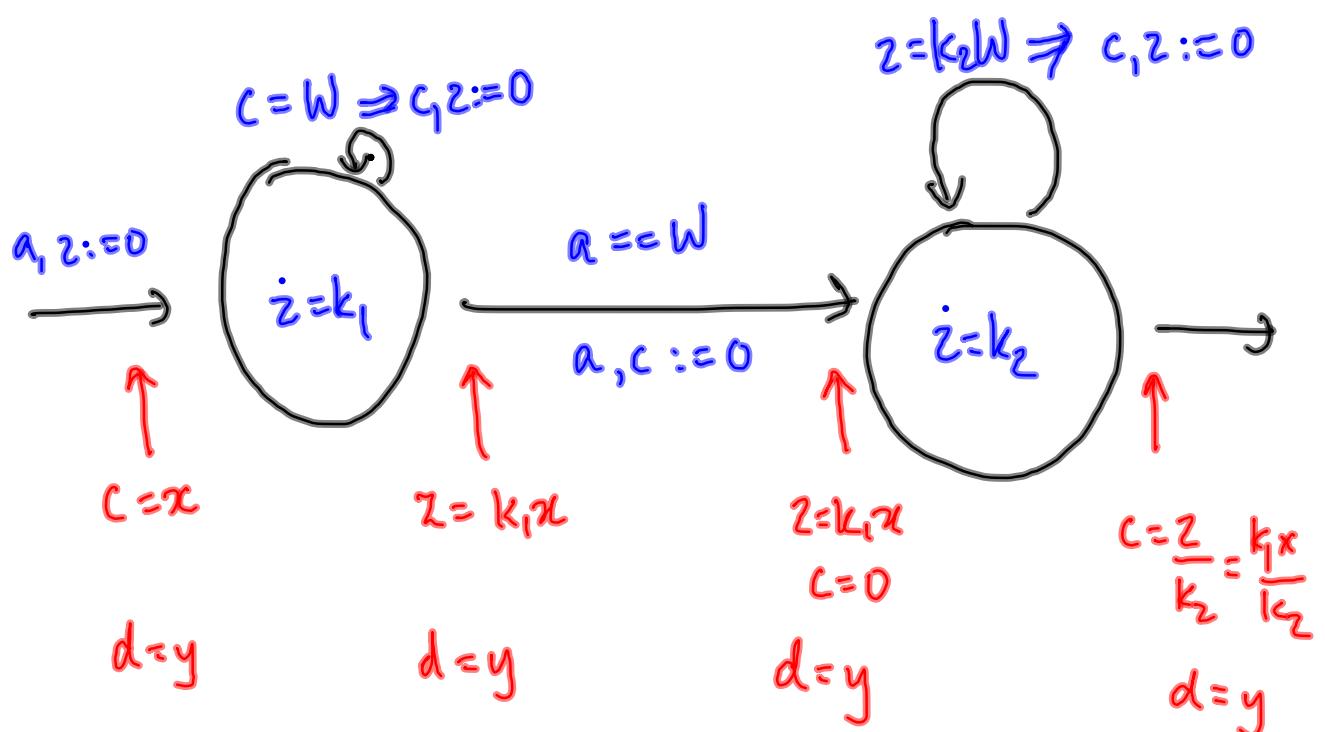
$$c \in [0, k_2]$$

$$c = k_1 \quad c \in (0, k_2] \quad \begin{array}{c} c \neq 0 \\ \xrightarrow{\hspace{1cm}} \\ 0 \quad k_2 \quad k_1 \end{array}$$

decrement/increment \rightsquigarrow multiply by $\frac{k_2}{k_1}$

divide by $\frac{k_2}{k_1}$ = multiply by $\frac{k_1}{k_2}$

Multiply by k_1/k_2



$k_1 > 0 > k_2$

$k_1 > 0, k_2 = 0$

⋮

Different encodings of C, D

Non exactness does not help

Alternative "practical" approach

Discretize time

Inaccuracy of time of measurement

Delay in effecting changes

"lazy" hybrid automata

Agrawal & Thiagarajan

