

Determinization and Expressiveness of Integer Reset Timed Automata with Silent Transitions

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Tata Institute Of Fundamental Research, Mumbai

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Workshop on Automata, Concurrency and Timed Systems

Introduction

Language Inclusion

ϵ -Removal

Expressiveness

Outline

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Timed Automata

- **Timed Automata (TA):** Finite State Automata + Clocks running at the rate of global time
- Extensions of timed automata with **Periodic Guards** [CG00] and **Silent Transitions** [BPGD98] have been introduced to model periodic behaviours
- Expressiveness: $TA < \text{Per-TA} < \epsilon\text{-TA}$
- TA are closed under union and intersection but not closed under determinization and complementation [AD94]
- Though reachability is decidable for all the above classes, language inclusion is undecidable [AD94]

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Integer Resets

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 - Fractional values of all the clocks remain equal
 - Also equal to the fractional value of the global time
- Extensions of IRTA with periodic constraints and silent transitions can be defined. However clock resets happen at integral time points: In case of Per-IRTA, resetting edge consists an atomic constraint of the form $\exists k \in \mathbb{N} : x \in [a + kp, a + kp]$
- Notion of *global but sparse time* as used in Time Triggered Architecture and Distributed Business Processes can be naturally modeled as ϵ -IRTA [SPKM08]

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Q & A

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- **Expressiveness**: $\text{IRTA} < \text{Per-IRTA} < \epsilon\text{-IRTA}$
- Can we get **ϵ -free and deterministic representations** for ϵ -IRTA? We introduce a new variant of timed automata called *Generalized Reset Timed Automata (GRTA)*. Every ϵ -IRTA can be effectively translated into a **1-Clock Deterministic Per-GRTA**
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$\delta\checkmark$ -Representation of Timed Words

- Each integer time stamp i (except 0) is marked by a \checkmark . All the events occurring at time point i appear immediately after this \checkmark
- Entering an interval $(i, i + 1)$ is marked by a δ . All the events occurring in the time interval $(i, i + 1)$ appear immediately after this δ
- **The mapping f :** example timed words and their mappings:
 $\rho_1 = \langle (a, 1.2), (b, 3.5), (c, 4), (d, 4.5), (e, 4.5), (f, 5.6), (g, 5.8) \rangle$
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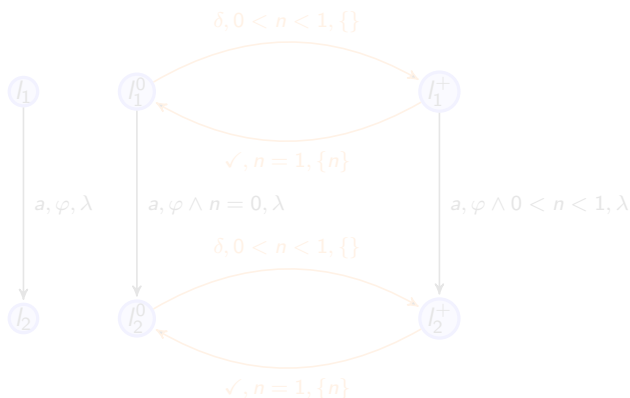
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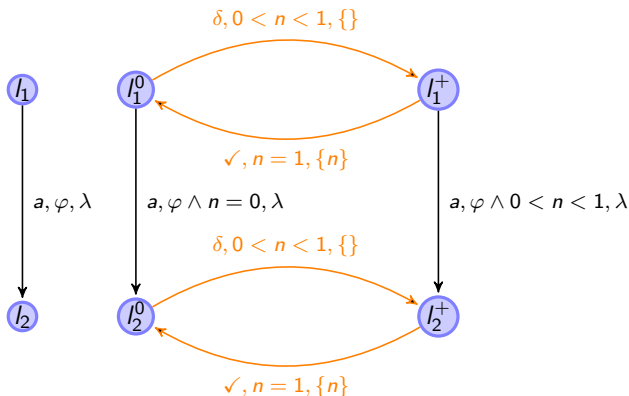
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- Apply following.



- Then apply region construction

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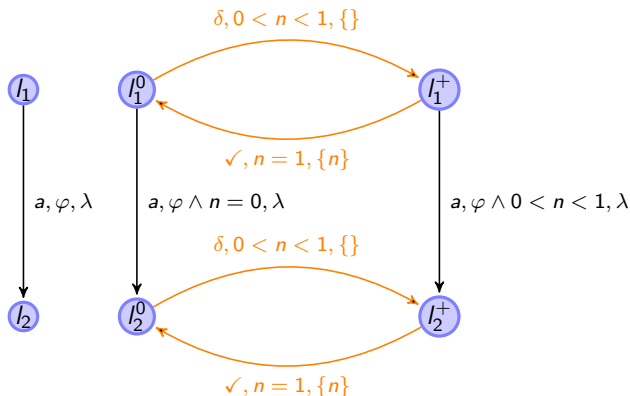
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Key Properties of f

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Language inclusion problem $L(\mathcal{A}) \subseteq L(\mathcal{B})?$, where \mathcal{A} is an ϵ -TA and \mathcal{B} is an ϵ -IRTA, can be decided (EXPSPACE) in the $\delta\checkmark$ -world [SPKM08]

- If \mathcal{A} is an ϵ -IRTA and $f(\rho) = f(\rho')$ then $\rho \in L(\mathcal{A})$ iff $\rho' \in L(\mathcal{A})$ [SPKM08]
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- In a $\delta\checkmark$ -word, when labels from Σ are ignored...
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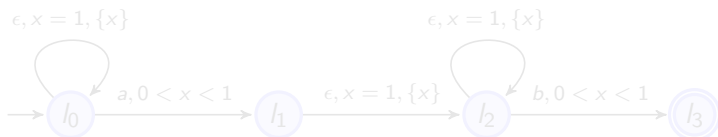
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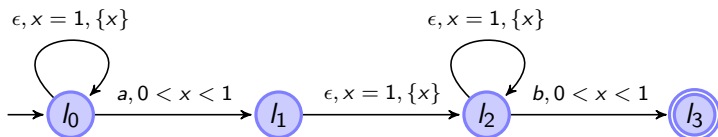


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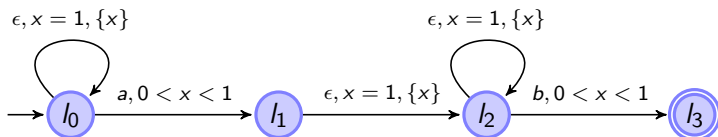


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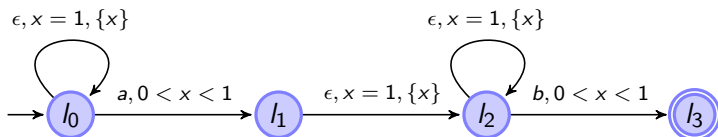


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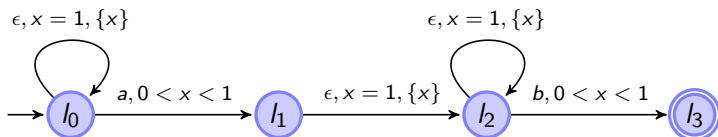


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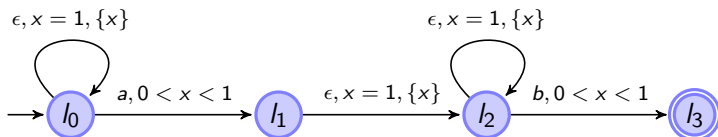


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- **Per-GRTA:** GRTA wherein the atomic constraints can be periodic. We will translate $\delta\checkmark$ -automata to Per-GRTA.
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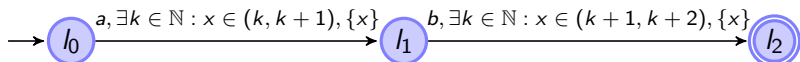
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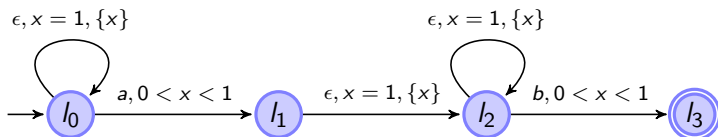
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Construction by Example

- ϵ -IRTA



- $\delta\checkmark$ -Automaton (equivalent to the one obtained by our construction)

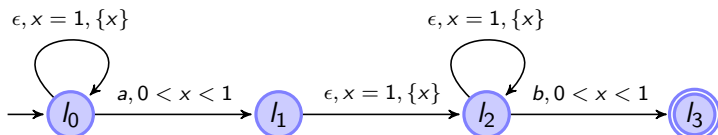


- The idea is to encode the time distances represented by $\delta\checkmark$ -paths into clock constraints. Deterministic, 1-Clock Per-GRTA

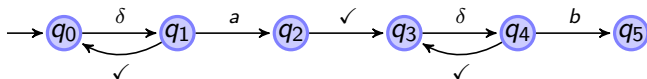


Construction by Example

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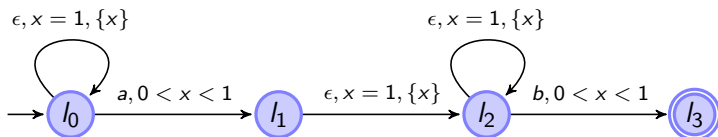


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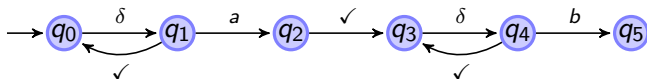


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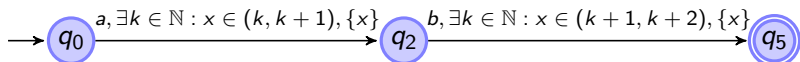
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For every ϵ -IRTA \mathcal{A} we can effectively construct a language equivalent 1-clock, deterministic Per-GRTA \mathcal{B} with at most double exponential blowup in number of locations

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 - Replace each \checkmark -transition by an ϵ -transition with constraint $x = 1$; Reset x
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Outline

Introduction

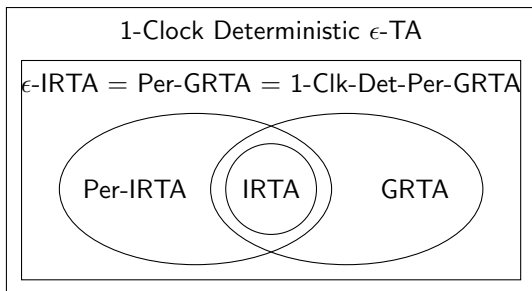
Language Inclusion

ϵ -Removal

Expressiveness

Relative Expressive Power

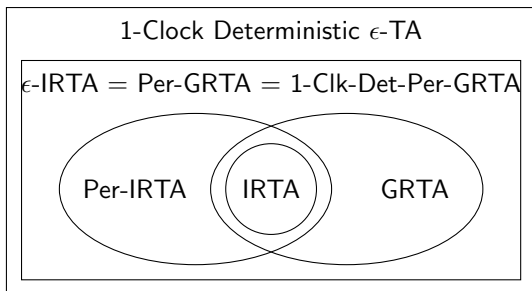
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One Clock Timed Automata

- 1C-TA are well behaved
 - $L(\mathcal{A}) \subseteq L(\mathcal{B})$ is decidable if \mathcal{B} is 1C-TA [OW04]
 - Reachability is NLOGSPACE-complete for 1C-TA [AOQW07]
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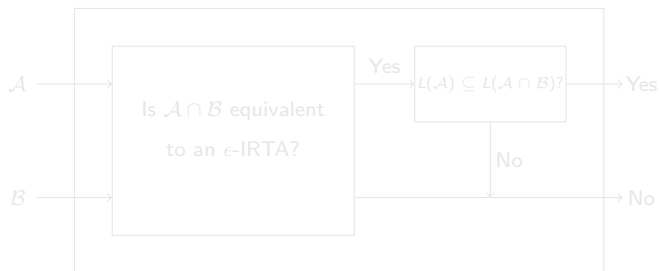
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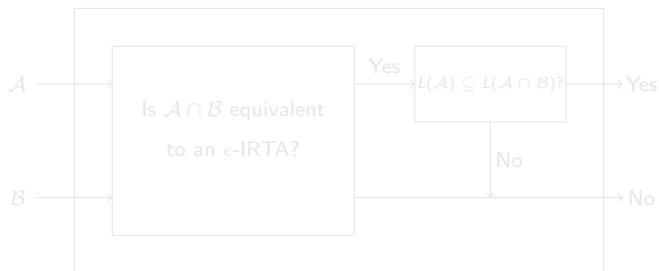
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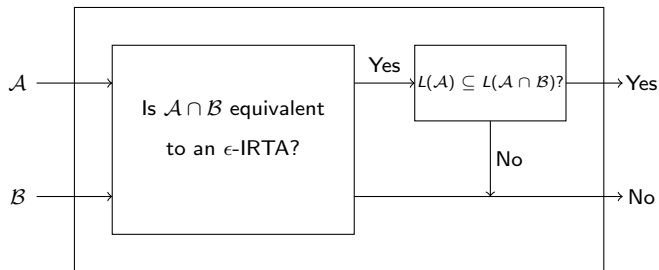
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Thank You



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