Relaxing conditions yields undecidedility

Timed automata are decidable - one-slope variables "initialized' by default, det jumps

2 slope variables it = k, a i=kz but not initialized "Simple" rectangular automaton

- a exactly one of is not a clock
- · initially all variable are 0, all reset to 0
- · all rectagles are compact (bounded & closed)

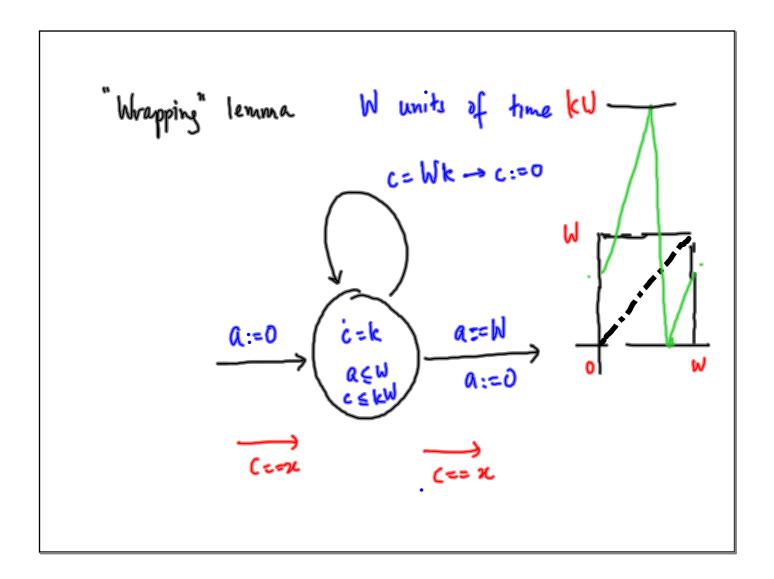
Thm: Reachebility is undendeble for simple antimata with one two-slope varieble.

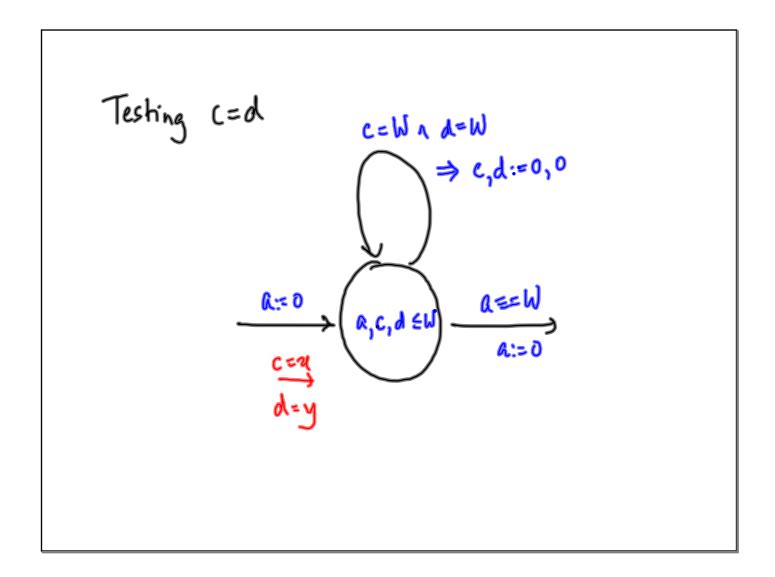
Jy s.t. ÿ∈ {tk1, k2} in each state

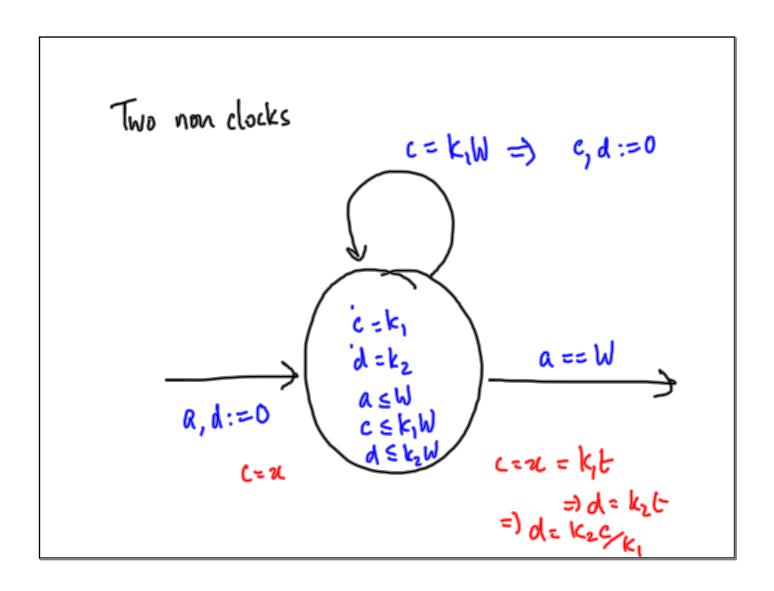
2 counter machines

1:

2:
$$\leftarrow$$
 c_1++ , $c_1 c_2++$, $c_2 c_3++$, $c_4 c_4+$, $c_4 c_5+$
 c_6+
 $c_$







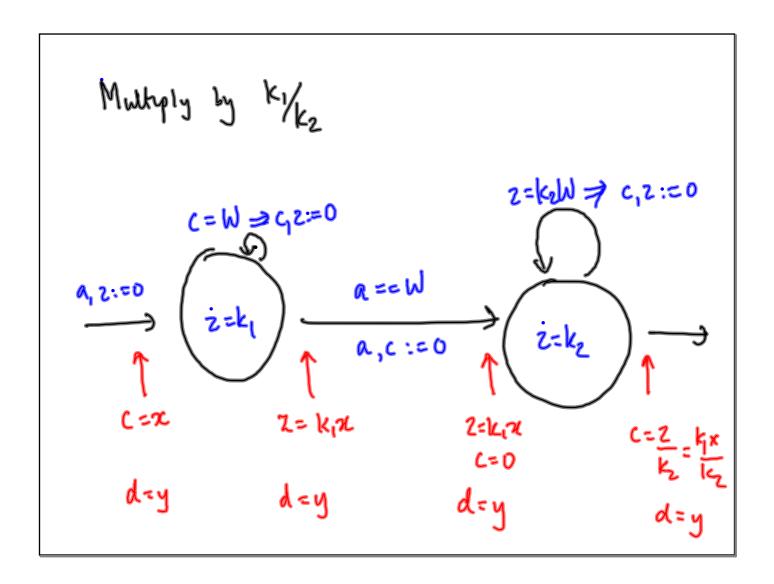
C=u c=
$$K_1 \left(\frac{k_2}{k_1}\right)^{k_1}$$
 C=0 c= k_1 C=1 c $\in (0, k_2]$

If C==0 c $\in [k_1, k_1]$ c=0

C!=0 cc $(0, k_2]$

decrement / increment multiply by $\frac{k_2}{k_1}$

divide by $\frac{k_2}{k_1} = \text{nulliply by } \frac{k_1}{k_2}$



K,707kz

K,707kz

K,707kz

i

Different encodings of C1D