



INFORMEL

Indo-French Formal Methods Lab



UPPSALA
UNIVERSITET

COMMUNICATING RECURSIVE PROGRAMS: CONTROL AND SPLIT-WIDTH

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Uppsala University, Sweden

Joint work with

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LSV, ENS Cachan, France

K. Narayan Kumar

Chennai Mathematical Institute, India



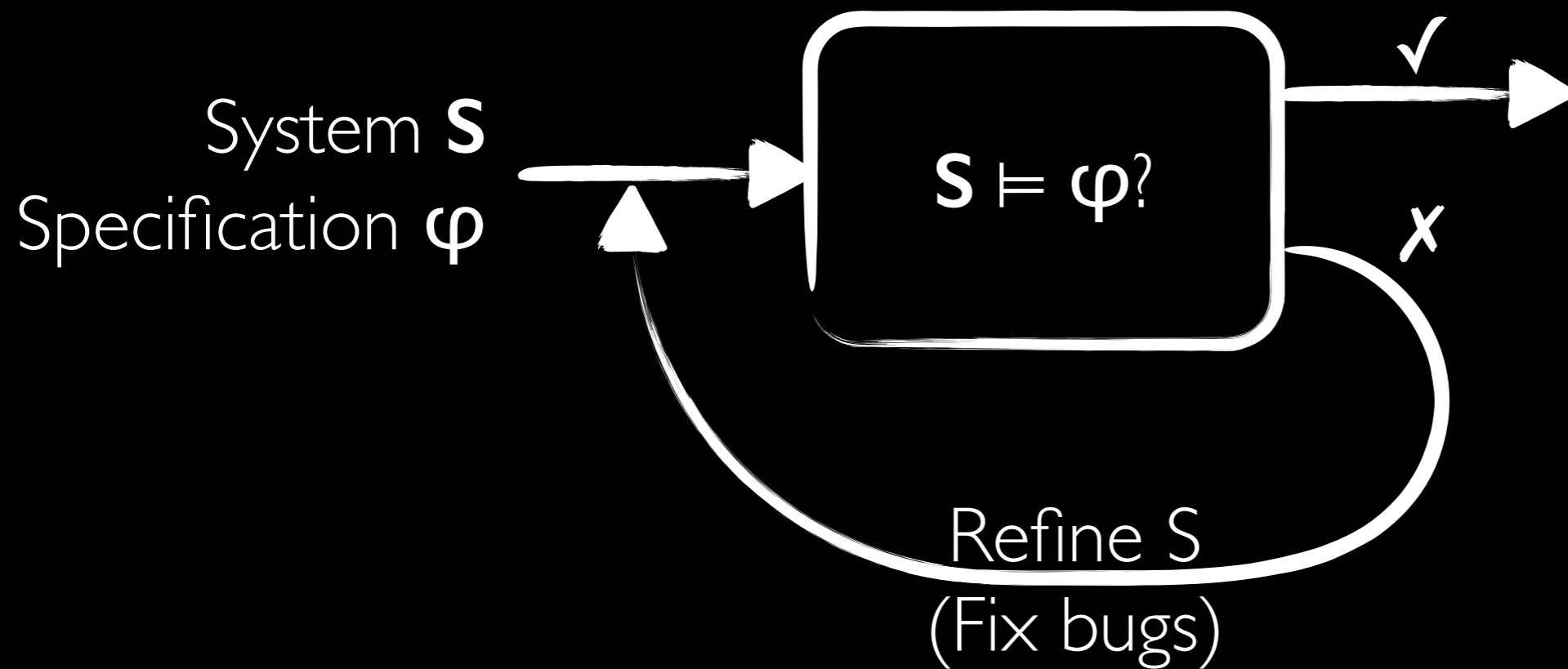
ACTS
09/02/2015, Chennai



cmj

VERIFICATION

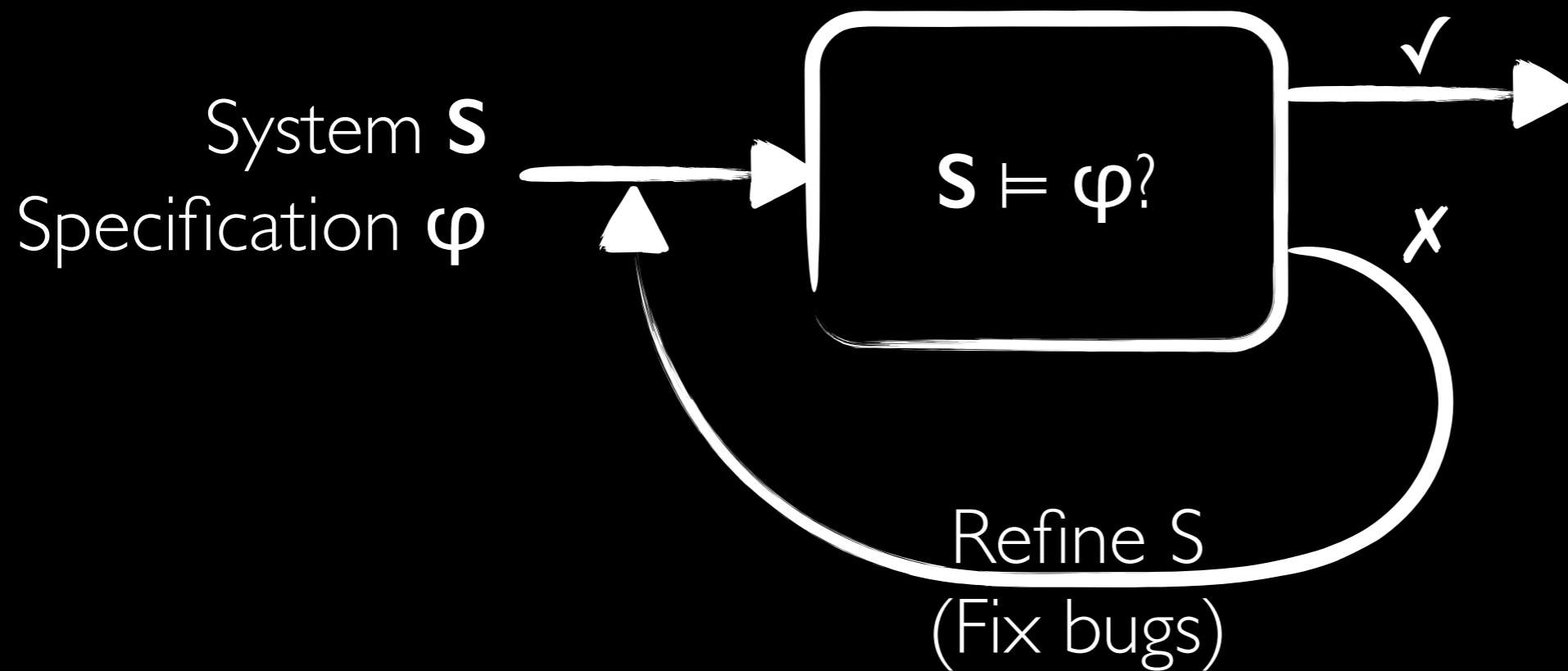
Model Checking



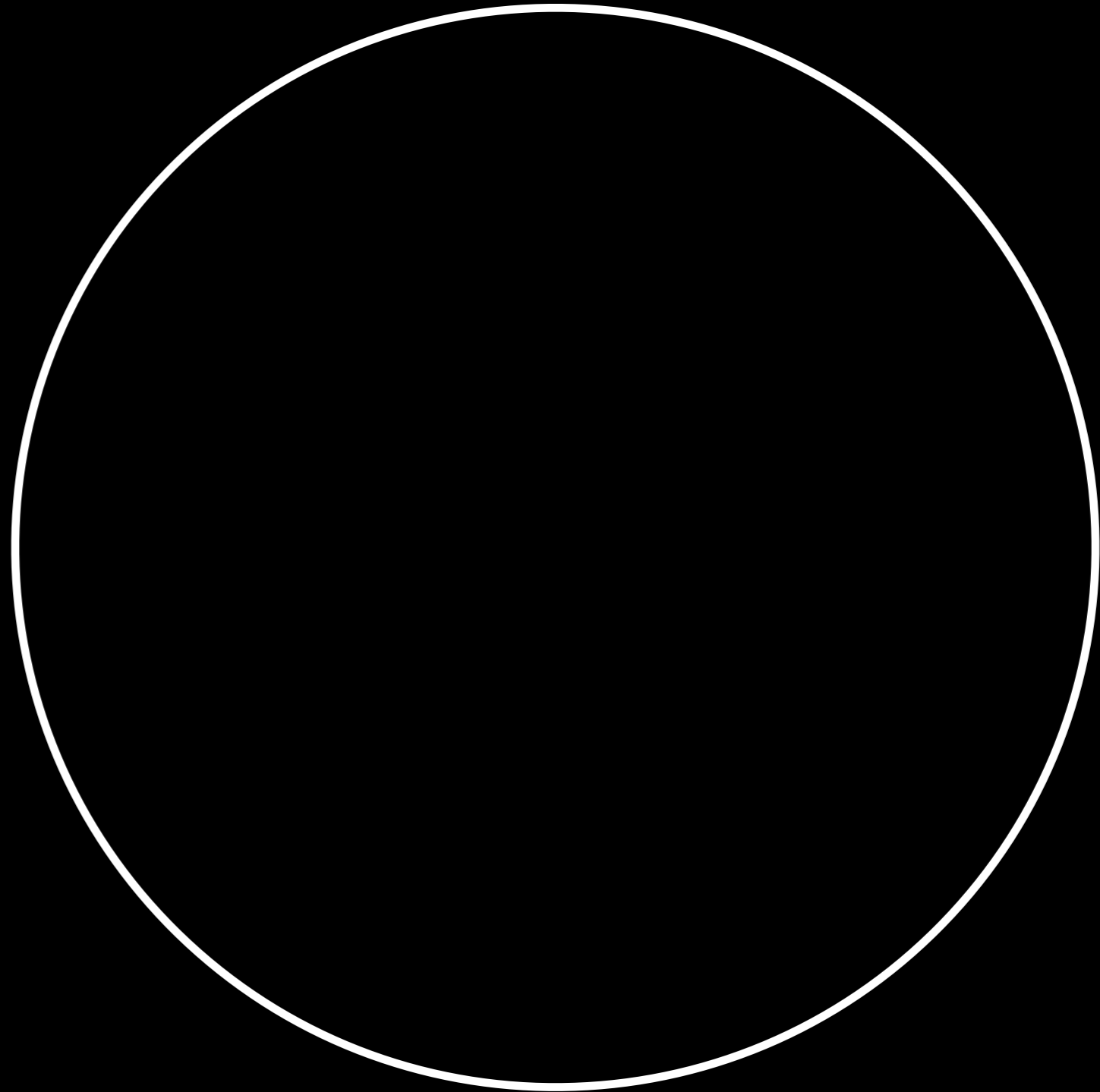
VERIFICATION

Model Checking

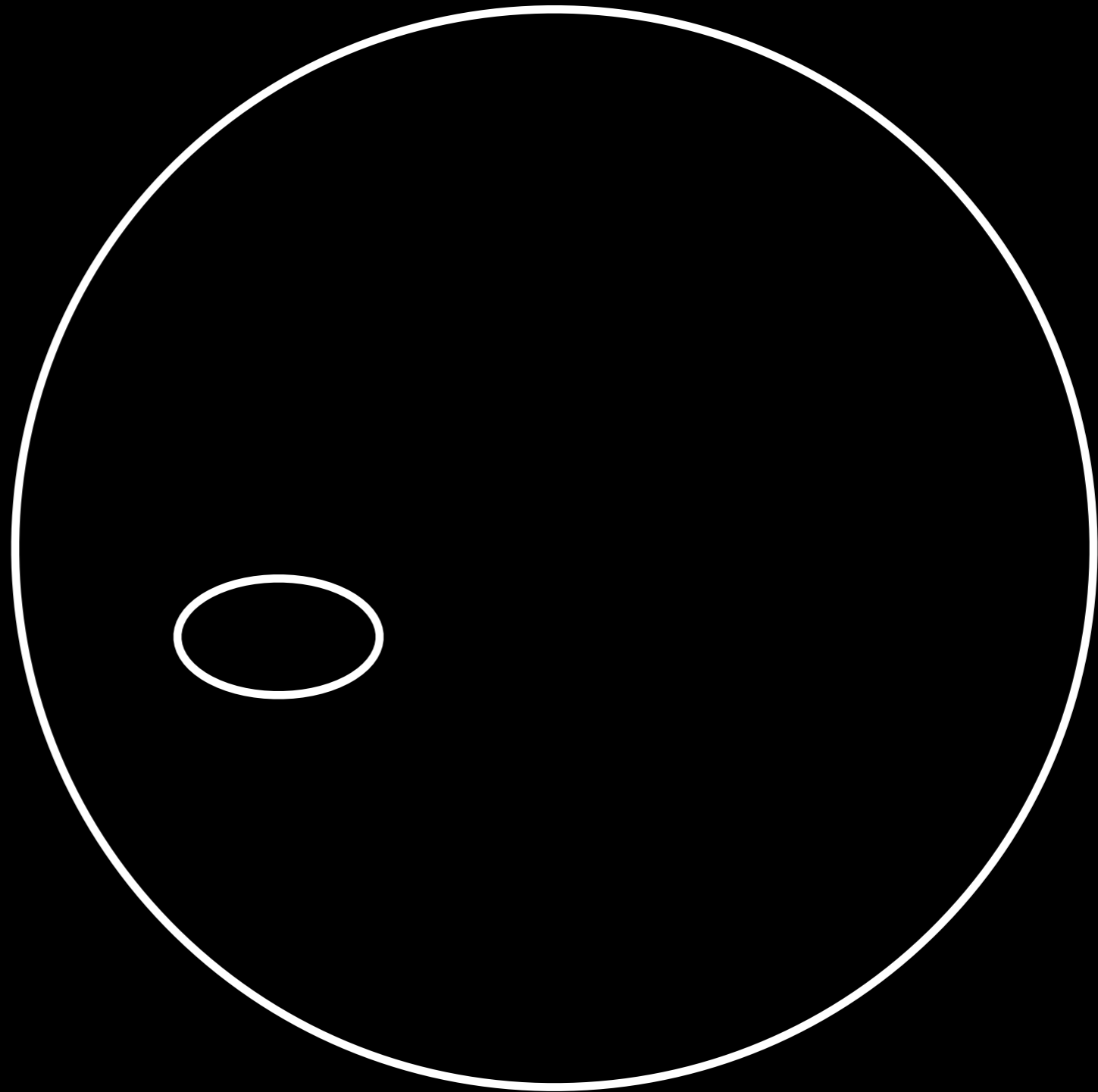
> Undecidable in many cases



UNDER-APPROXIMATE VERIFICATION

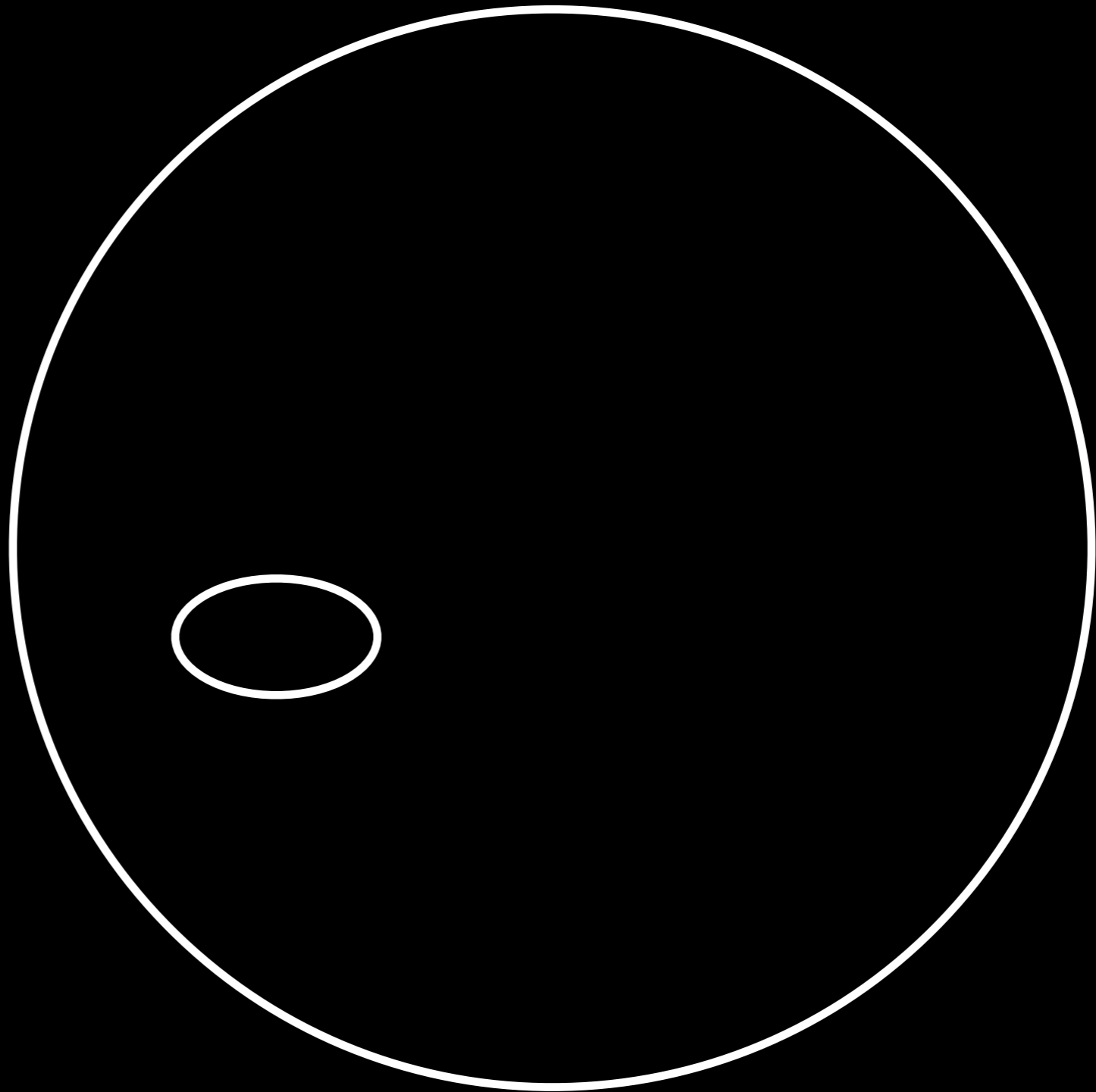


UNDER-APPROXIMATE VERIFICATION



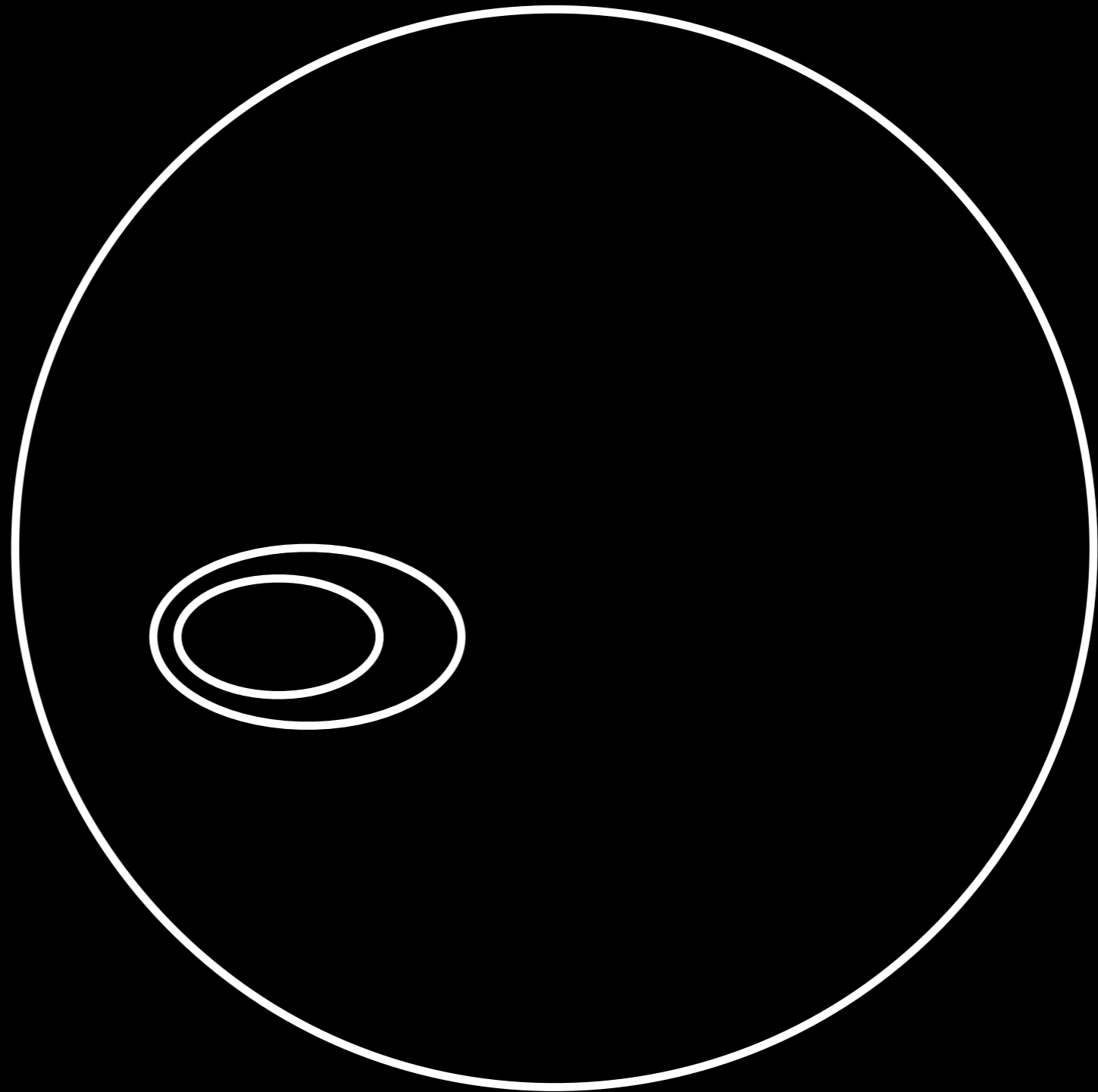
UNDER-APPROXIMATE VERIFICATION

> Parametrised



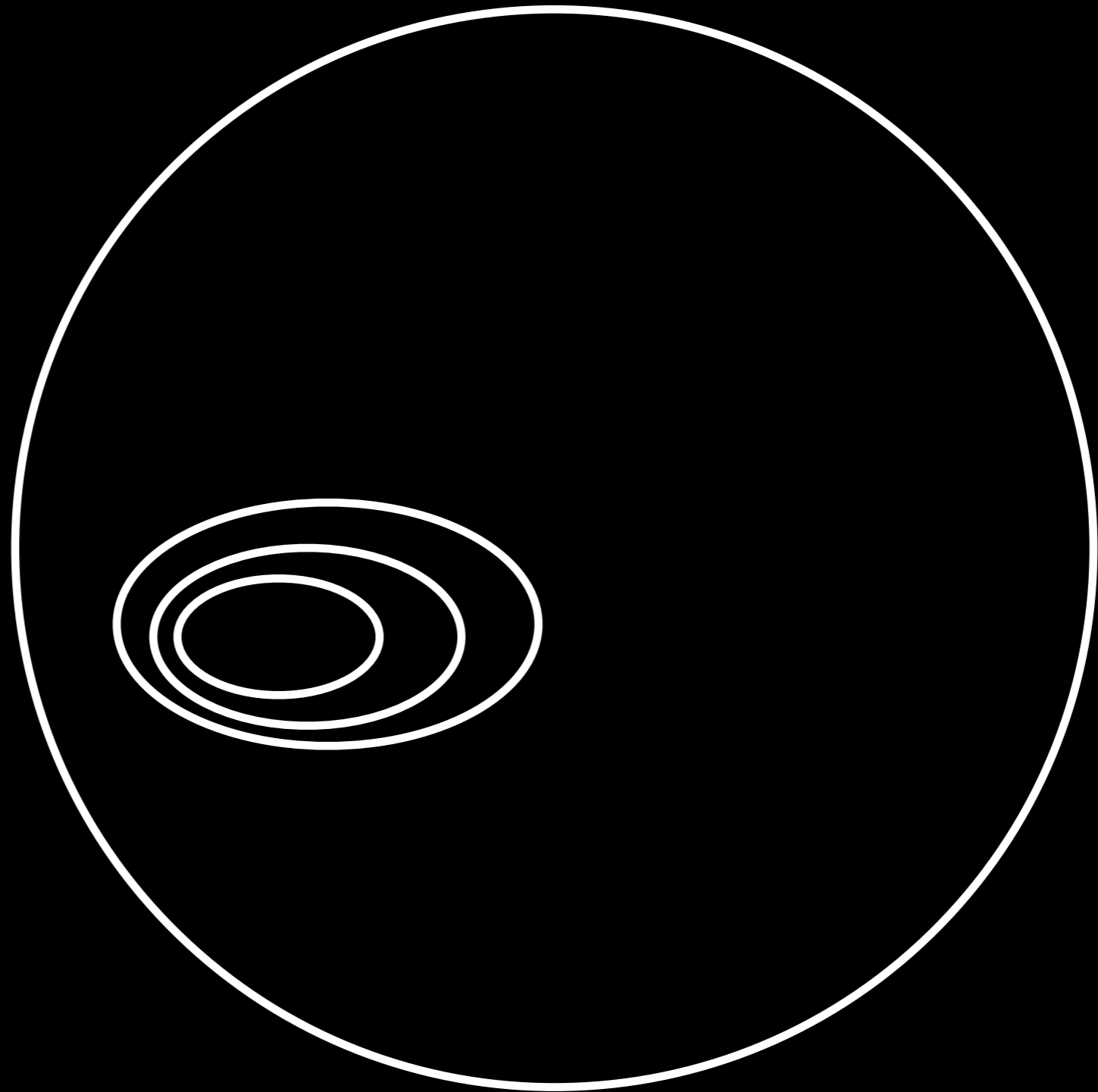
UNDER-APPROXIMATE VERIFICATION

> Parametrised



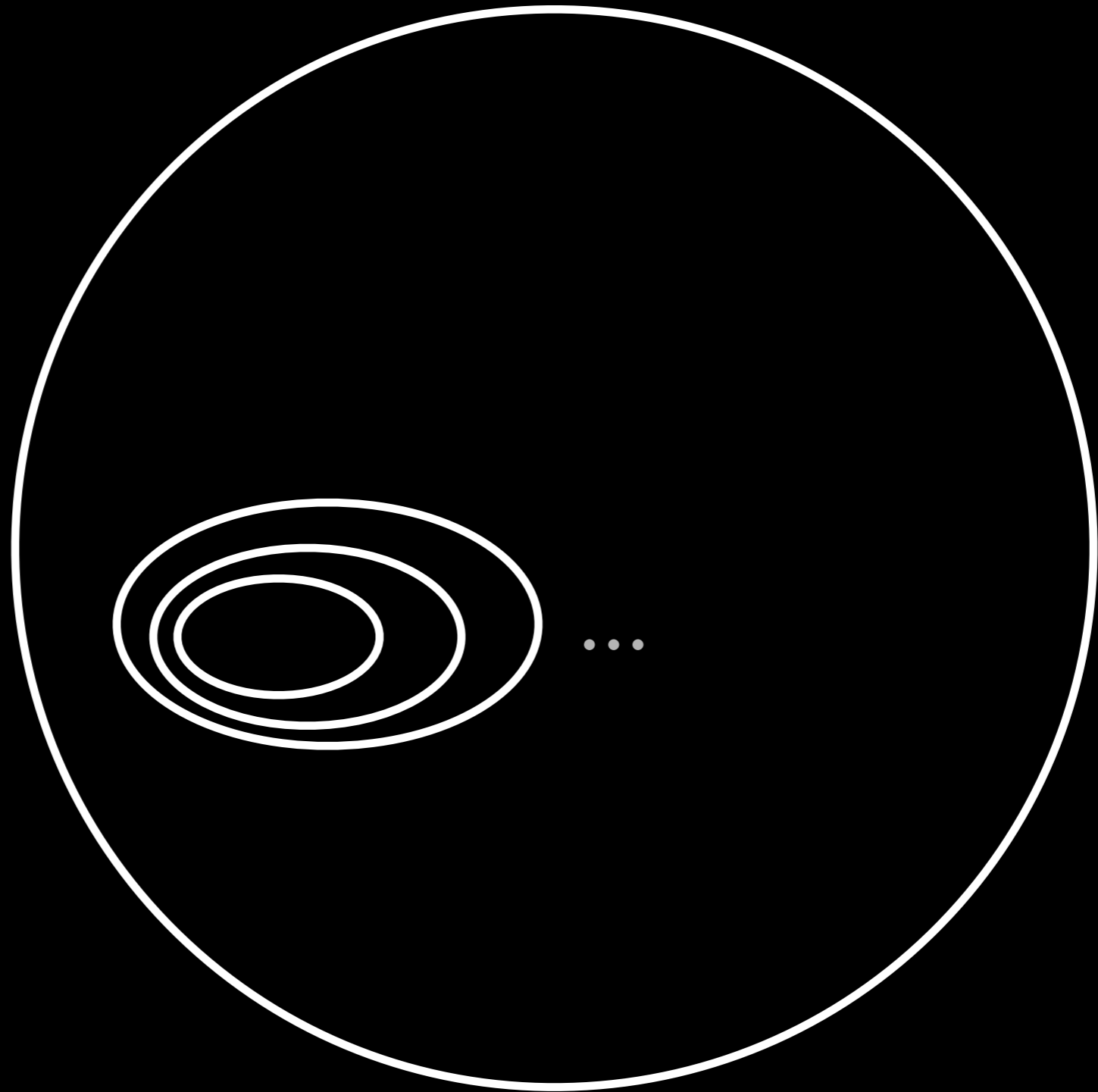
UNDER-APPROXIMATE VERIFICATION

> Parametrised



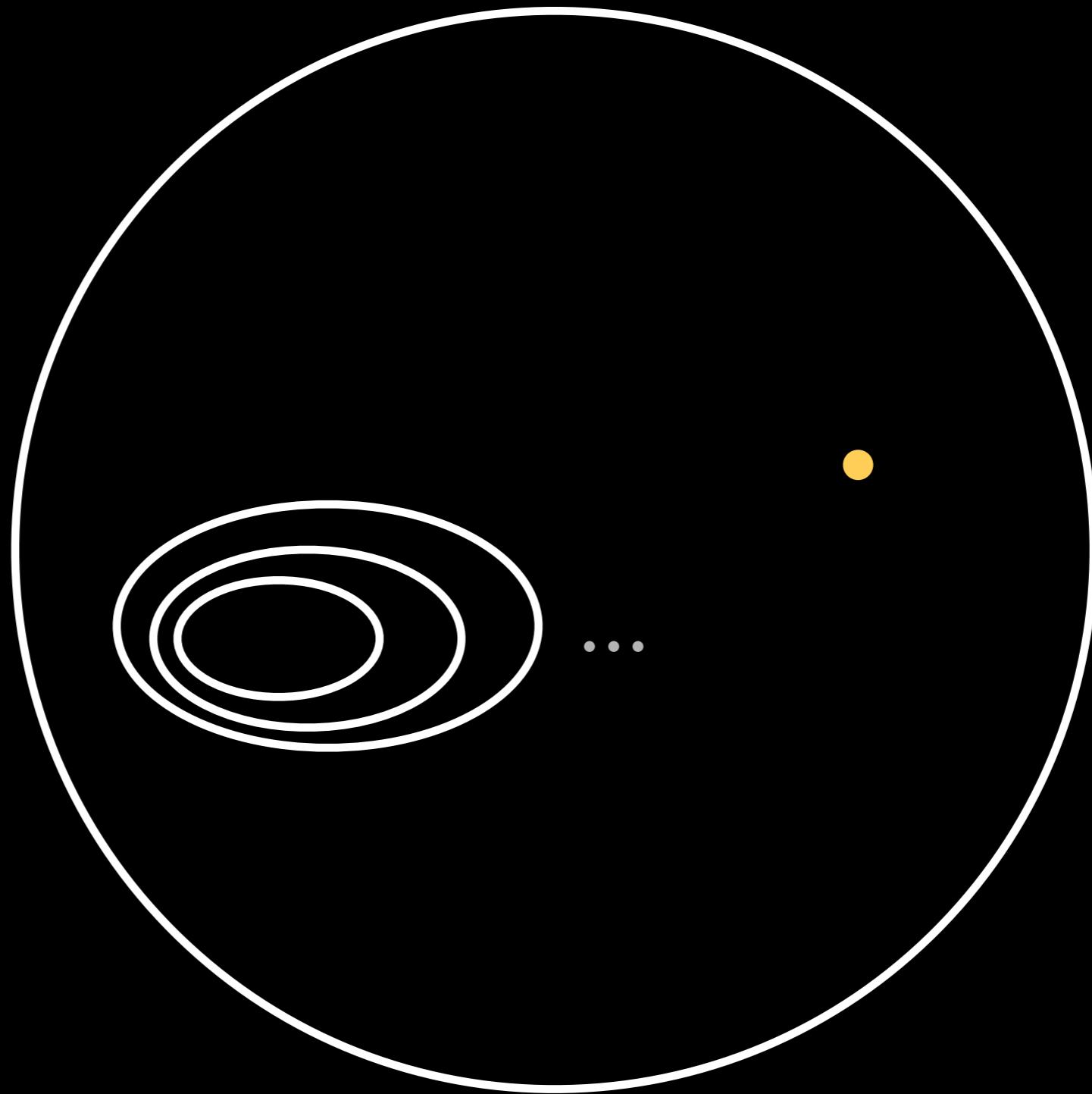
UNDER-APPROXIMATE VERIFICATION

> Parametrised



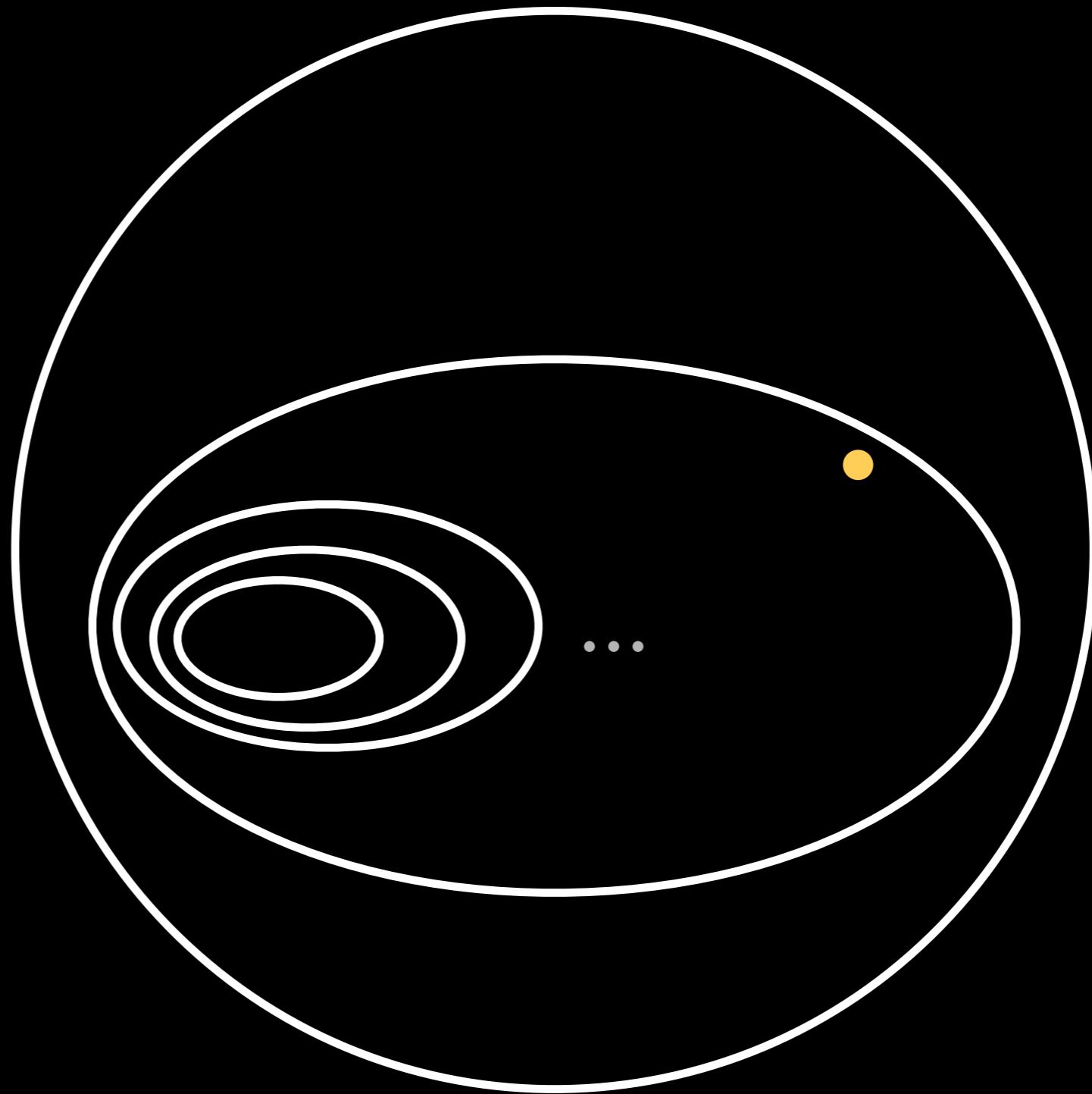
UNDER-APPROXIMATE VERIFICATION

- > Parametrised
- > Exhaustive



UNDER-APPROXIMATE VERIFICATION

- > Parametrised
- > Exhaustive

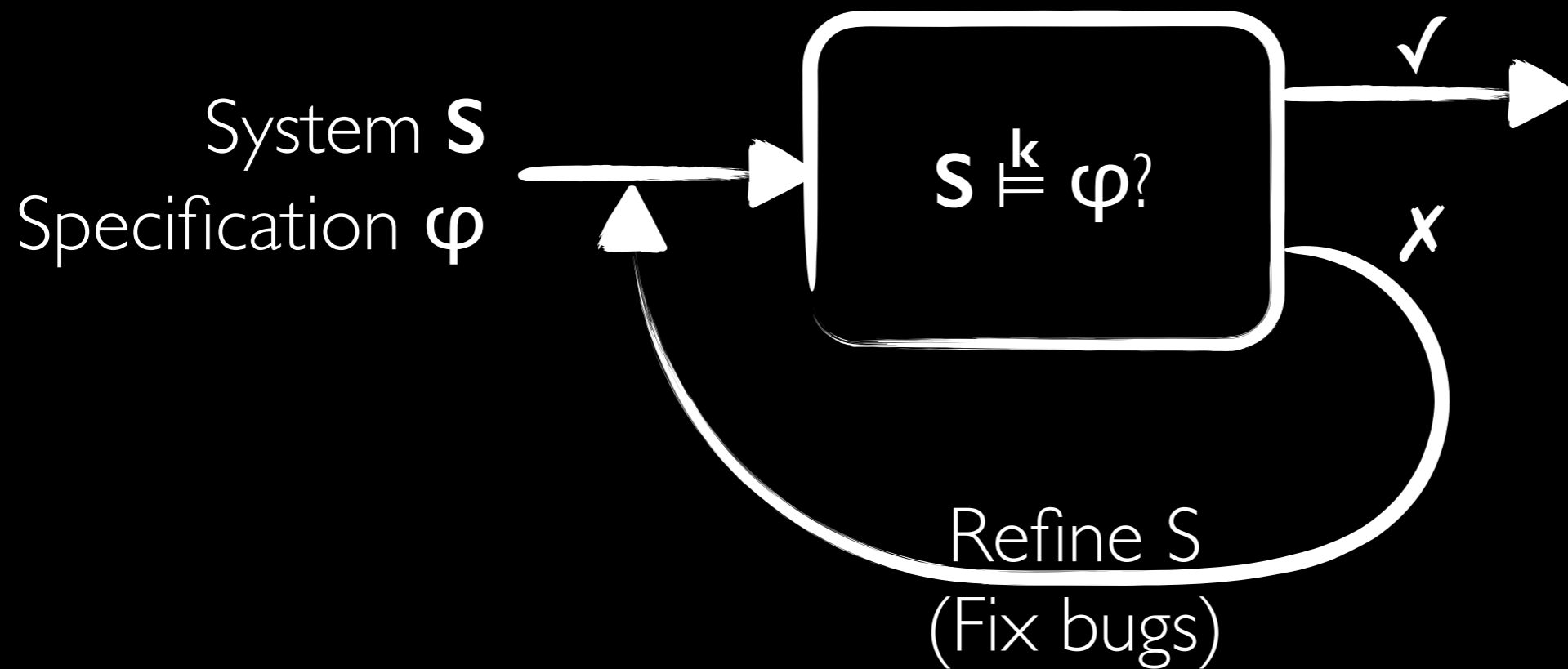


UNDER-APPROXIMATE VERIFICATION

Model Checking



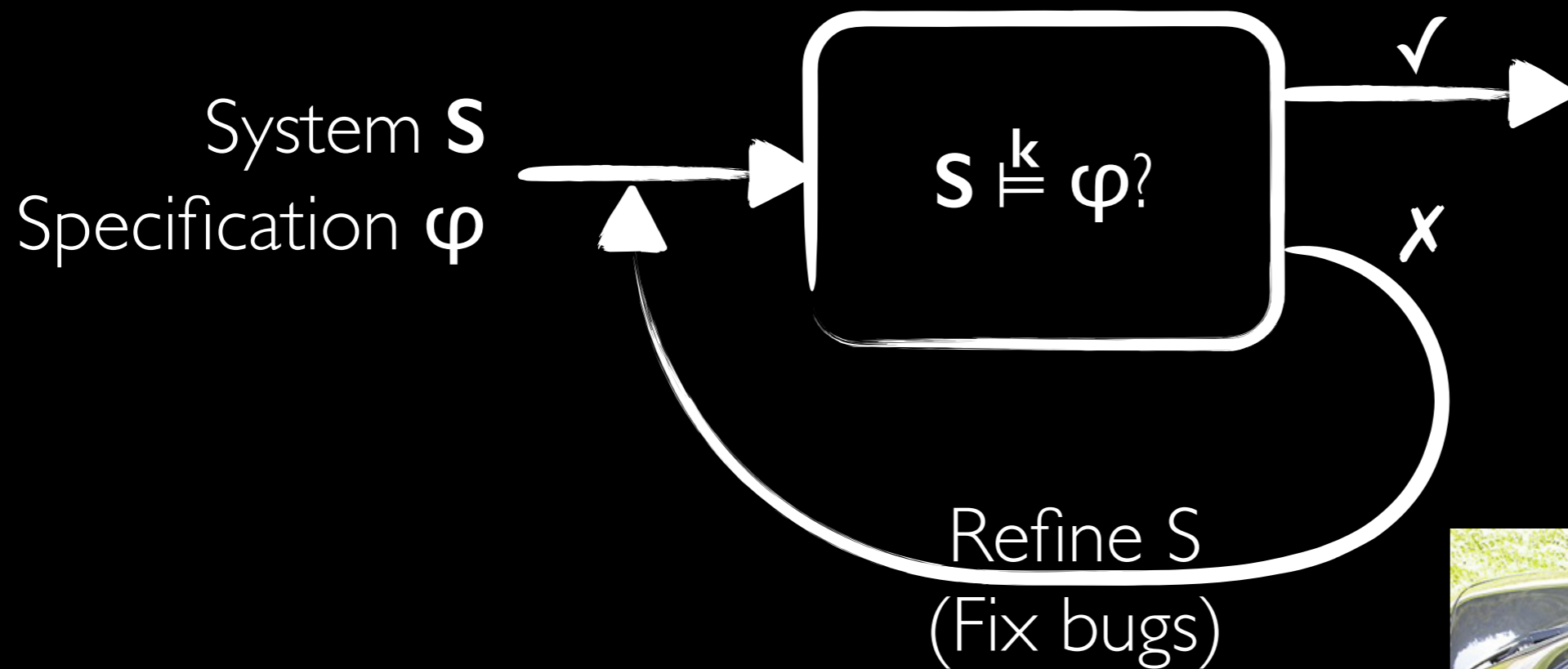
Decidable



UNDER-APPROXIMATE VERIFICATION

Model Checking

> Decidable



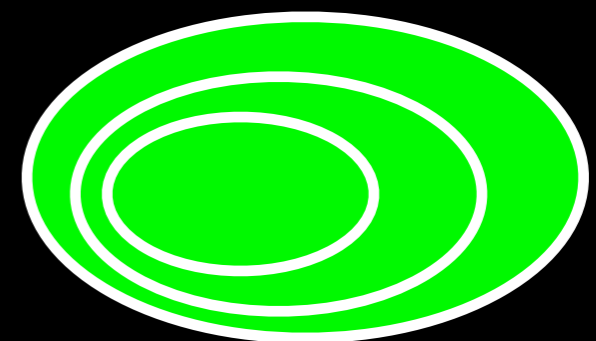
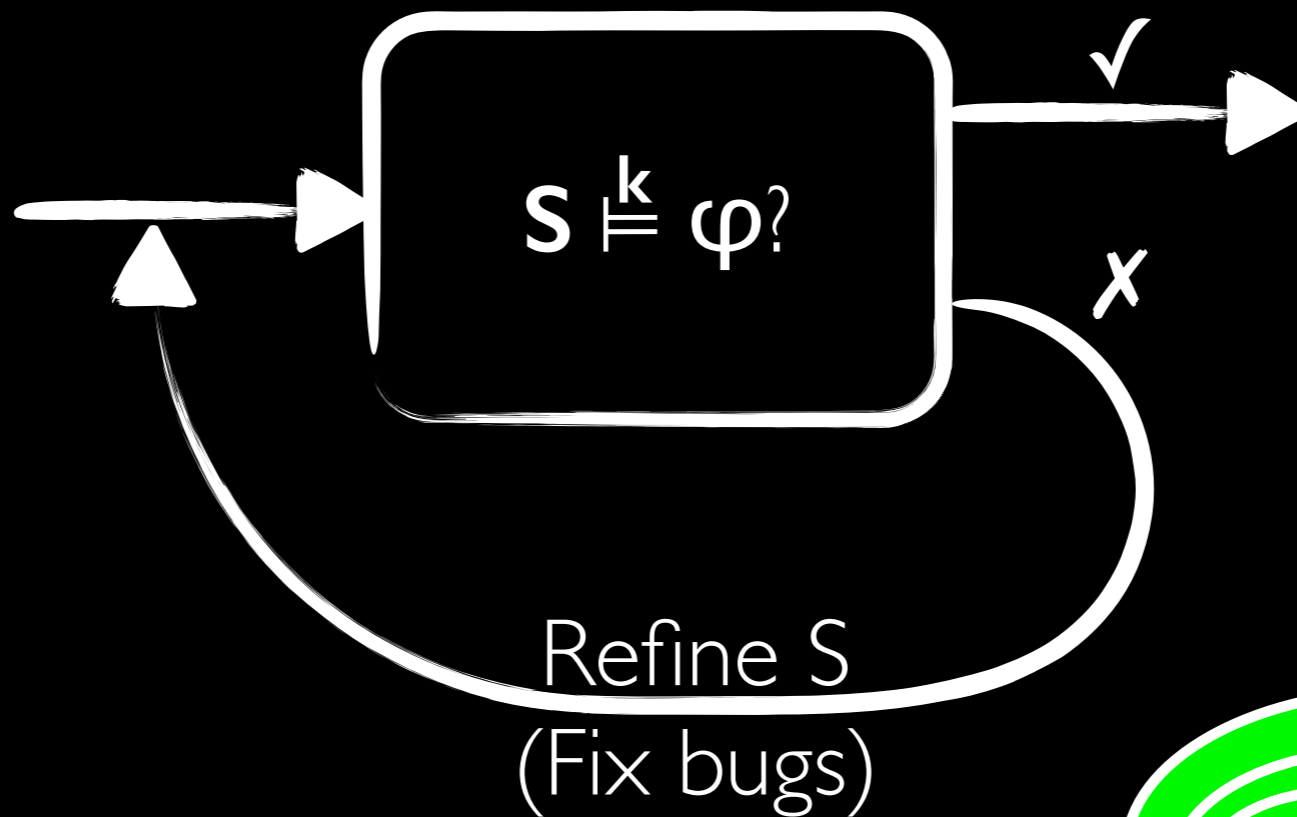
UNDER-APPROXIMATE VERIFICATION

Model Checking



Decidable

System S
Specification φ

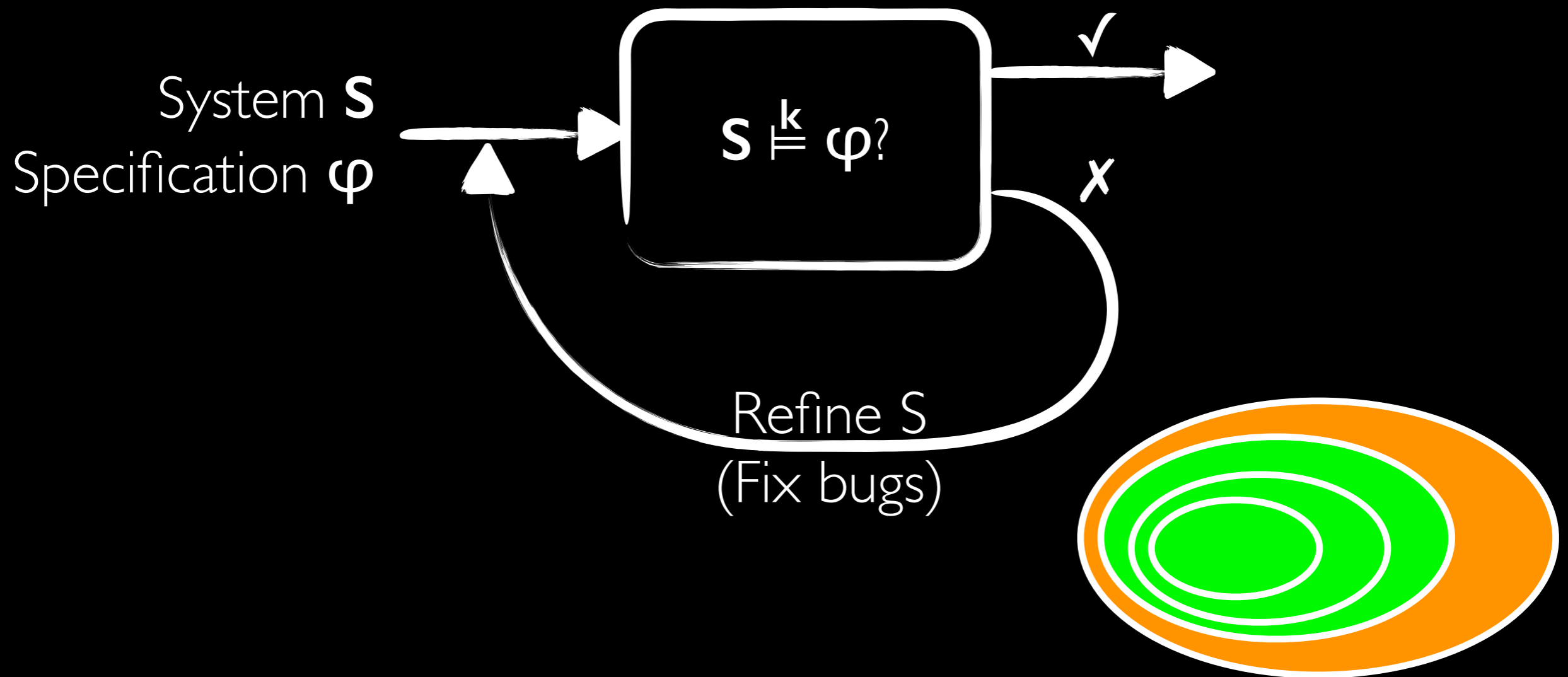


UNDER-APPROXIMATE VERIFICATION

Model Checking



Decidable



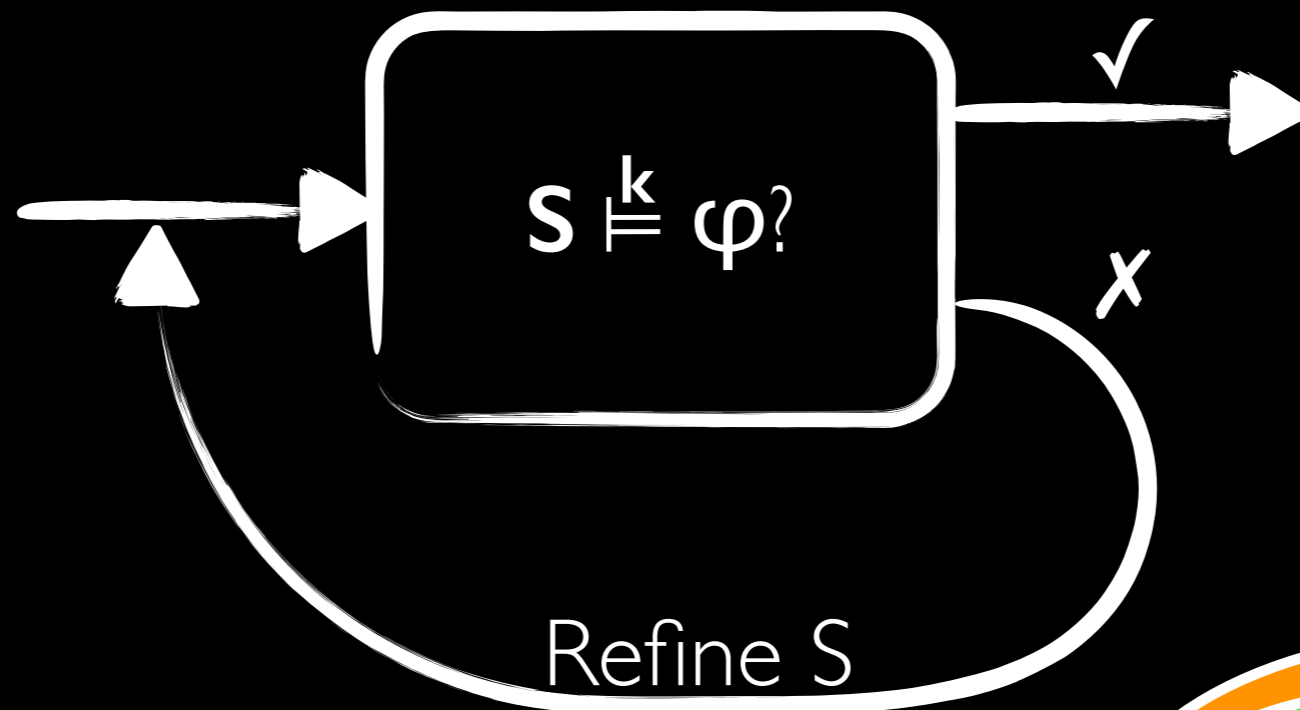
UNDER-APPROXIMATE VERIFICATION

Model Checking

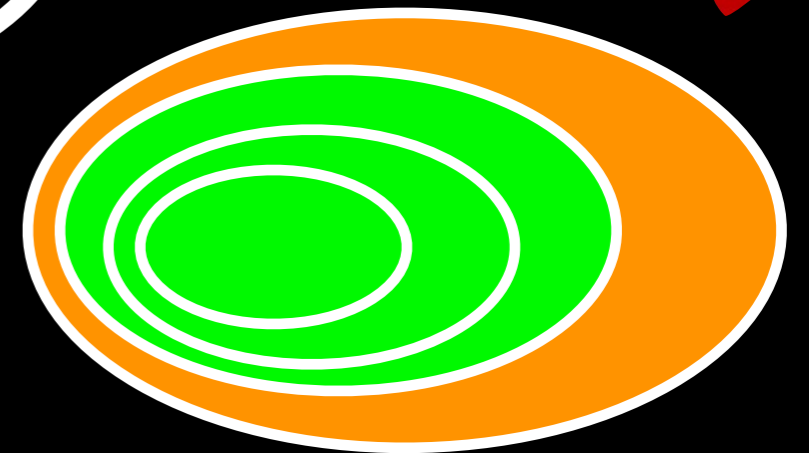


Decidable

System S
Specification φ

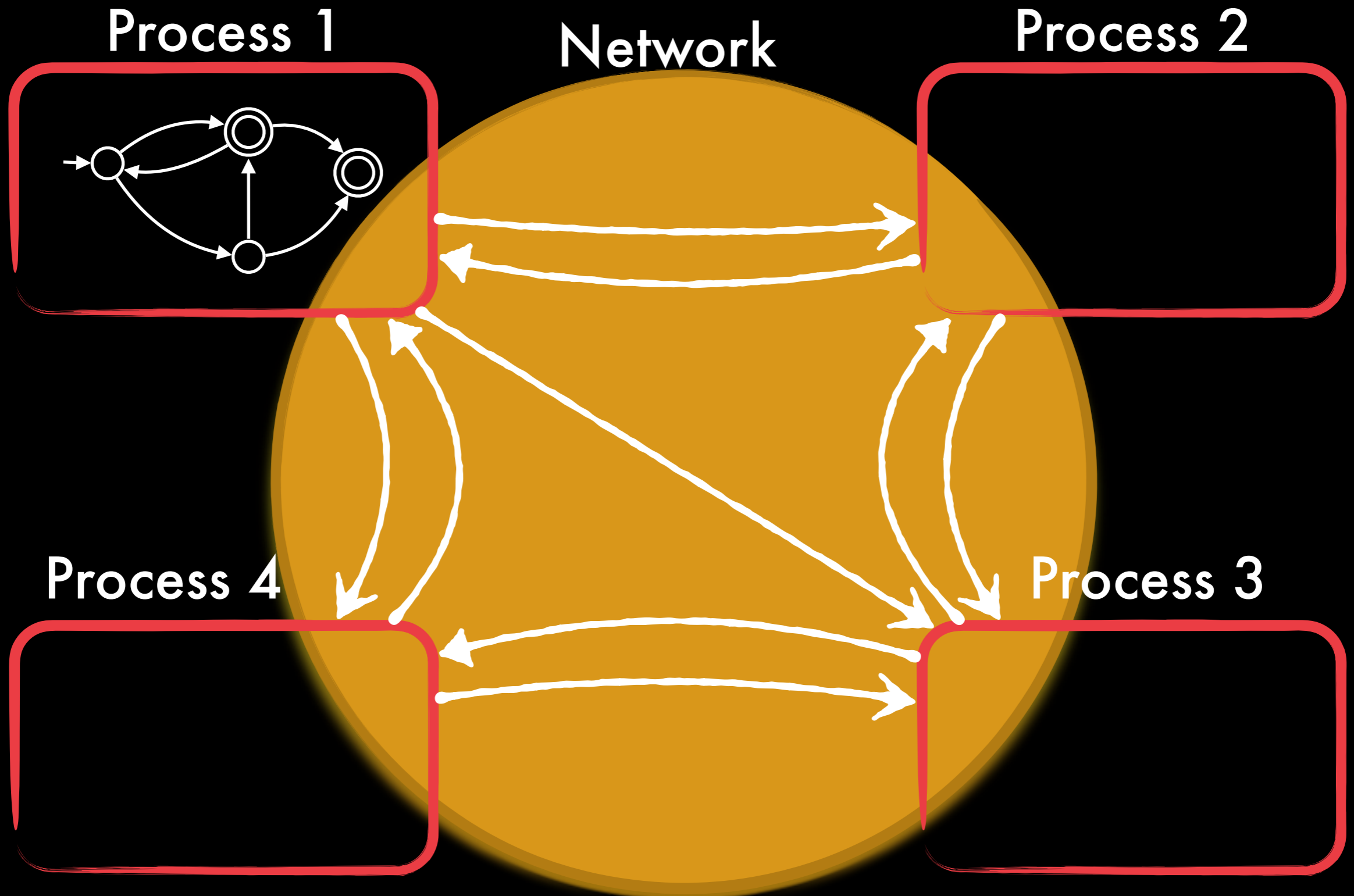


Refine S
(Fix bugs)



COMMUNICATING RECURSIVE PROGRAMS: CONTROL AND SPLIT-WIDTH

COMMUNICATING DISTRIBUTED SYSTEMS



VERIFICATION PROBLEMS

- * Emptiness or Reachability
- * Inclusion or Universality
- * Satisfiability ϕ
- * Model Checking: $S \models \phi$
 - * Temporal logics
 - * Propositional dynamic logics
 - * Monadic second order logic

COMMUNICATING RECURSIVE PROGRAMS:

- Turing powerful: verification undecidable
- Under-approximations
 - Decidable
 - Controllable

COMMUNICATING RECURSIVE PROGRAMS: CONTROL AND SPLIT-WIDTH

- Turing powerful: verification undecidable
- Under-approximations
 - Decidable
 - Controllable

CONTROLLERS FOR VERIFICATION OF COMMUNICATING SYSTEMS

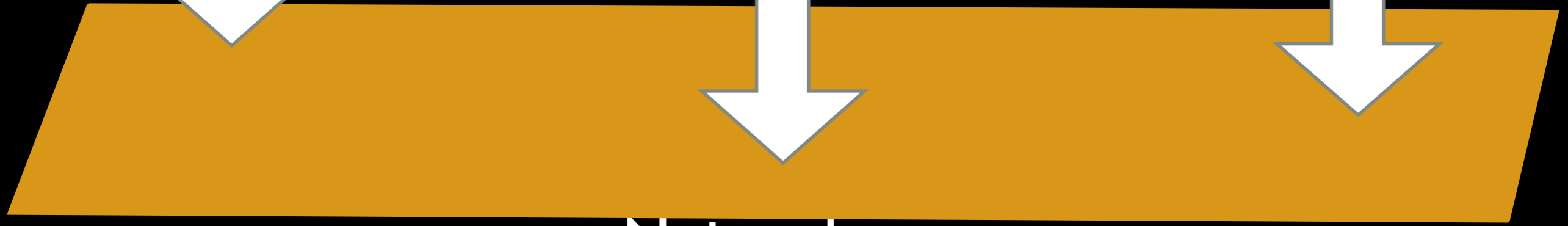
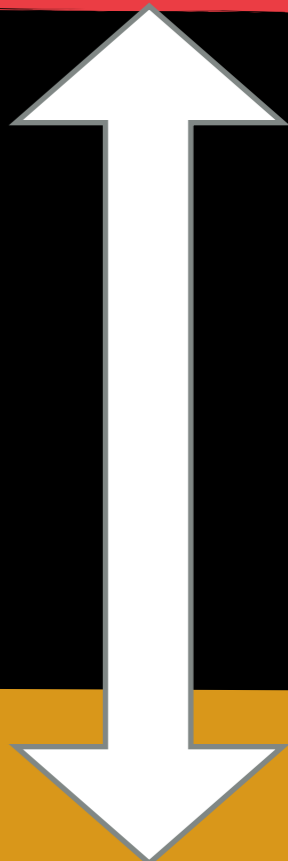
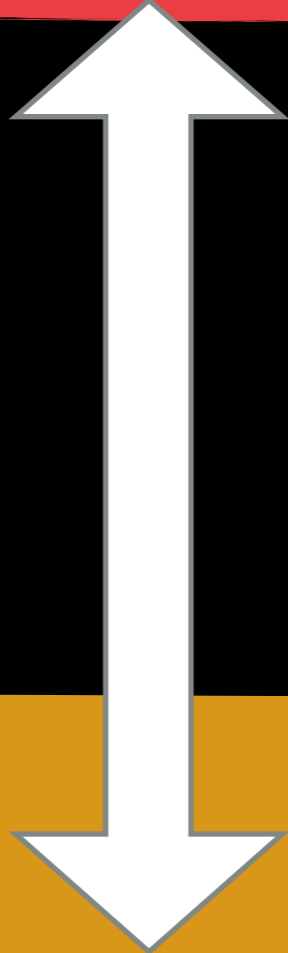
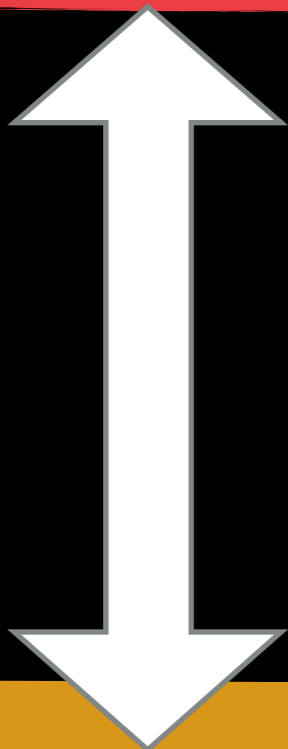
COMMUNICATING DISTRIBUTED SYSTEMS



Process 1

Process 2

Process 3



Network

CONTROLLERS FOR DISTRIBUTED SYSTEMS



Process 1

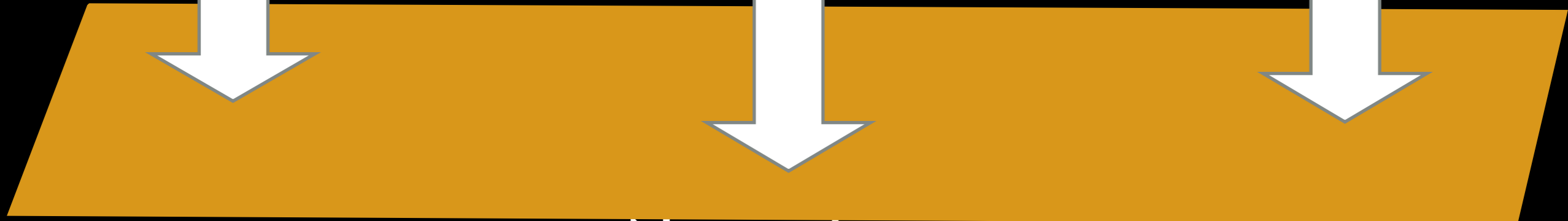
Process 2

Process 3

Controller 1

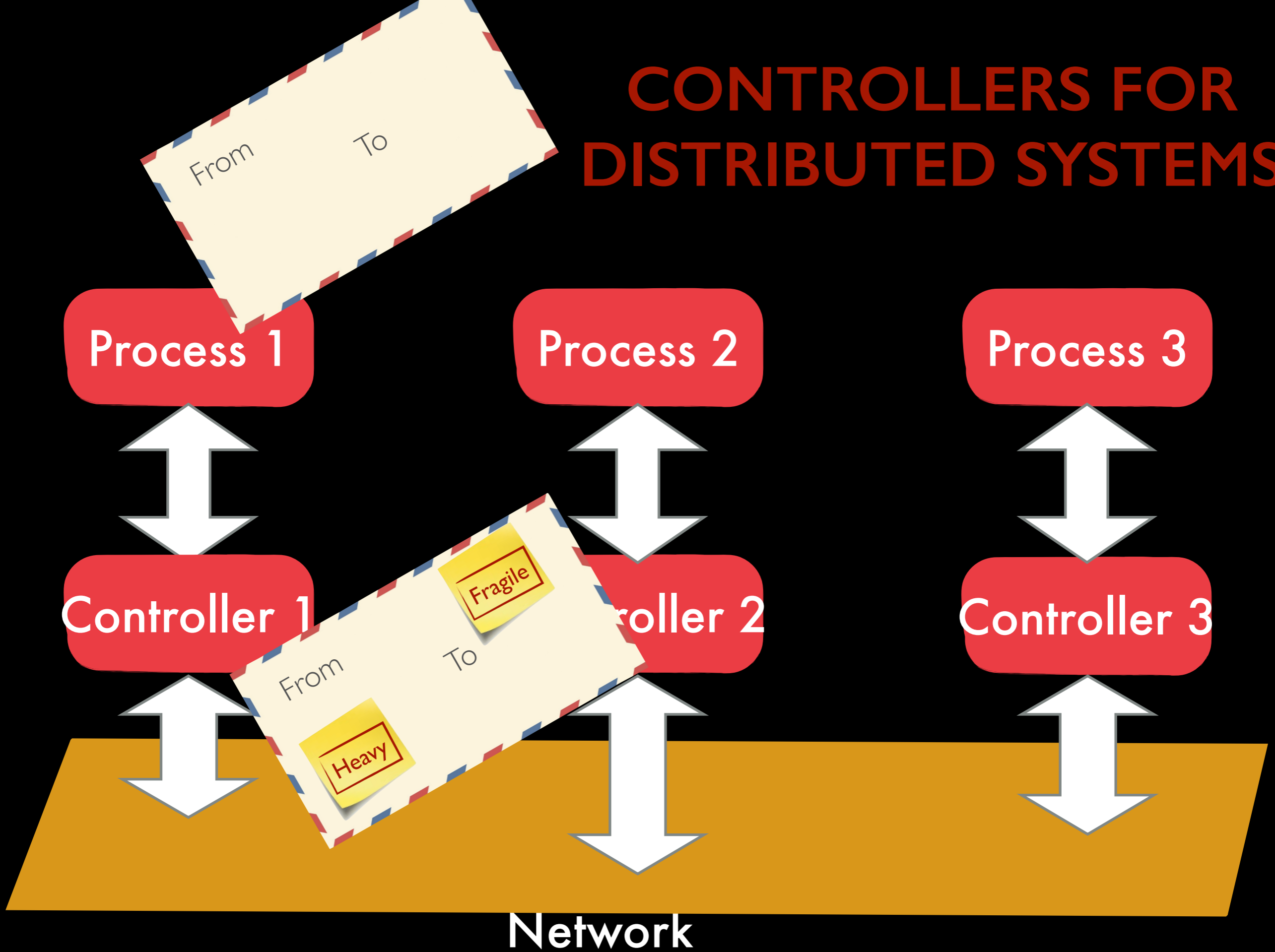
Controller 2

Controller 3



Network

CONTROLLERS FOR DISTRIBUTED SYSTEMS



Process 1

Process 2

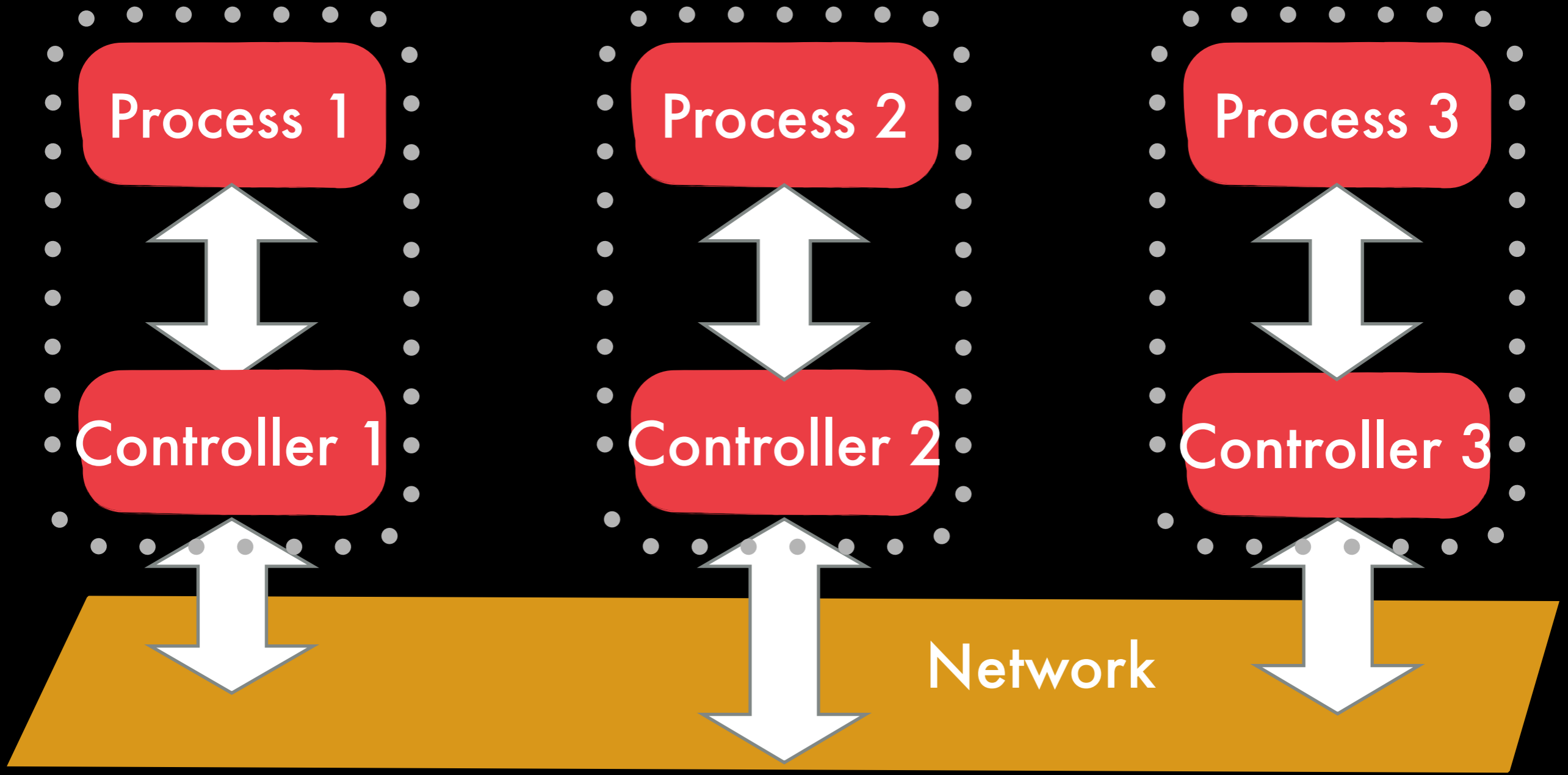
Process 3

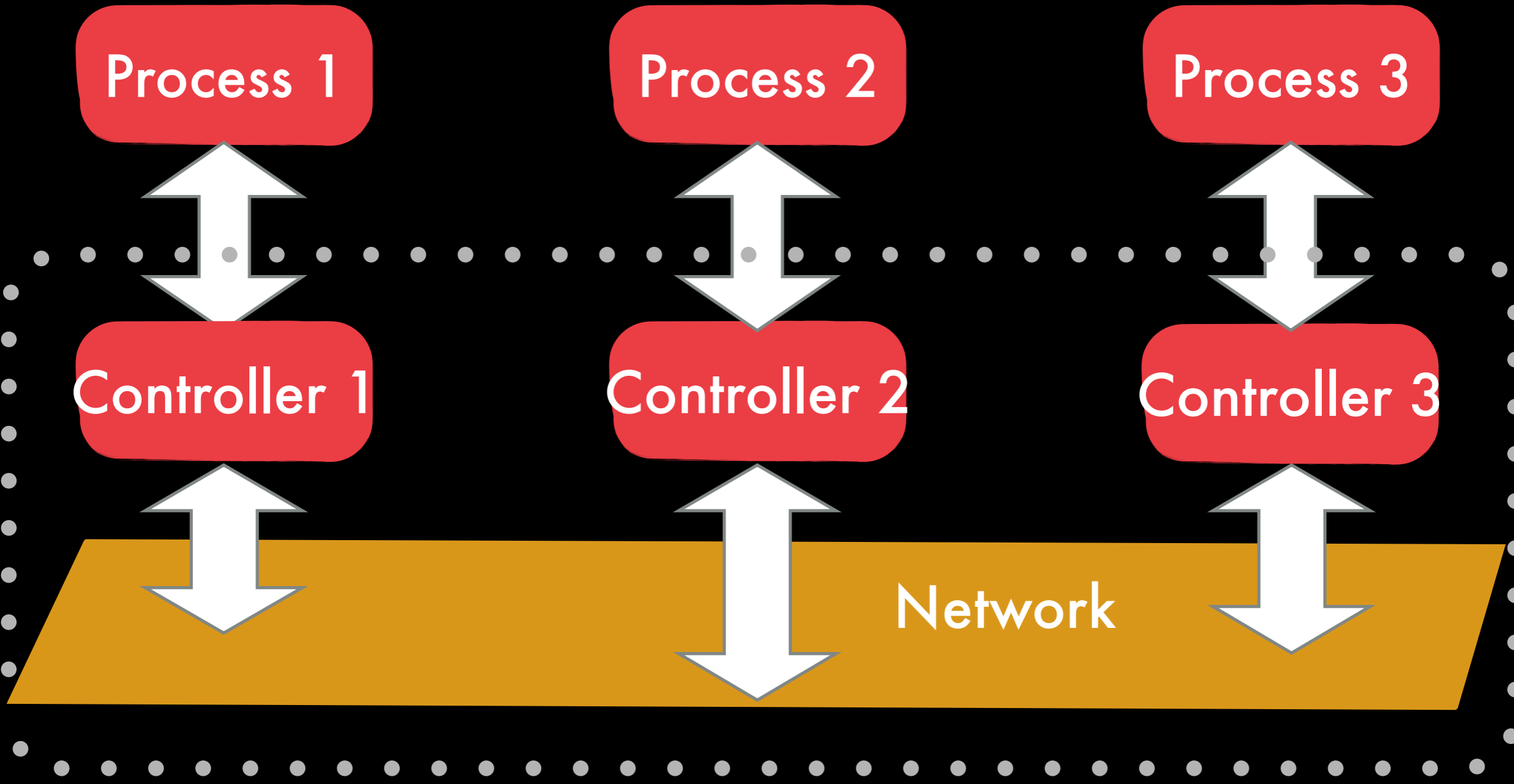
Controller 1

Controller 2

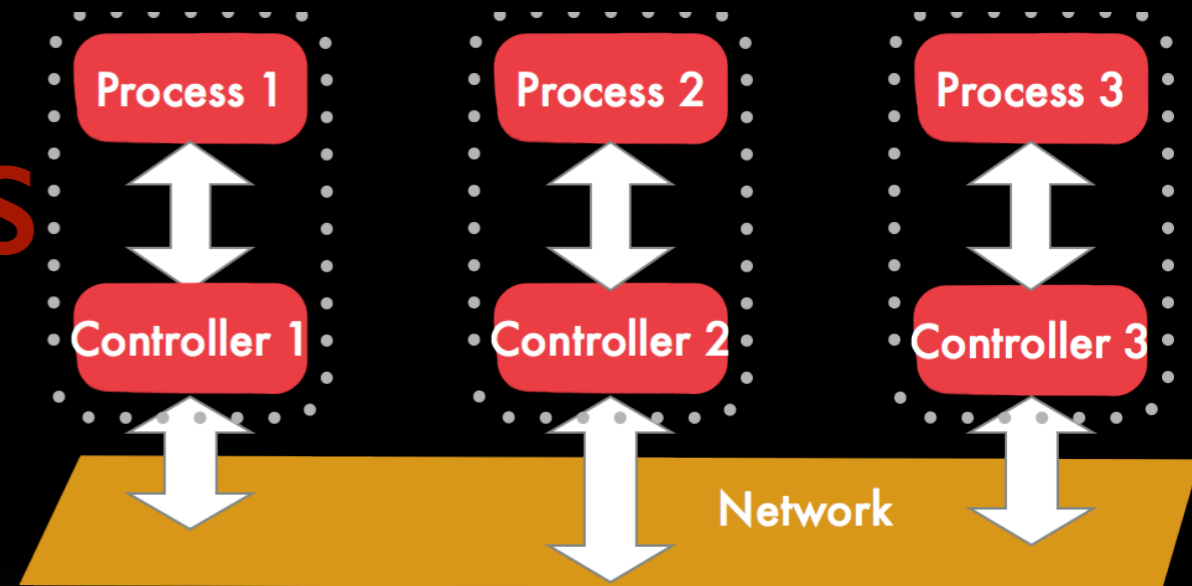
Controller 3

Network



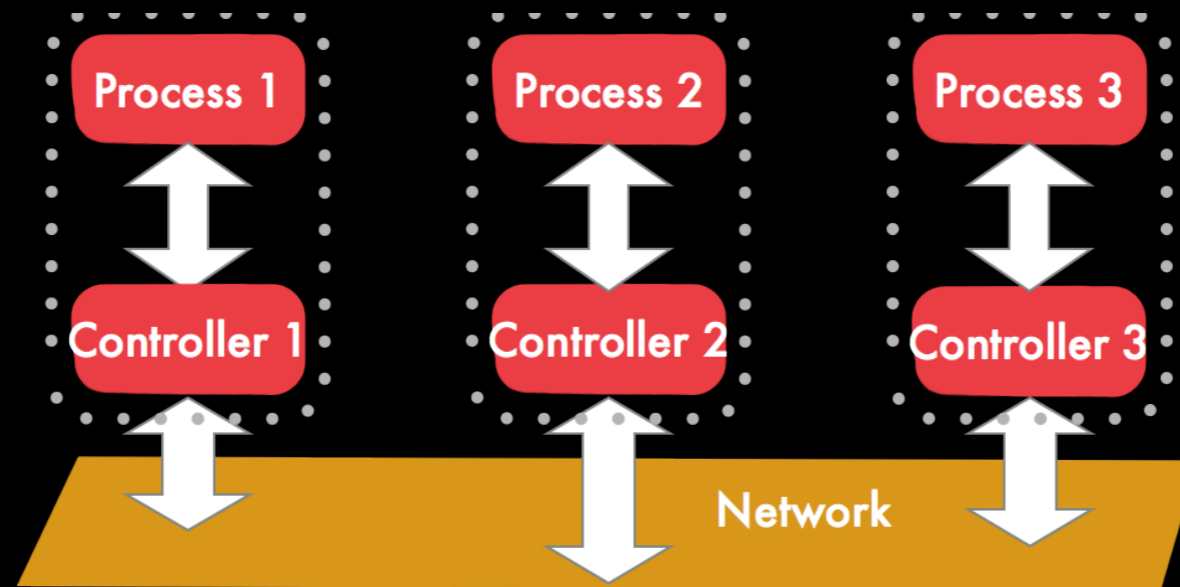


CONTROLLERS FOR DISTRIBUTED SYSTEMS



- > Collection of local controllers
- > Communication via piggy-backing
- > Privacy: Do NOT read states/messages

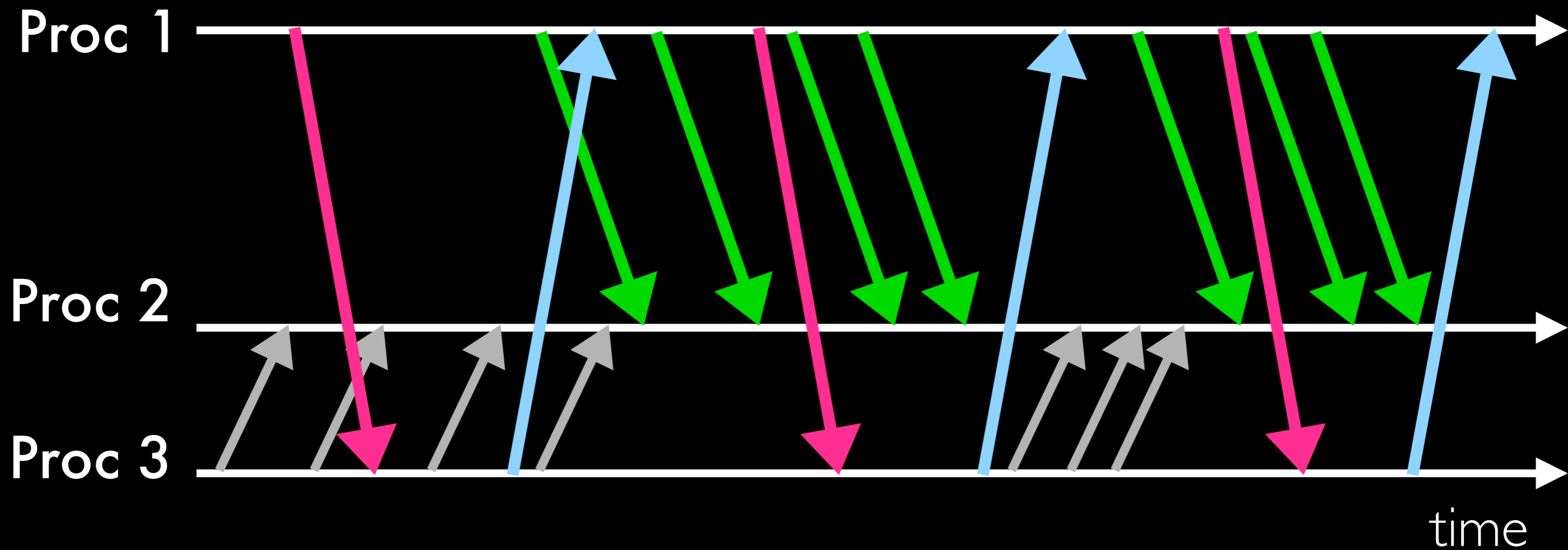
LET'S DESIGN A CONTROLLER



UNDER-APPROXIMATION: BOUNDED (K) PHASE

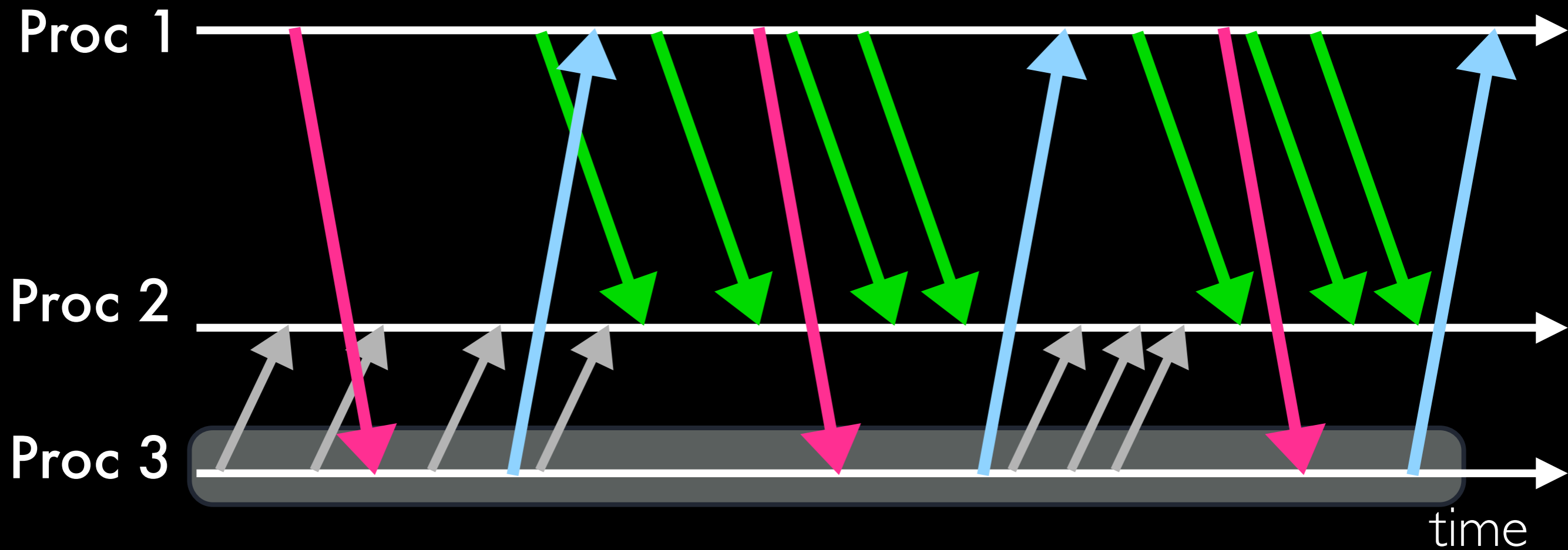
PHASE

Receive from one process, send to all processes



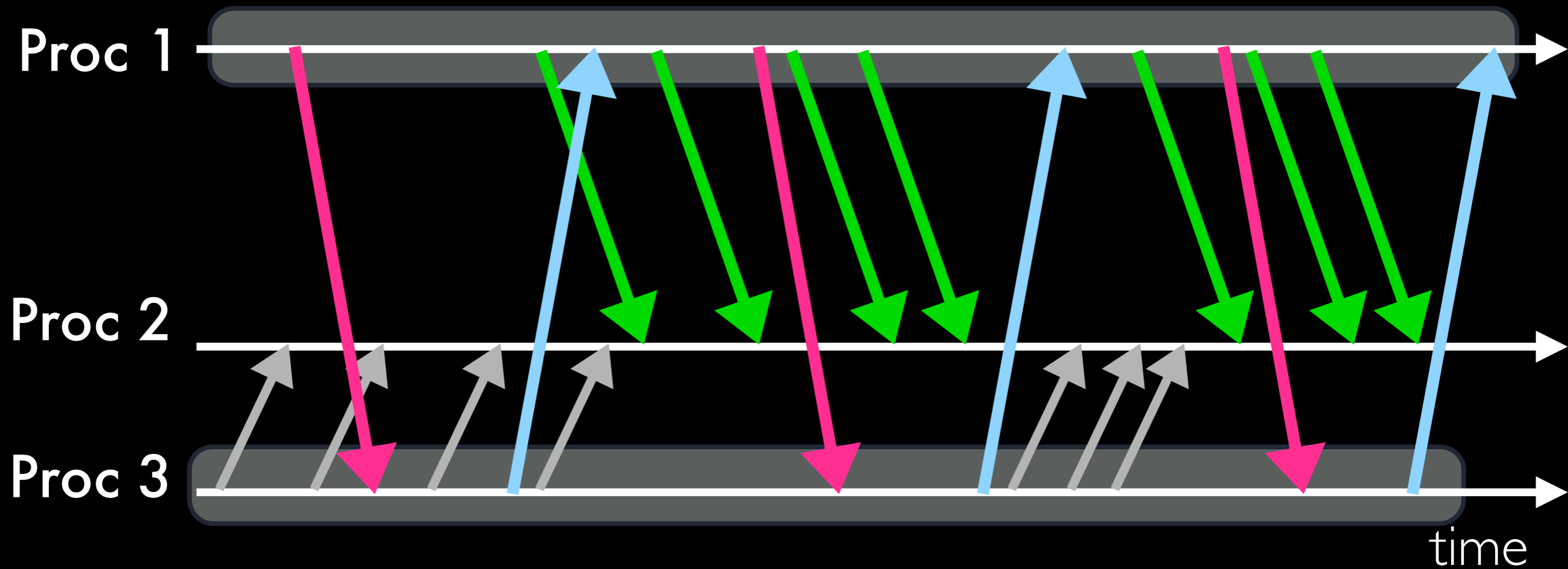
PHASE

Receive from one process, send to all processes



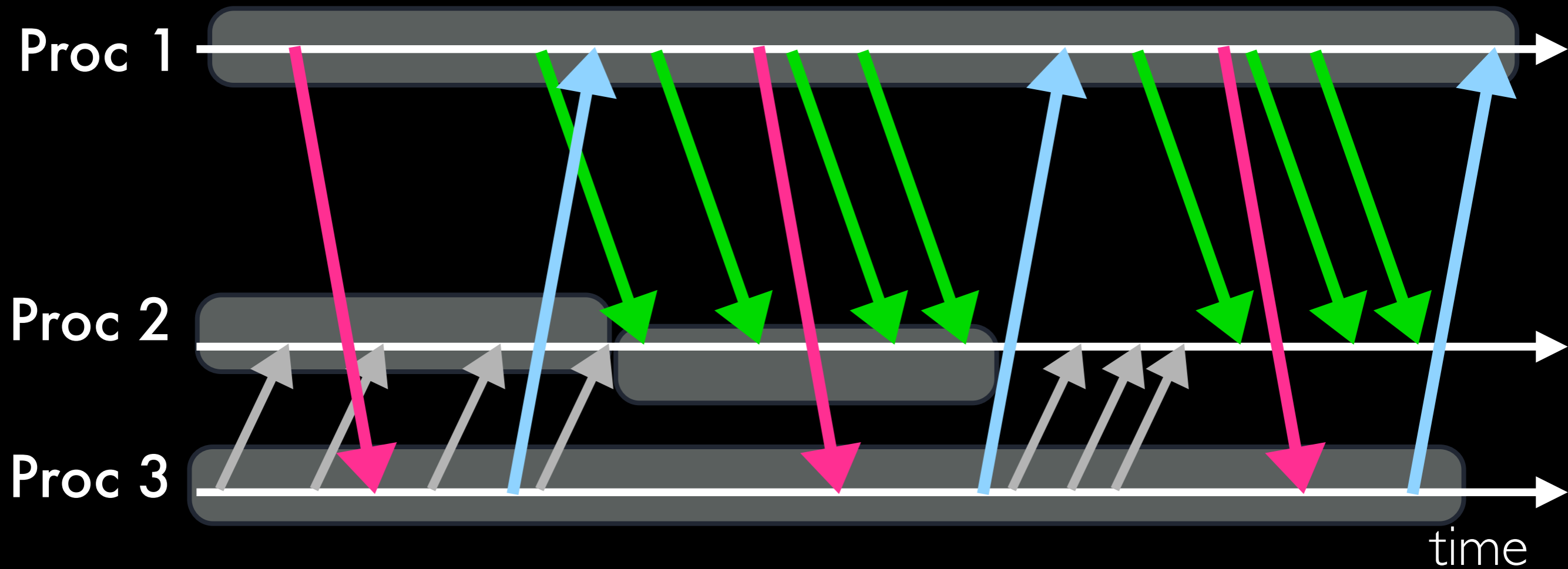
PHASE

Receive from one process, send to all processes



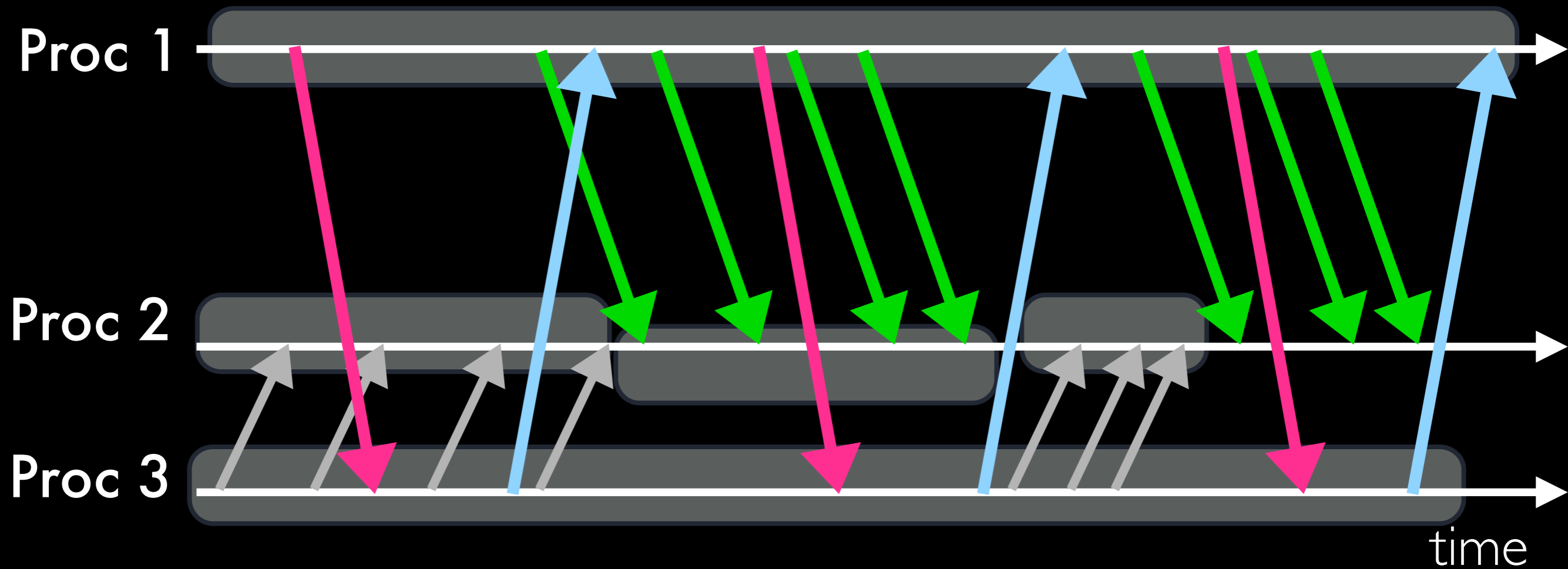
PHASE

Receive from one process, send to all processes



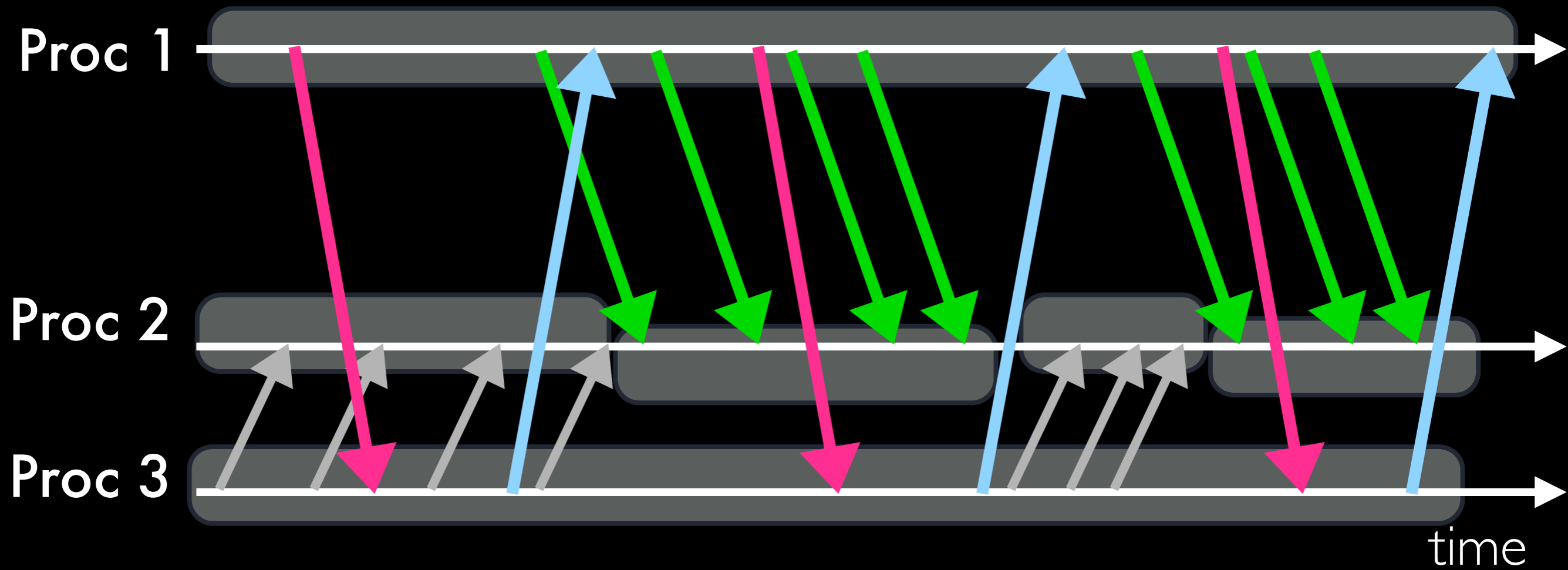
PHASE

Receive from one process, send to all processes



PHASE

Receive from one process, send to all processes

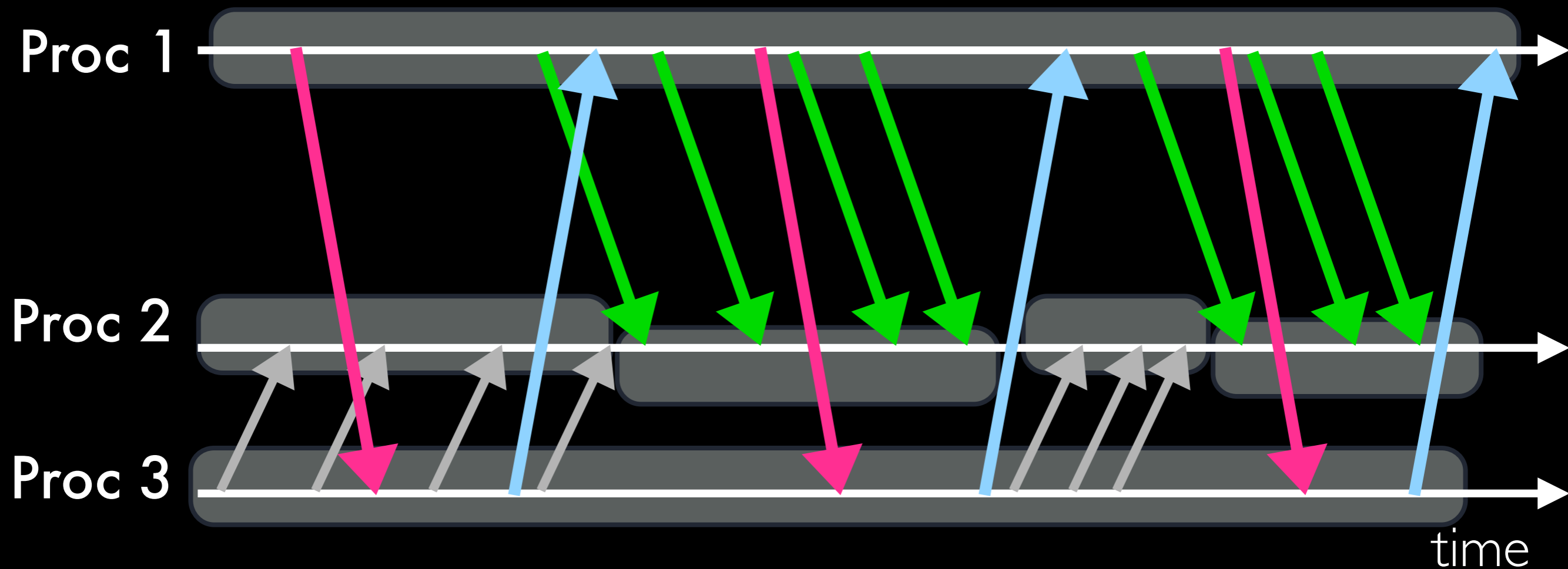


PHASE

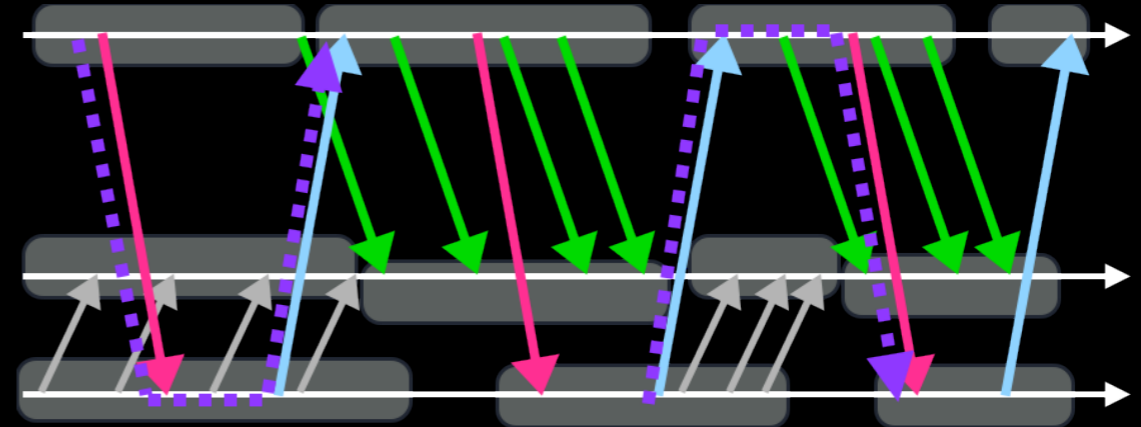
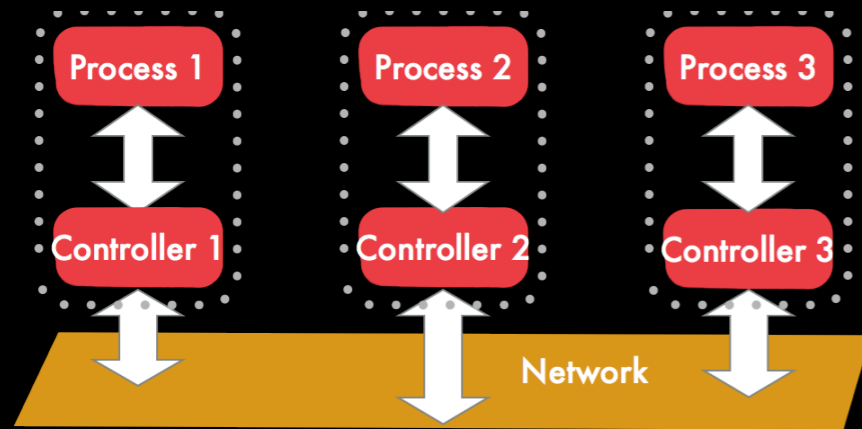
Receive from one process, send to all processes

k-BOUNDED PHASE

1. At most k phases on each process
2. No cycles



DISTRIBUTED CONTROLLER FOR K-BOUNDED PHASE U-A



A local controller for each process

State

Has a Phase Counter

Remembers current sender

Transitions

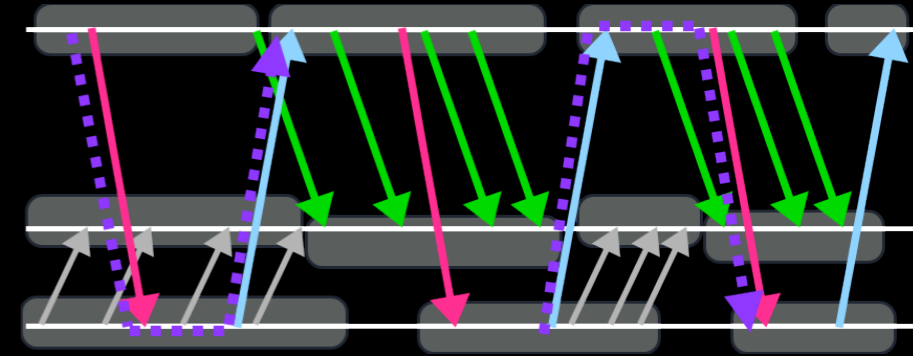
Different sender?

Detect Cycle?

Increment counter,
Update sender

DISTRIBUTED CONTROLLER FOR K-BOUNDED PHASE U-A

Detect Cycle?



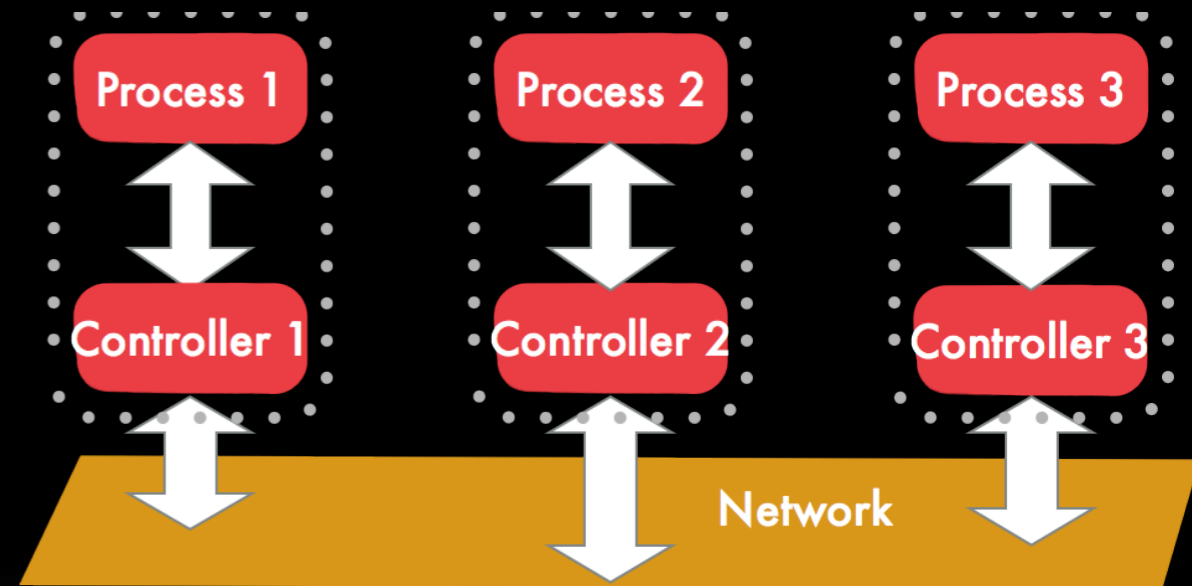
Phase Vectors

best info about phase number of other processes

Sends: tag with phase vector

Receives: update phase vector by taking MAX

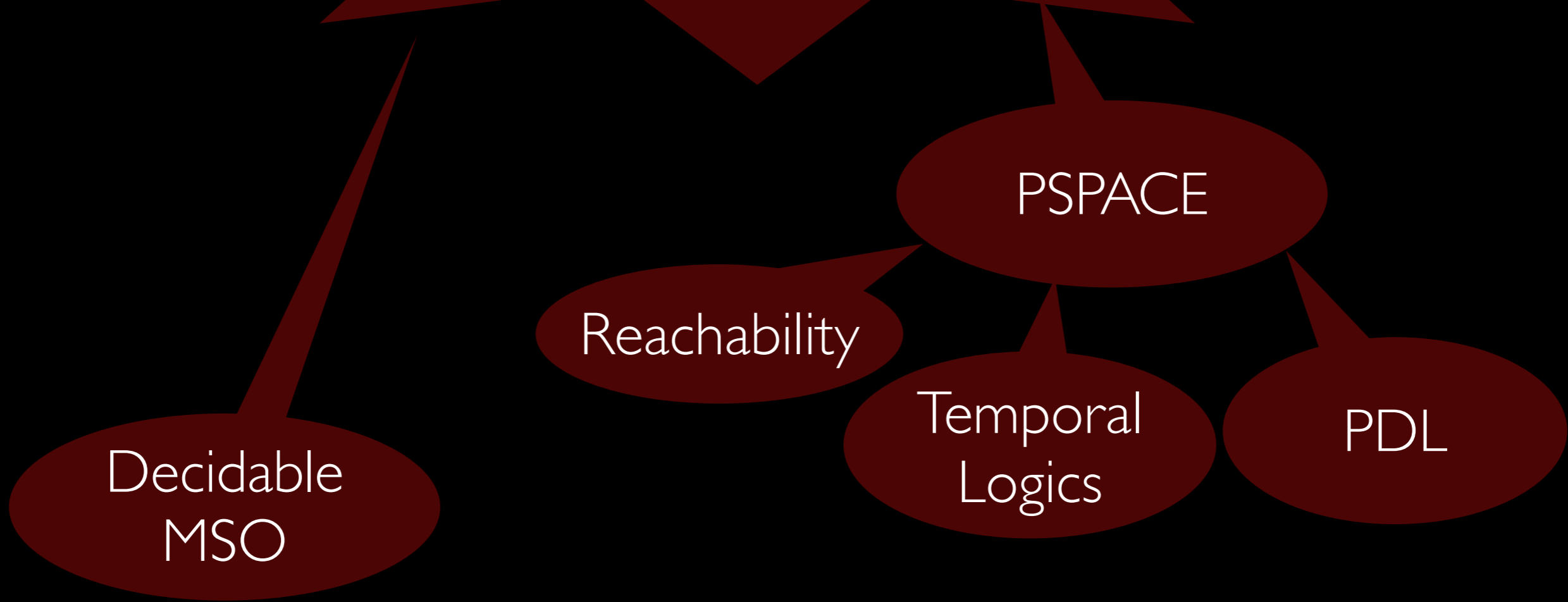
CONTROLLERS FOR BOUNDED PHASE DISTRIBUTED SYSTEMS



- > Collection of local controllers
 - > Communication via piggy-backing
 - > Privacy: Do NOT read states/messages
-
- > System independent
 - > Generic
 - > Deterministic
 - > Finite state

DECIDABILITY OF K BOUNDED PHASE

Polynomial SPLIT-WIDTH



Decidable
MSO

PSPACE

Reachability

Temporal
Logics

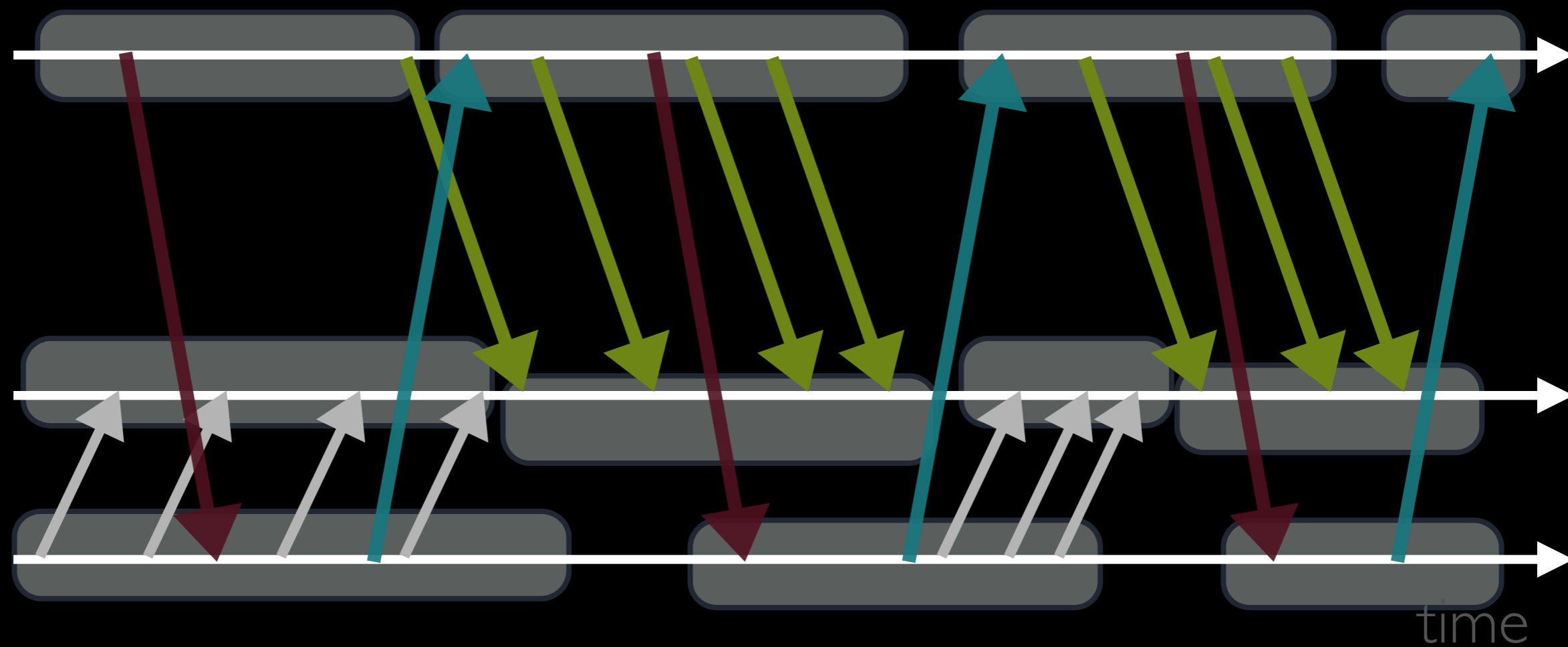
PDL

Polynomial SPLIT-WIDTH

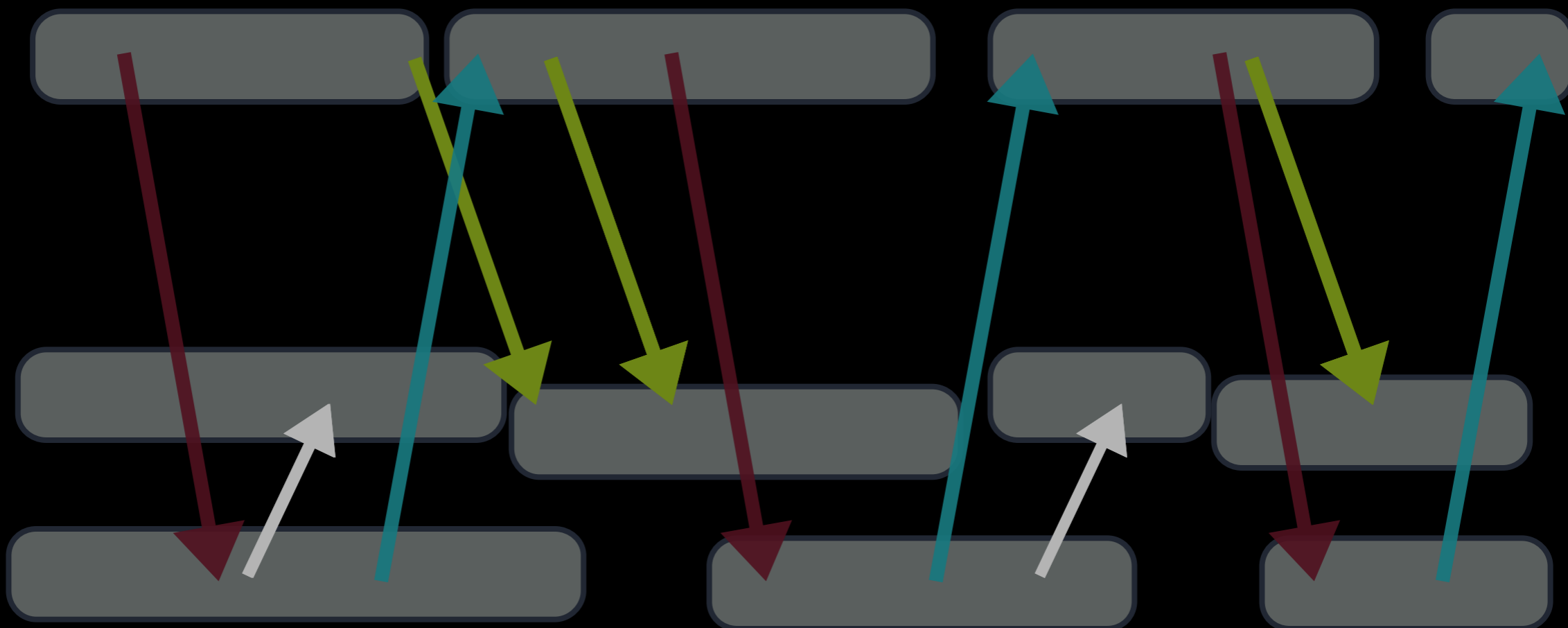
Refine phases to tree-like

bound split-width

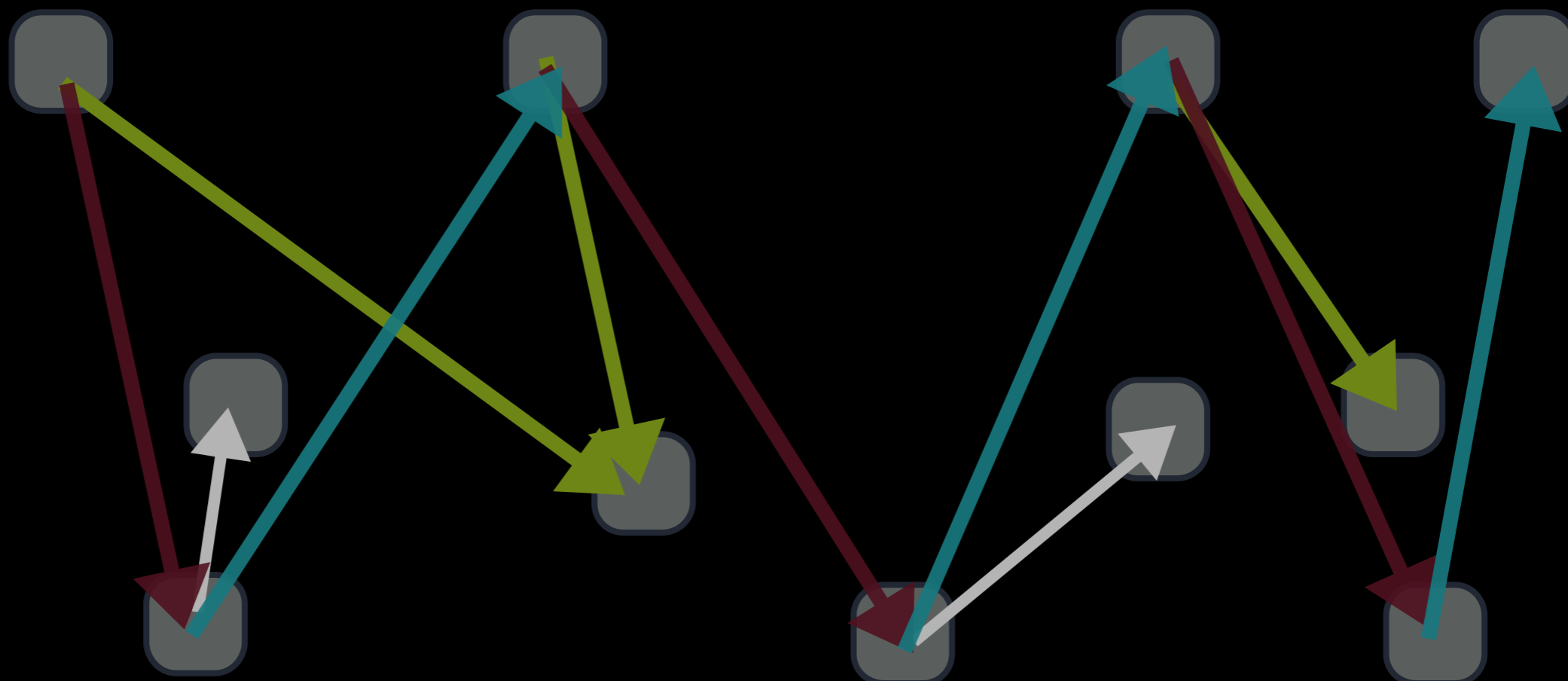
ACYCLIC PHASE DECOMPOSITION



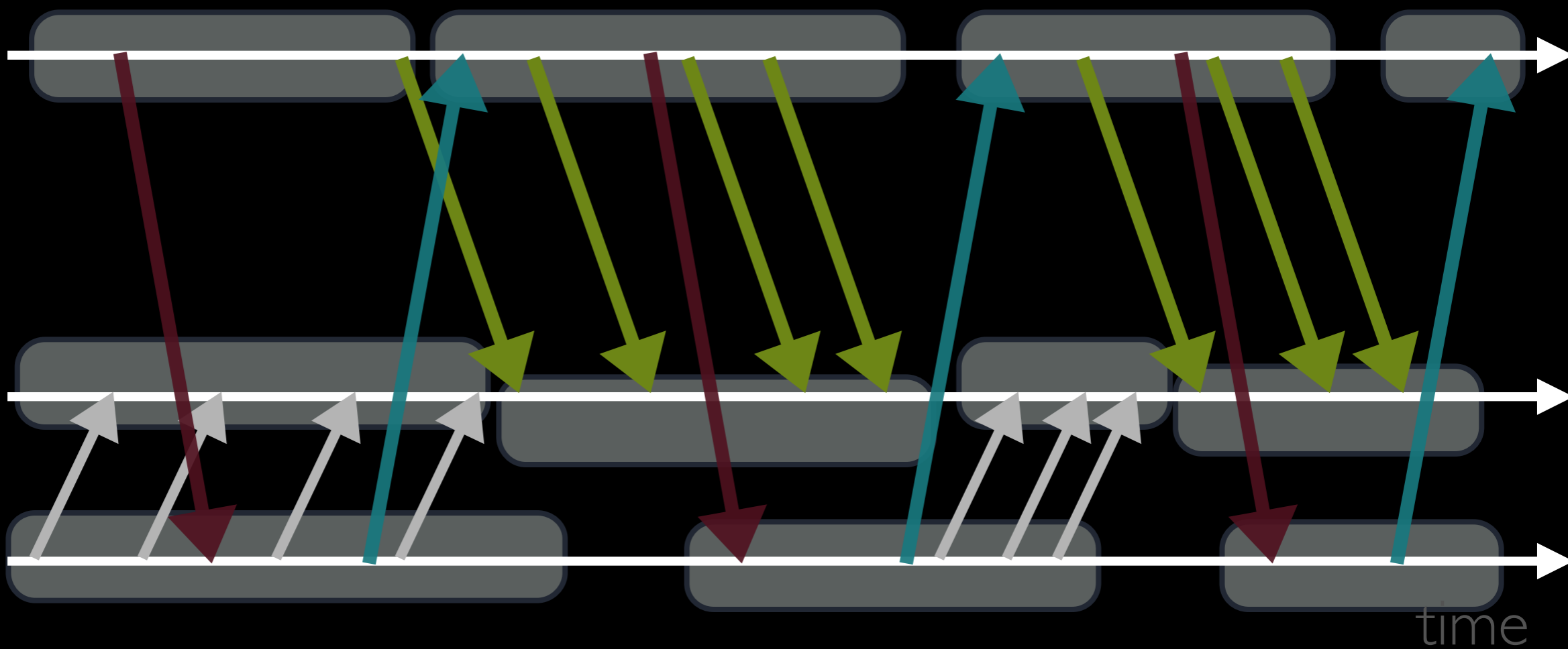
INDUCED GRAPH ON PHASE



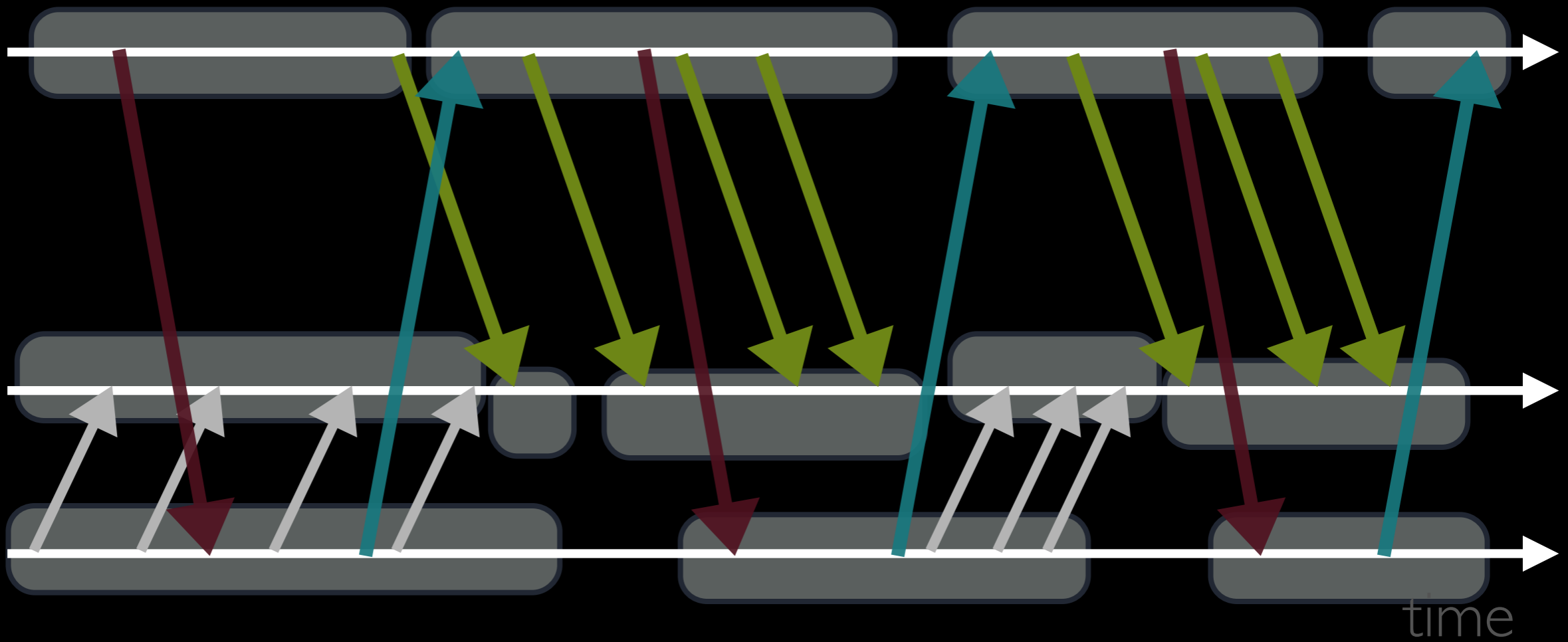
INDUCED GRAPH ON PHASE



PHASE DECOMPOSITION



PHASE DECOMPOSITION

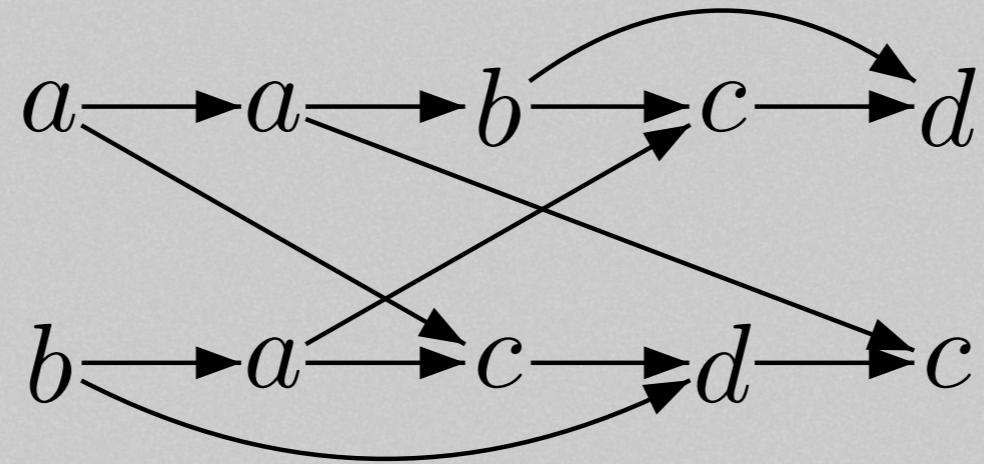


Tree-like

A red, multi-pointed starburst shape with eight points, centered on a black background. The text "Polynomial SPLIT-WIDTH" is written in white, sans-serif font across the center of the starburst.

Polynomial SPLIT-WIDTH

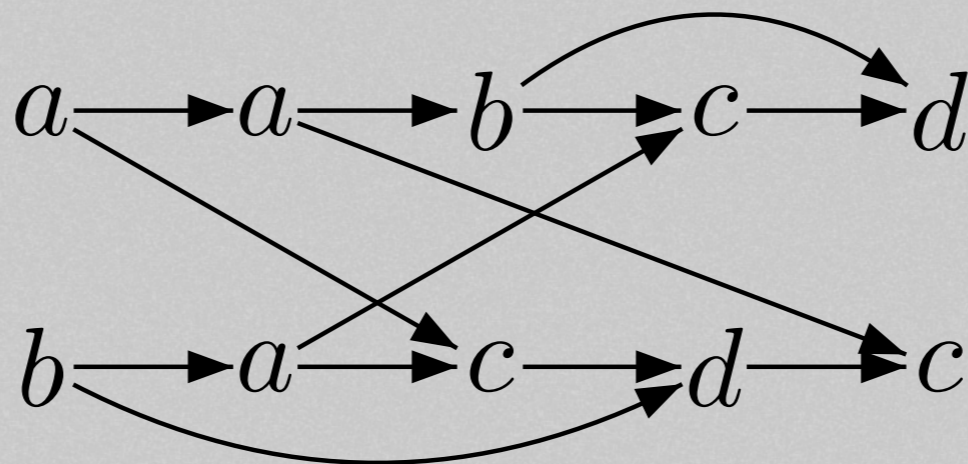
Split-width

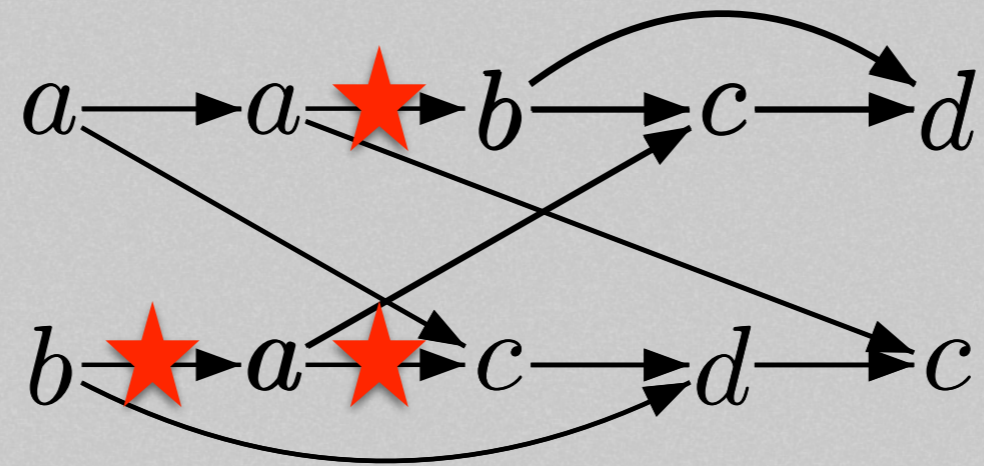


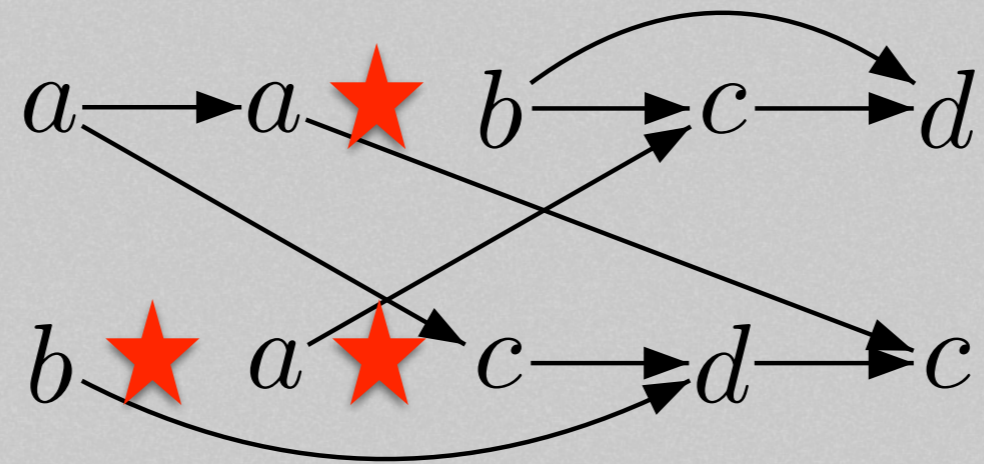
Split-width

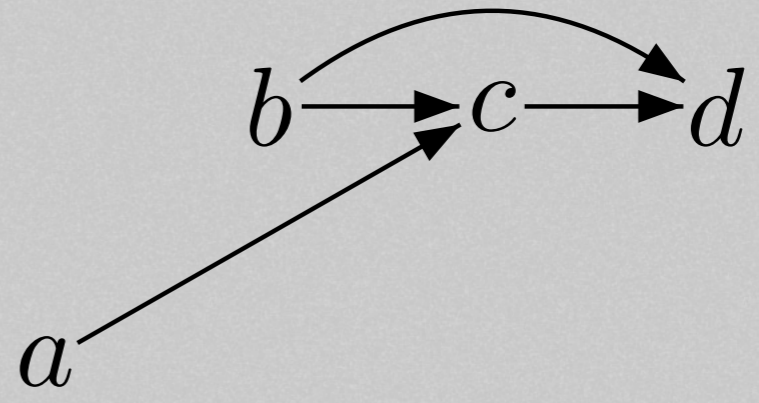
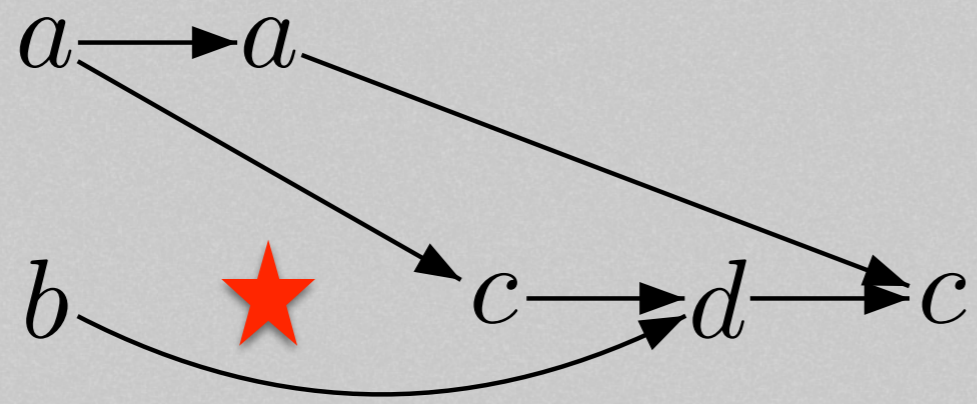


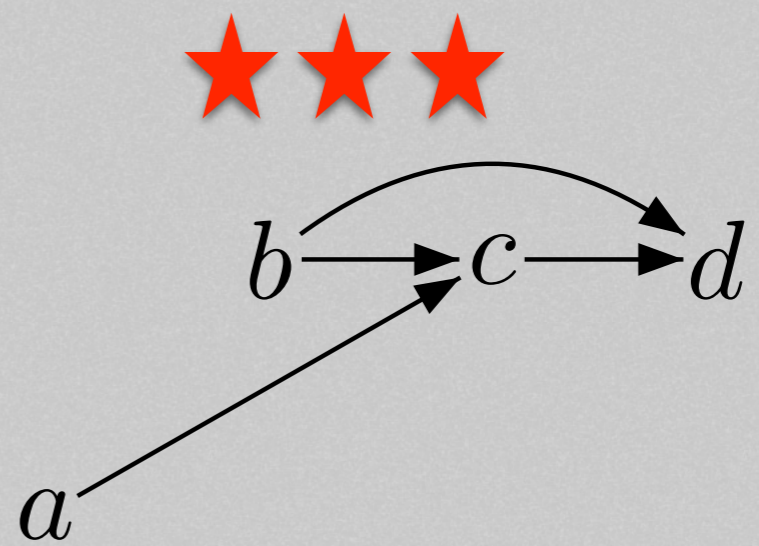
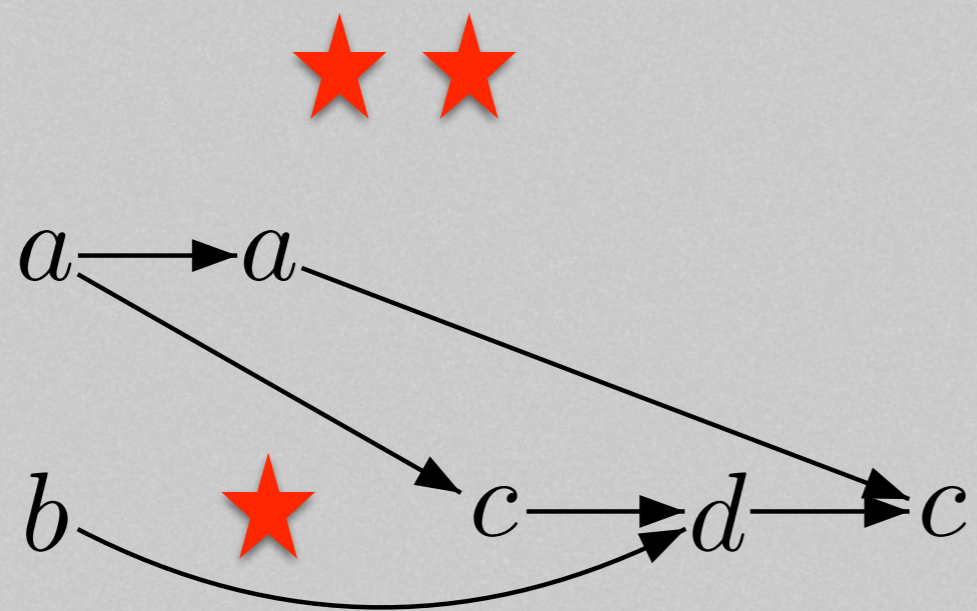
BUDGET

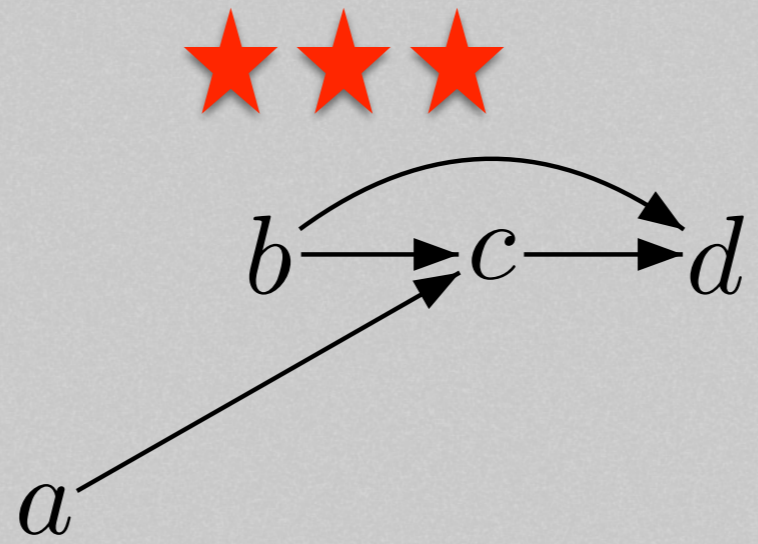


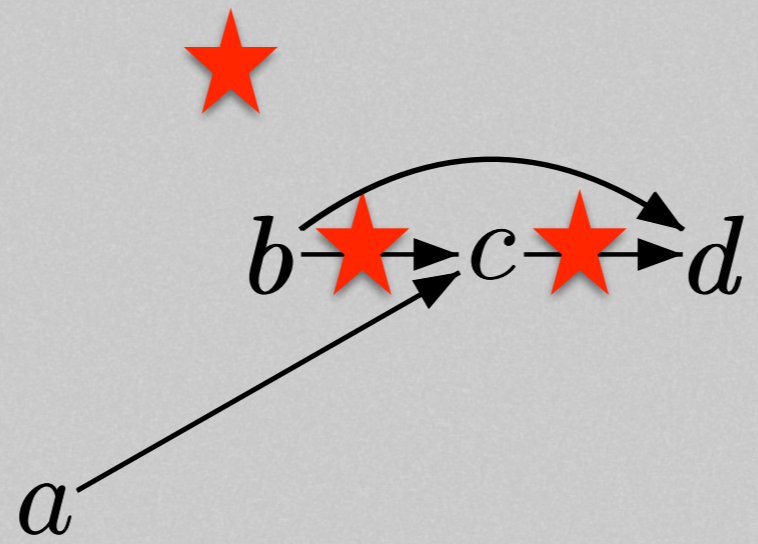


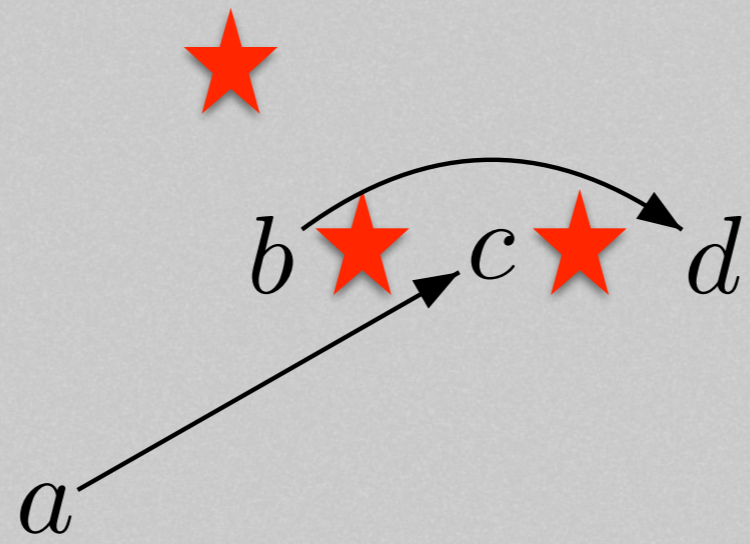


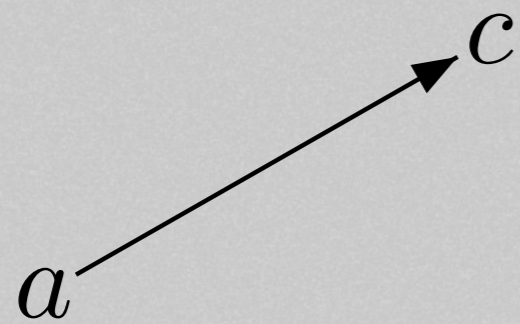


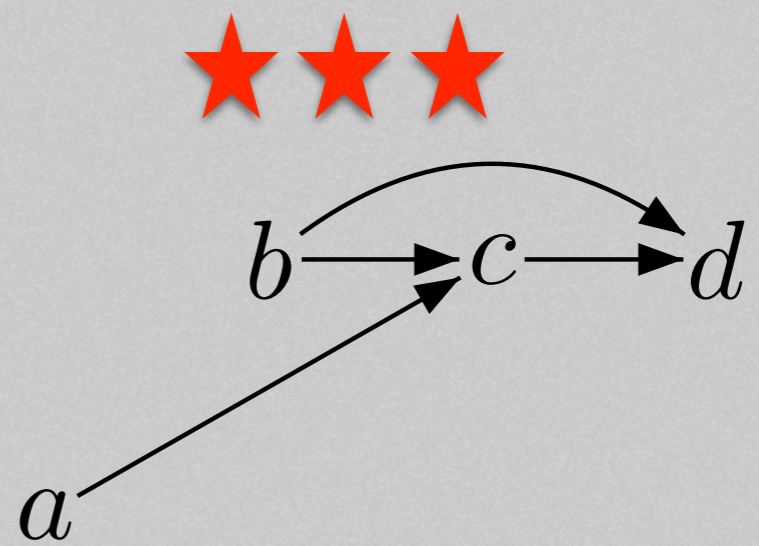
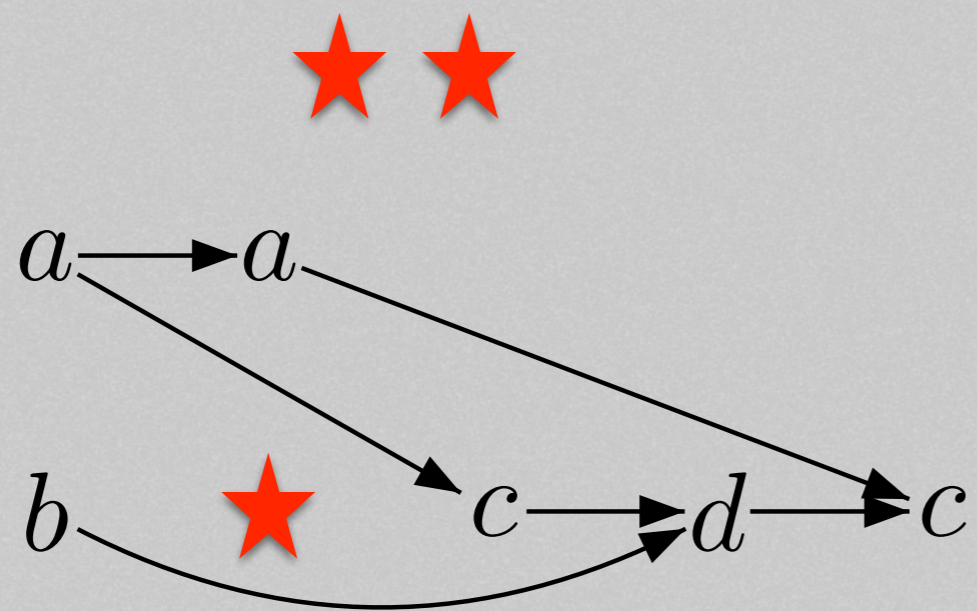


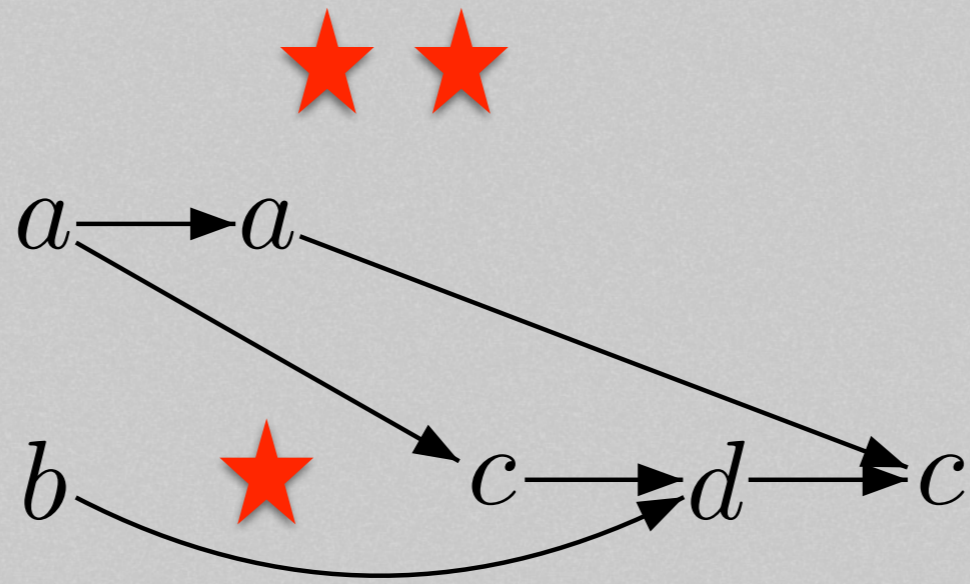


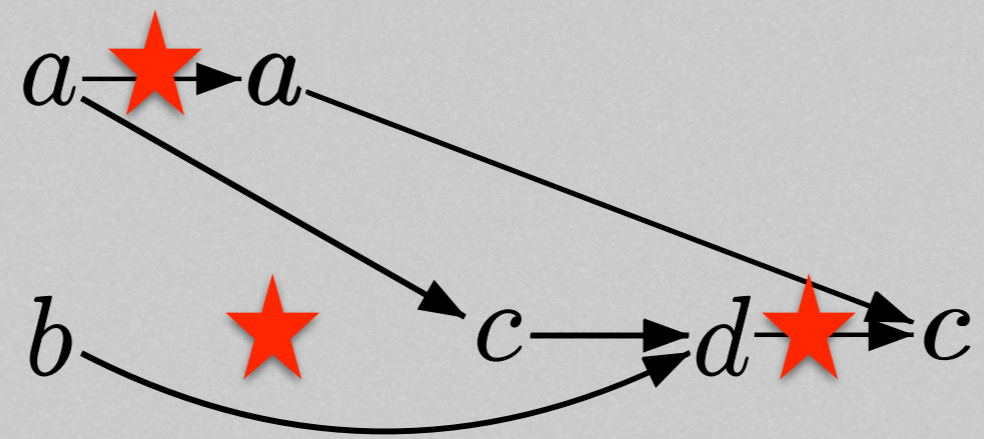


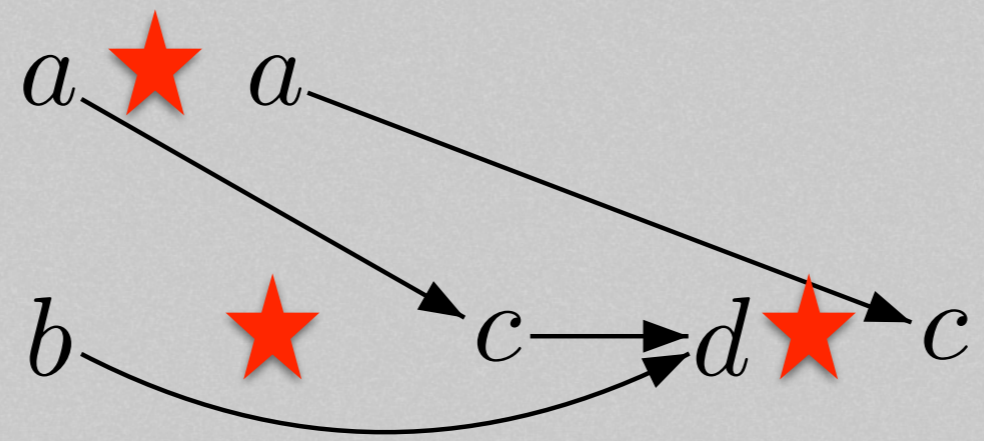


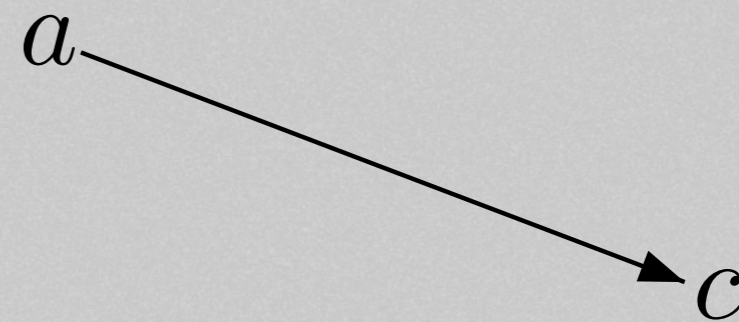
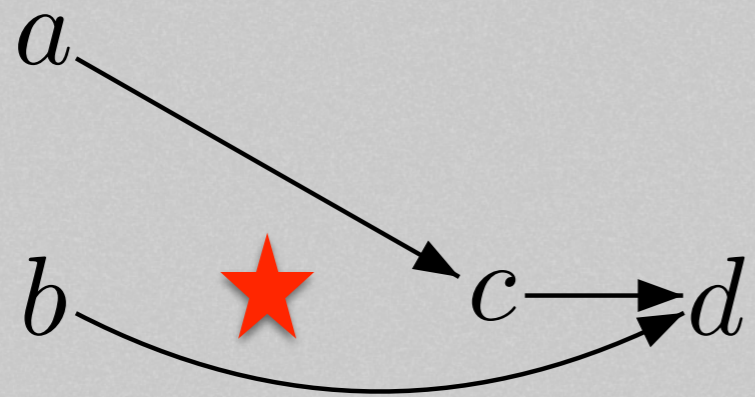


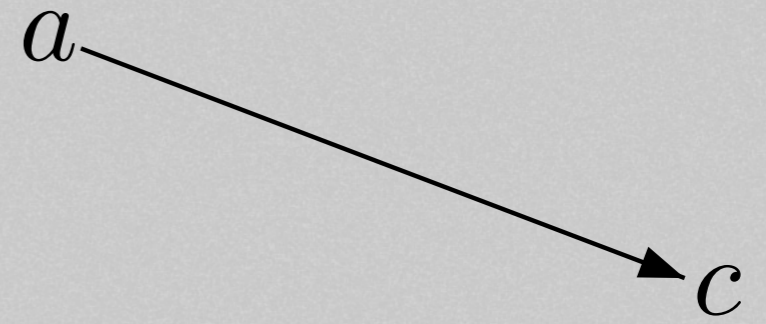
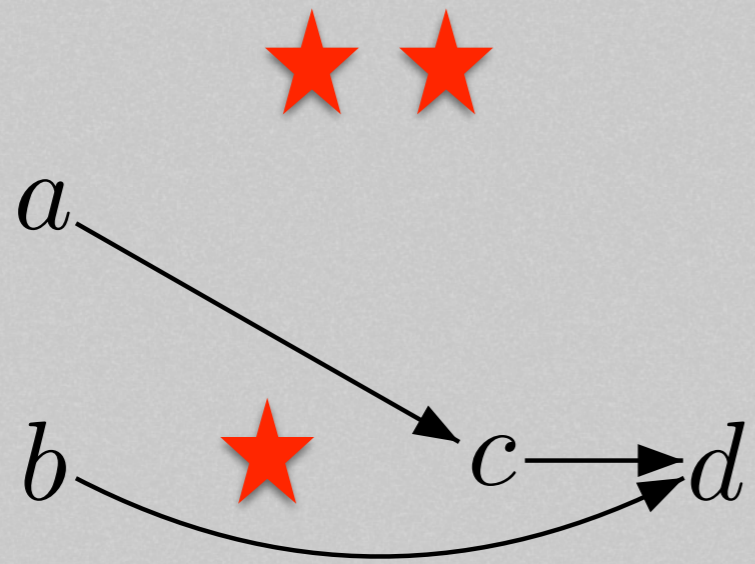


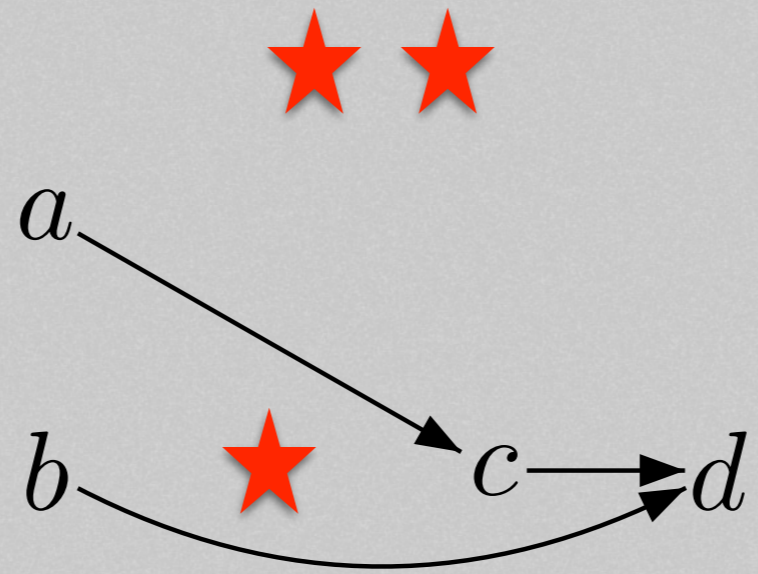


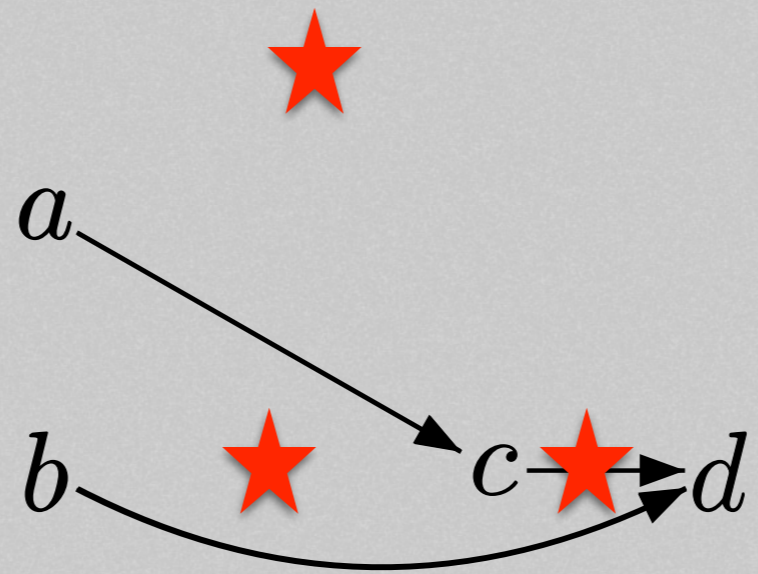


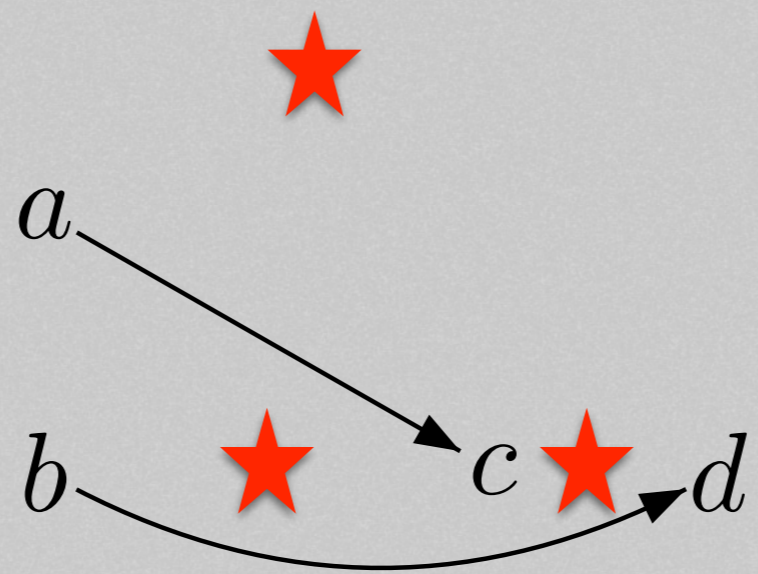


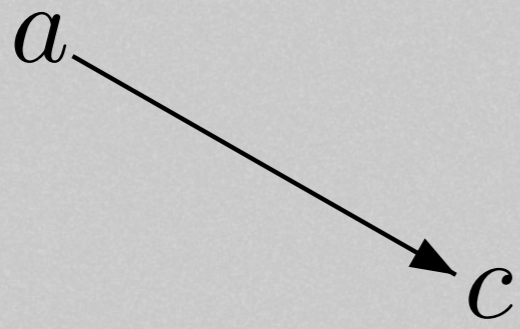




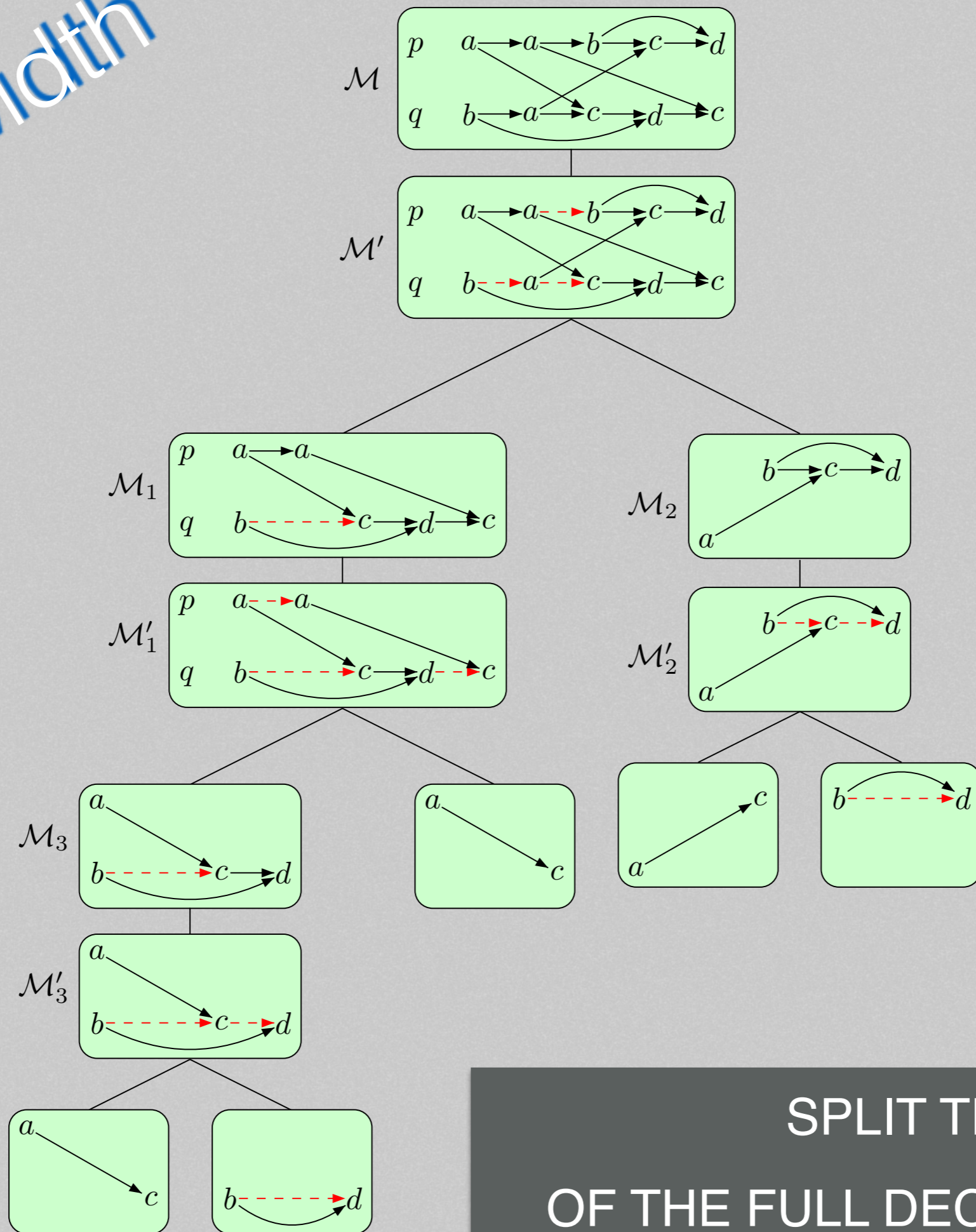




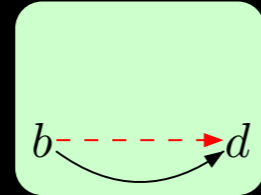
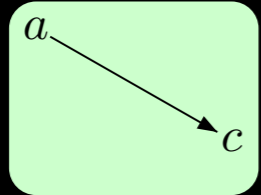
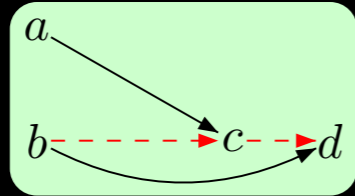
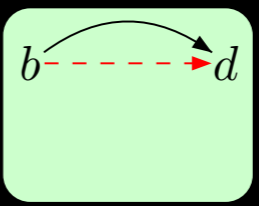
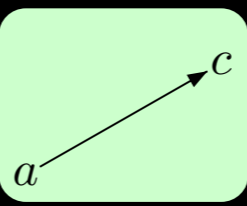
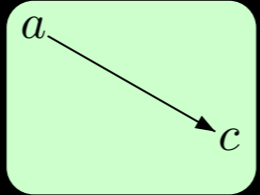
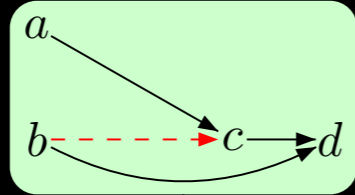
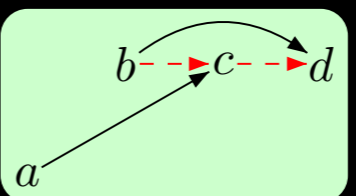
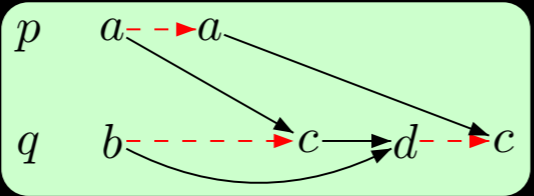
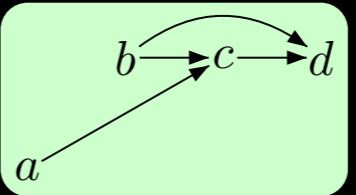
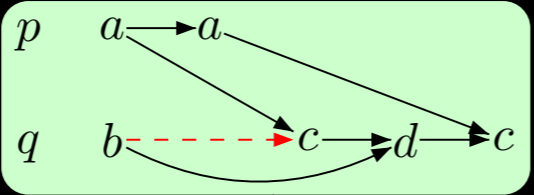
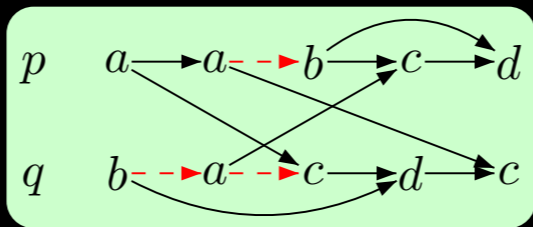
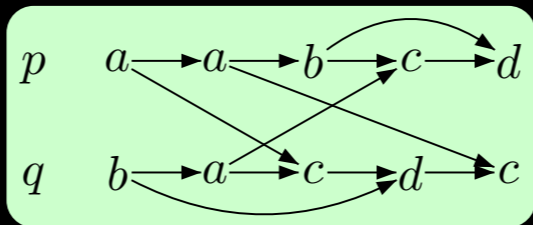




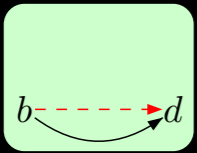
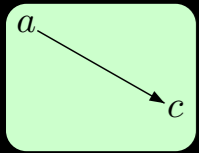
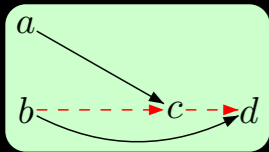
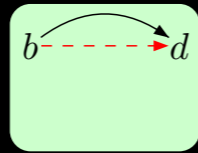
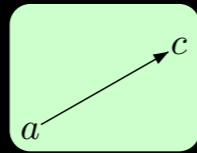
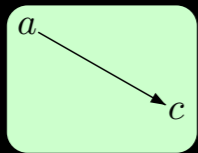
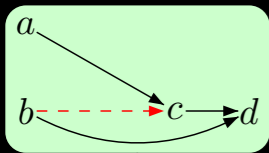
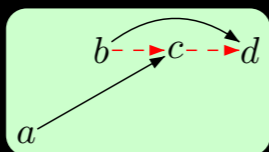
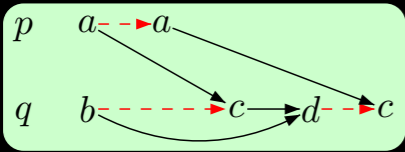
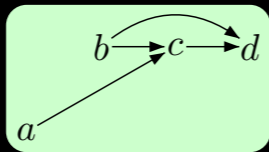
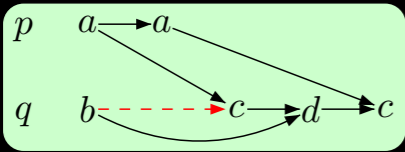
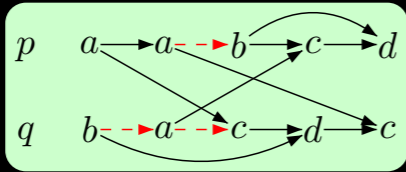
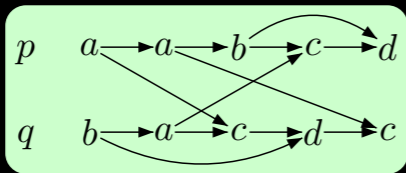
Split-width



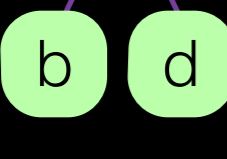
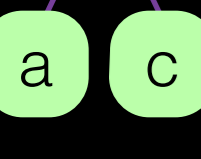
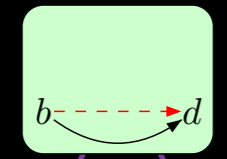
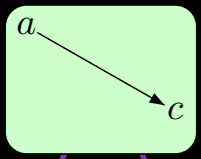
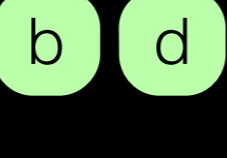
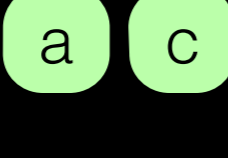
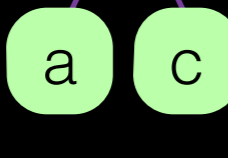
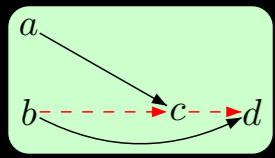
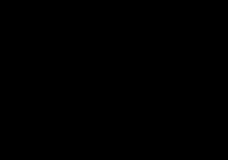
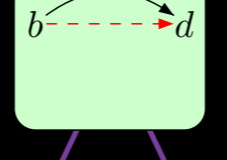
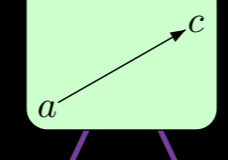
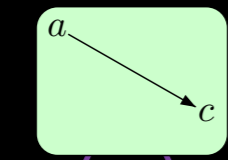
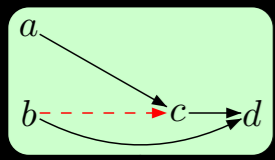
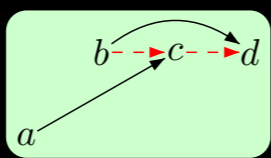
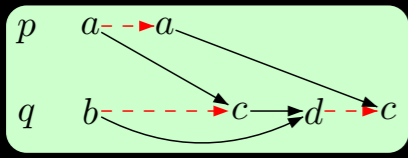
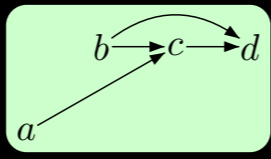
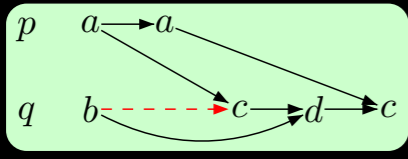
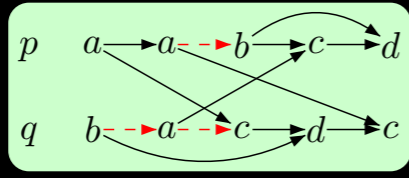
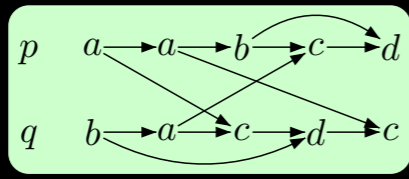
SPLIT TREE
OF THE FULL DECOMPOSITION



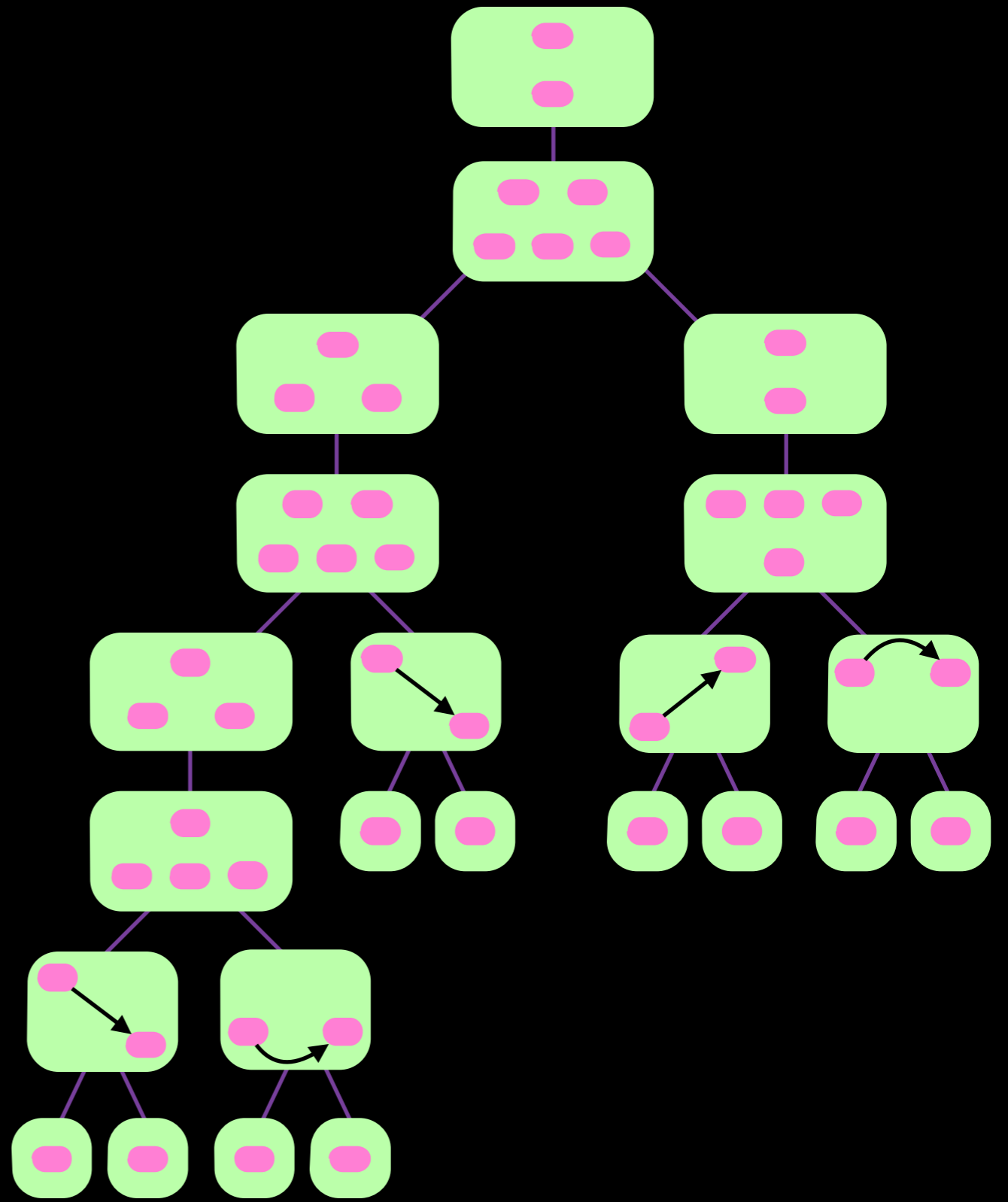
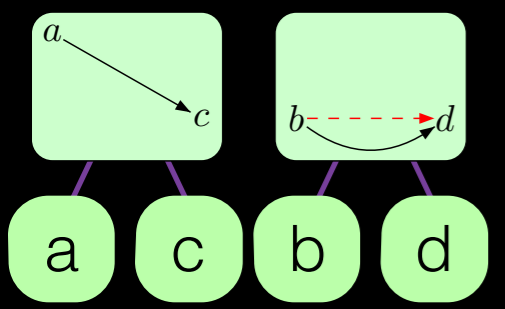
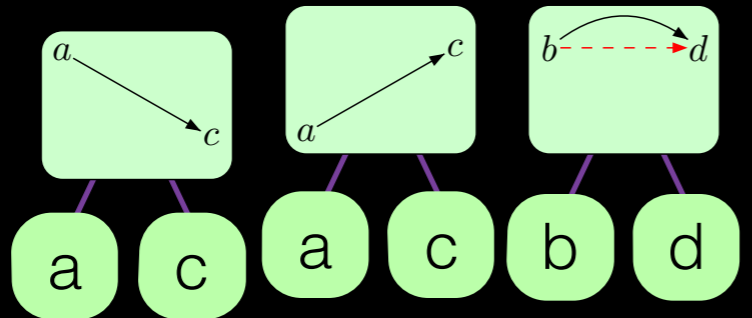
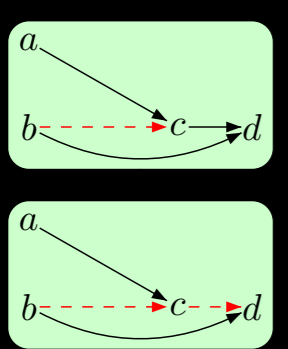
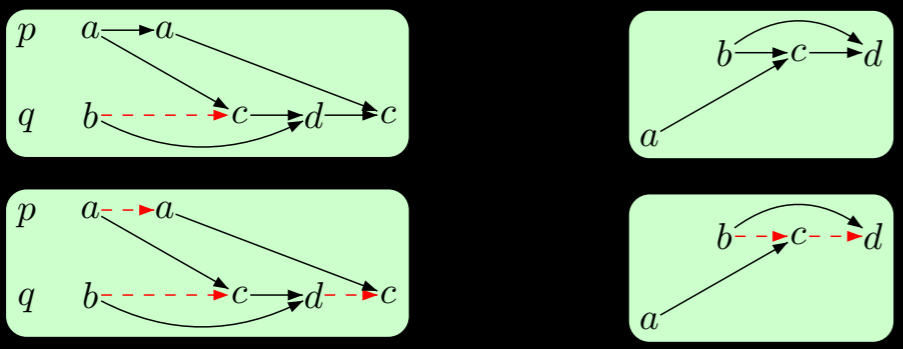
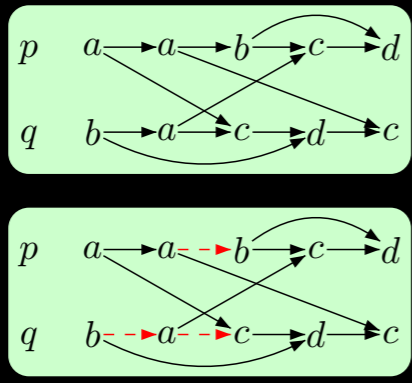
TREE INTERPRETATION



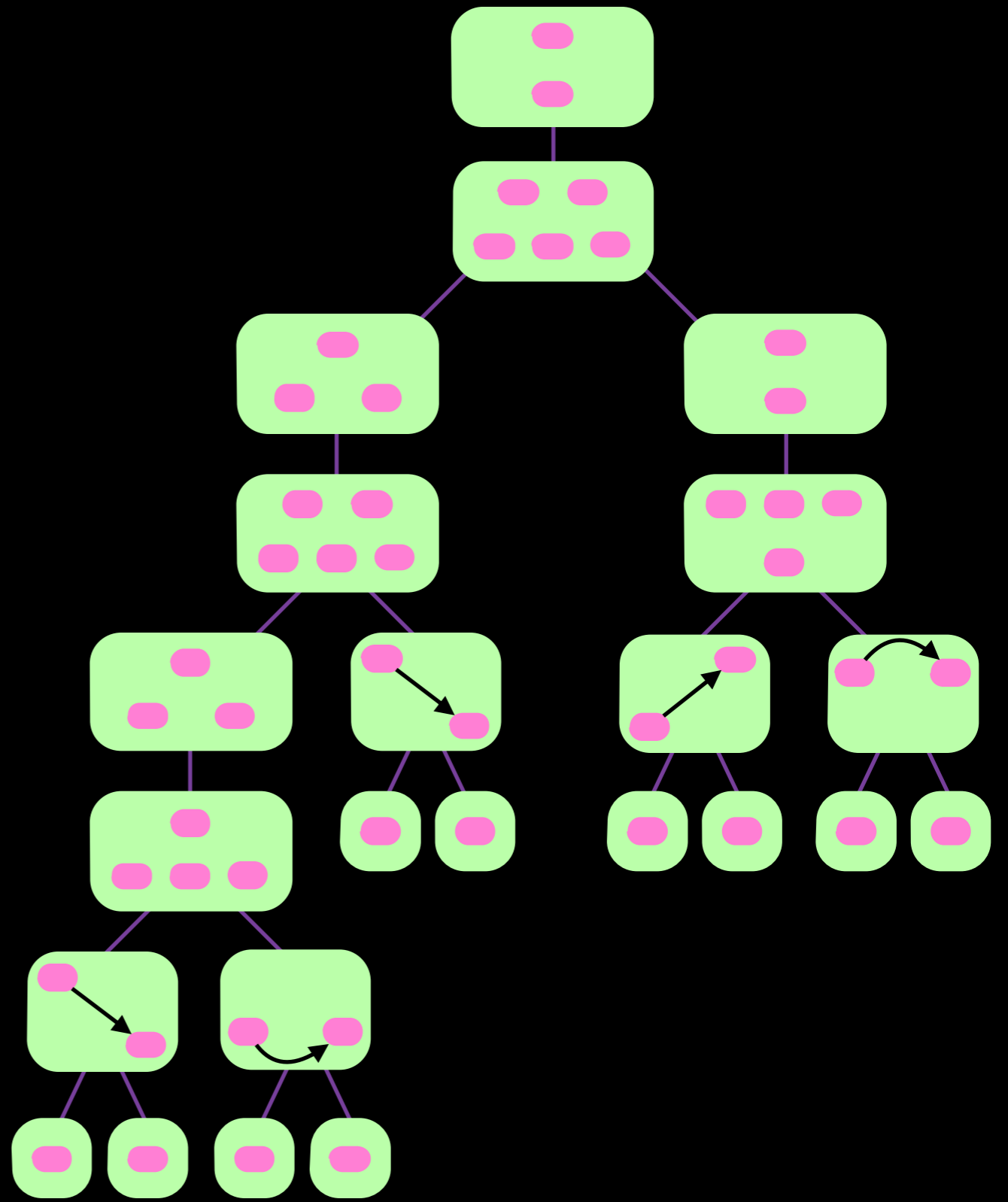
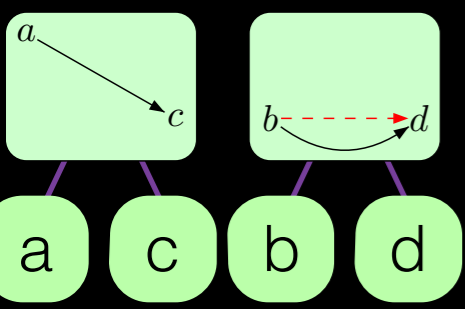
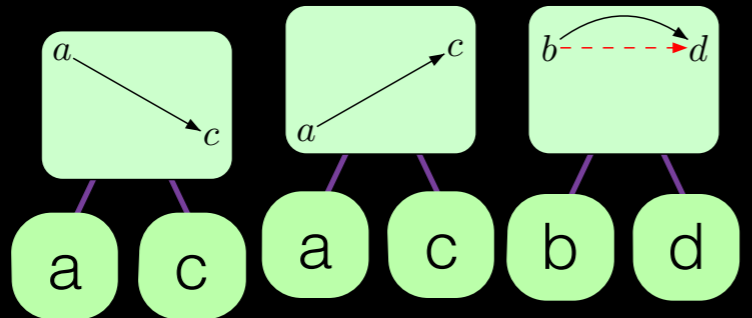
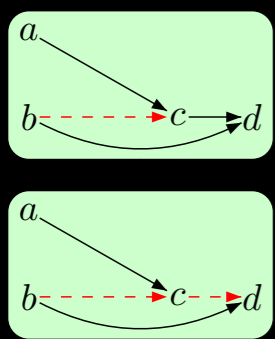
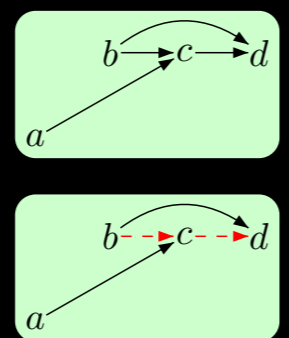
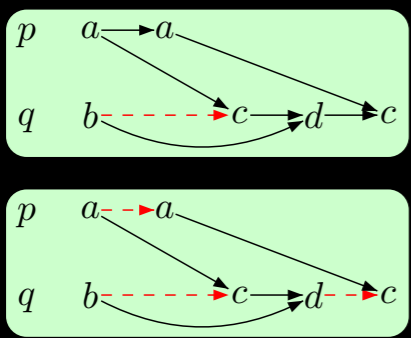
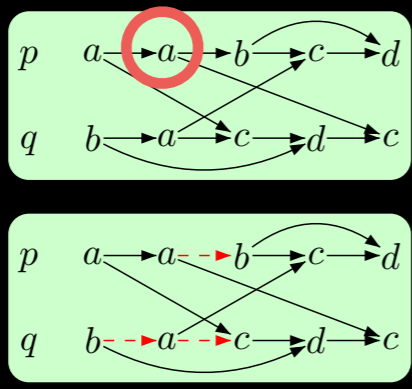
TREE INTERPRETATION



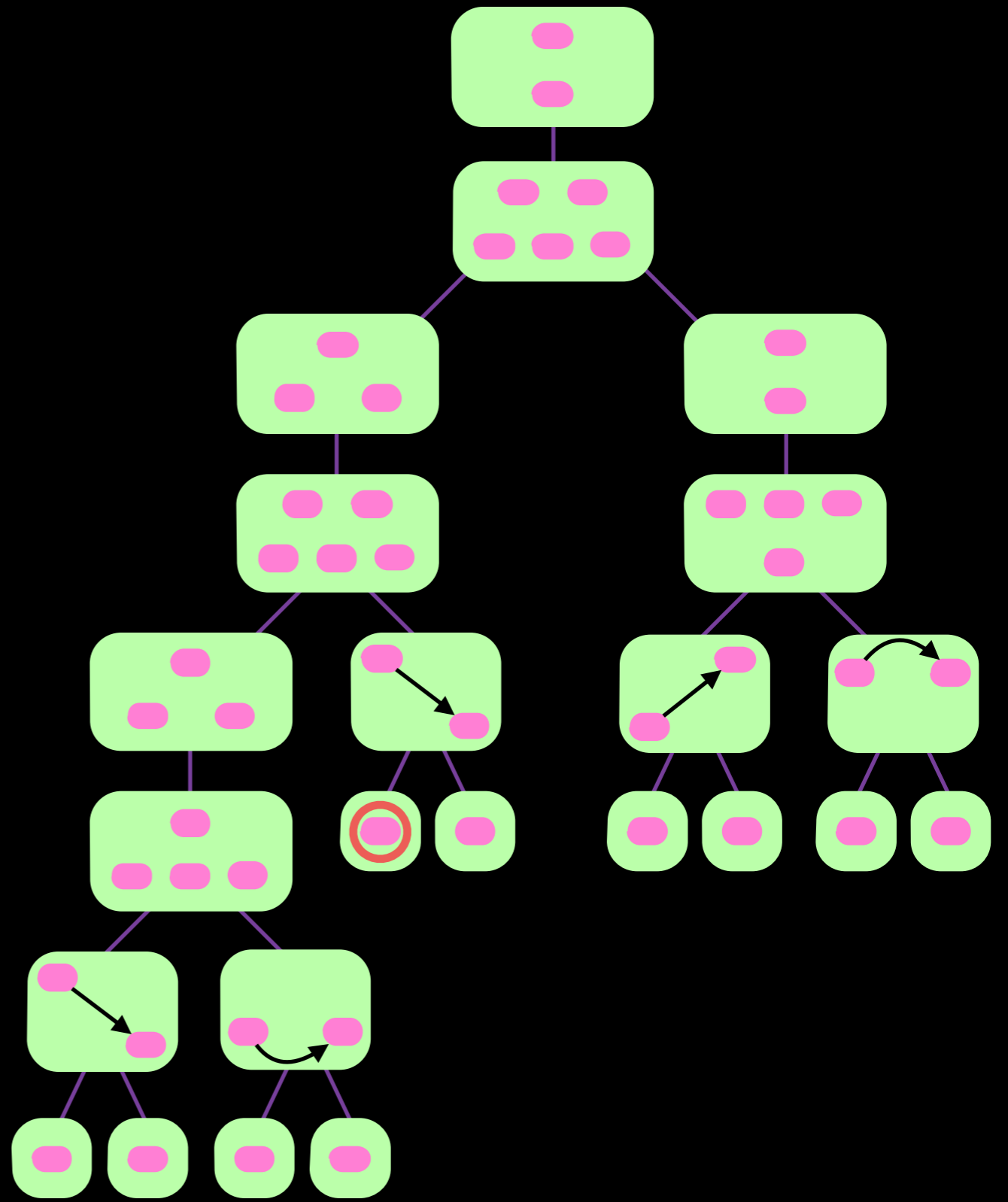
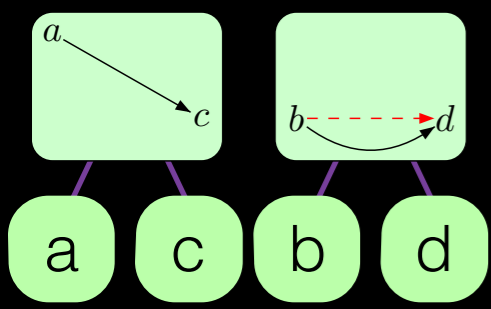
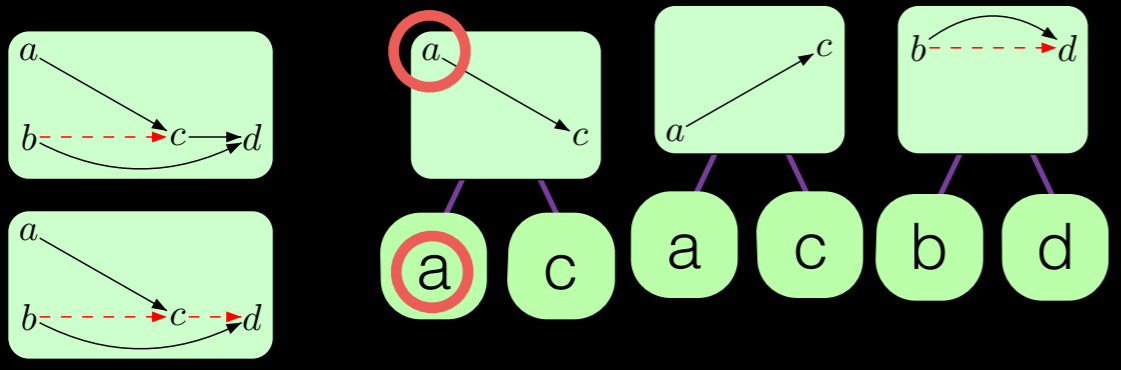
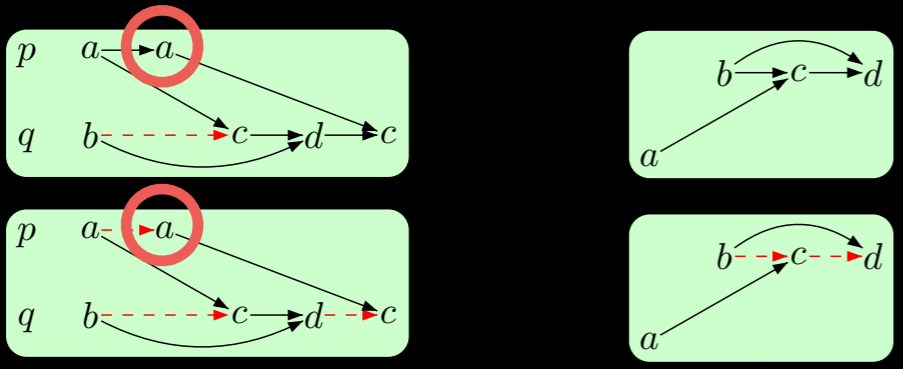
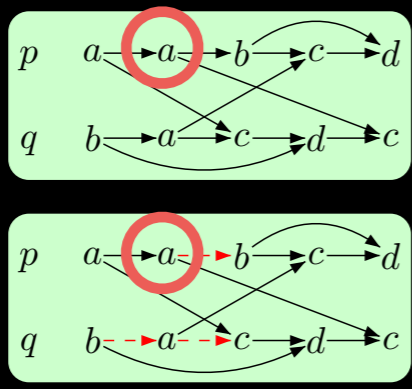
TREE INTERPRETATION



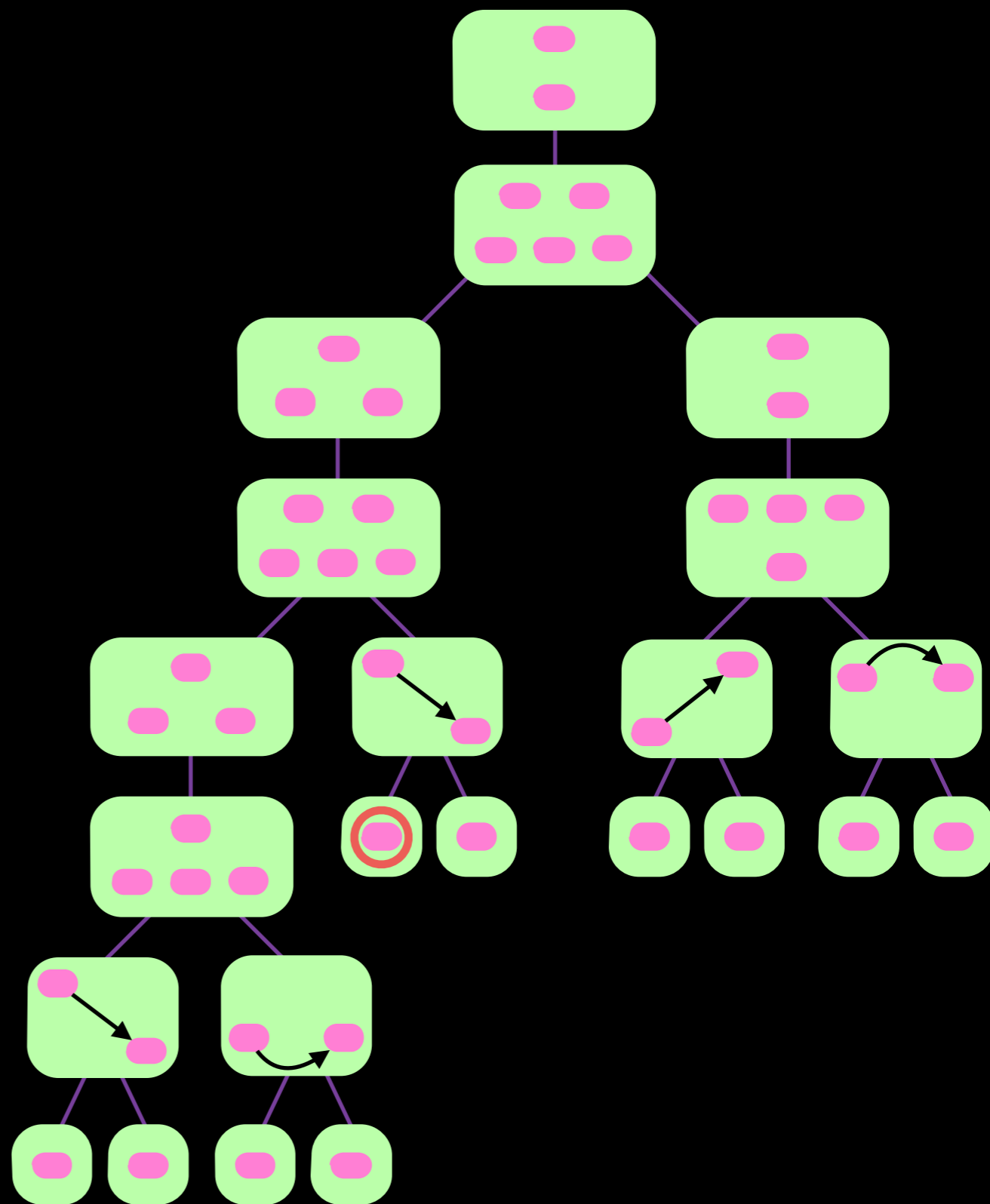
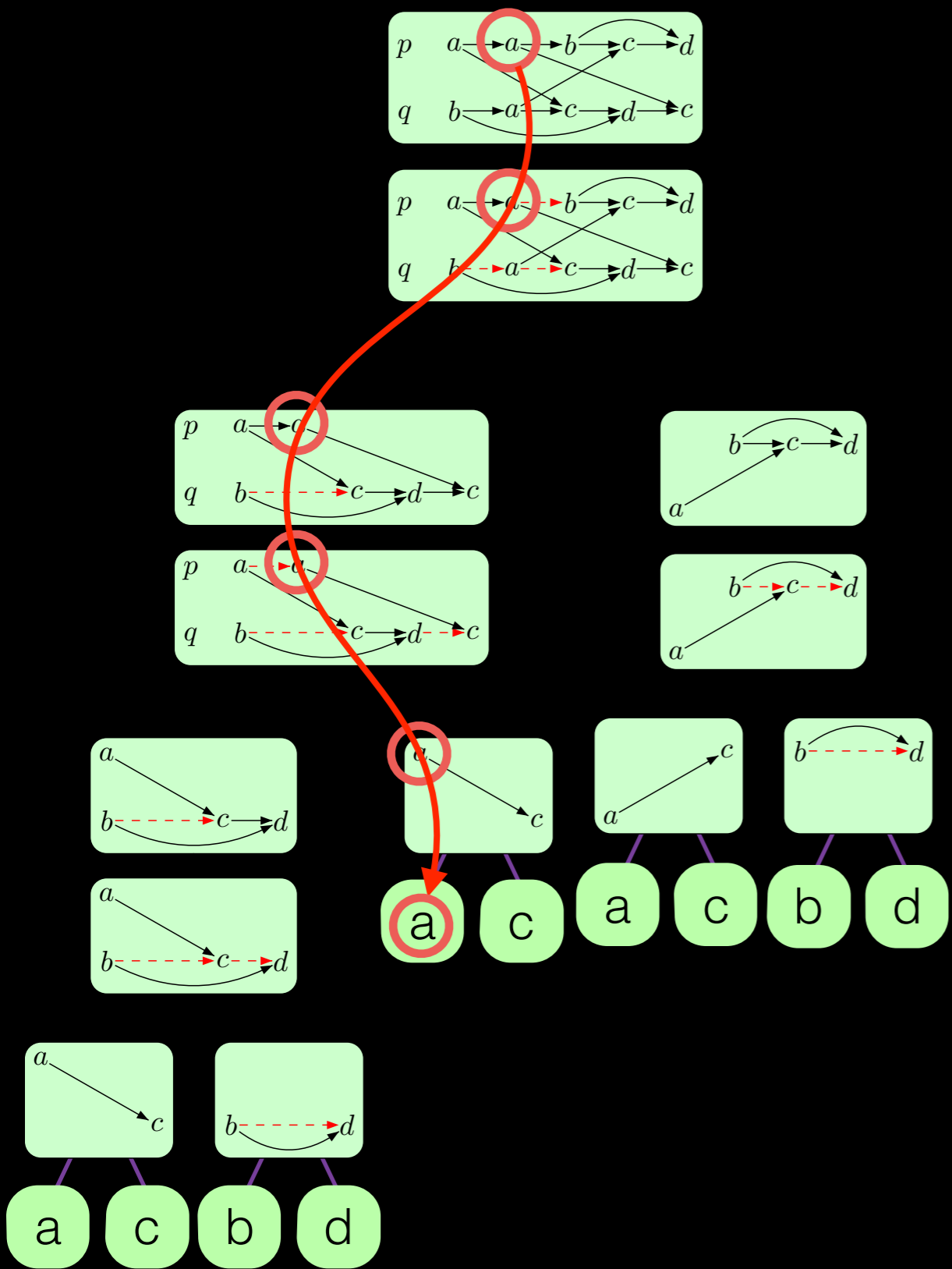
TREE INTERPRETATION



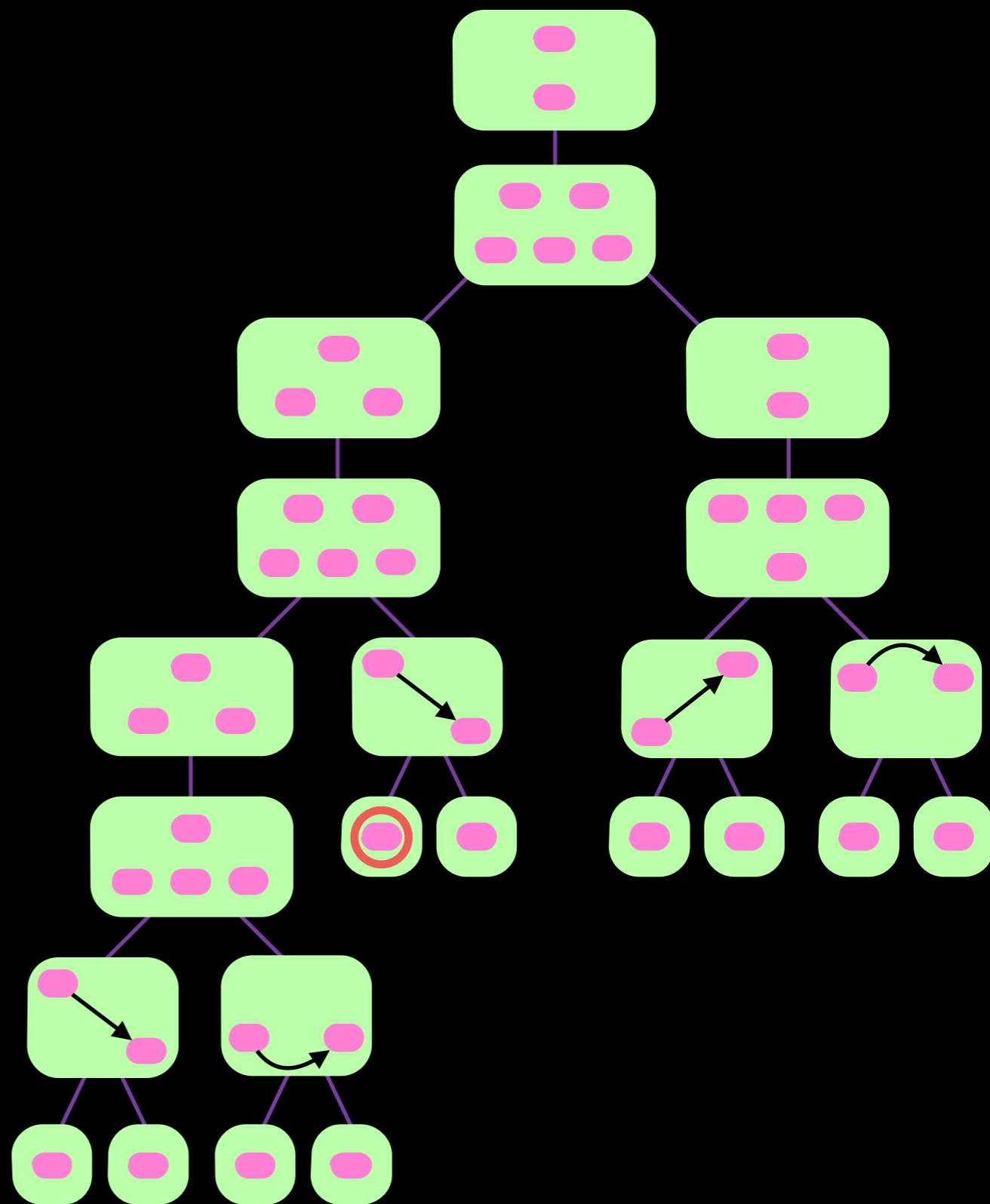
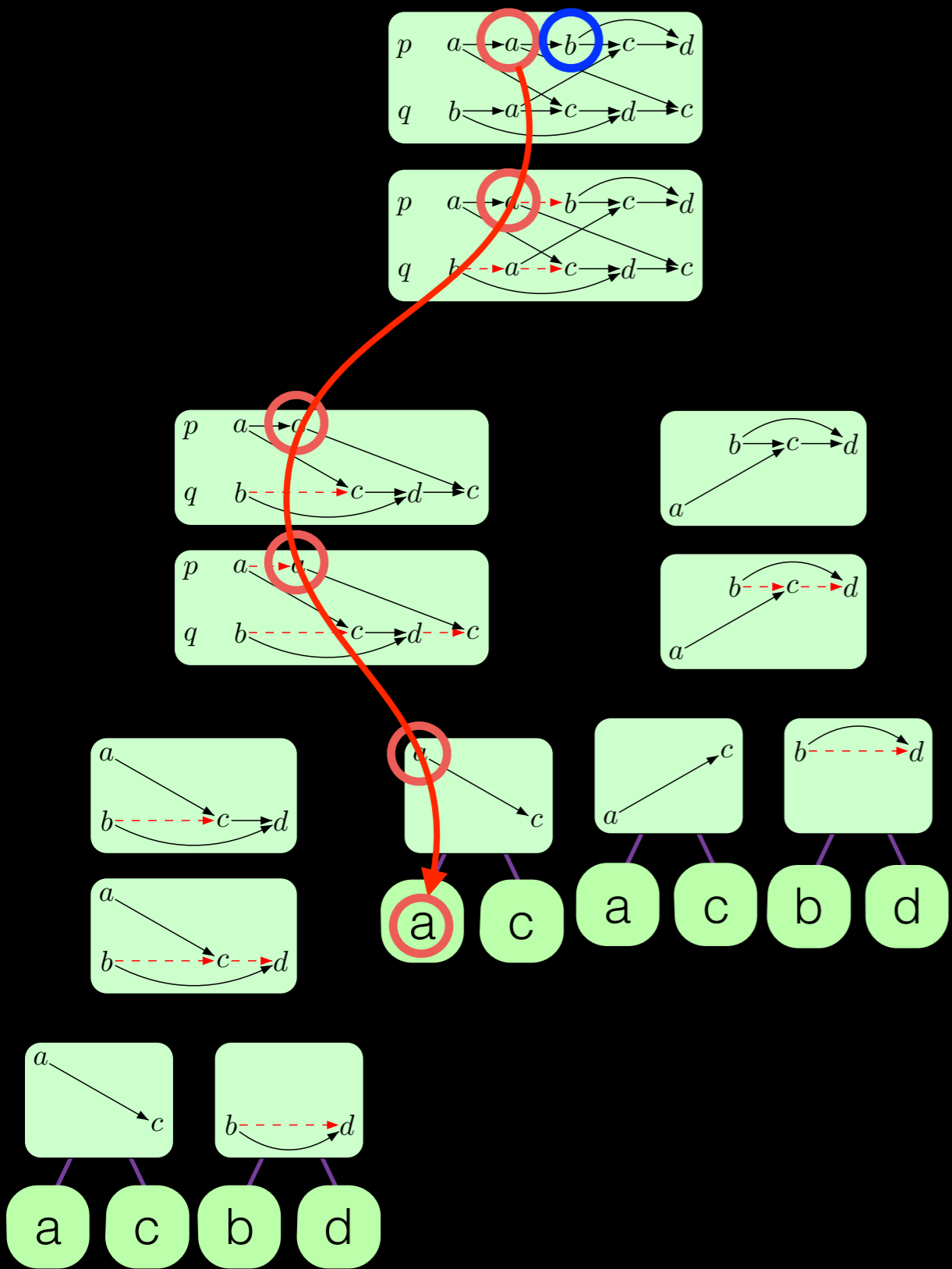
TREE INTERPRETATION



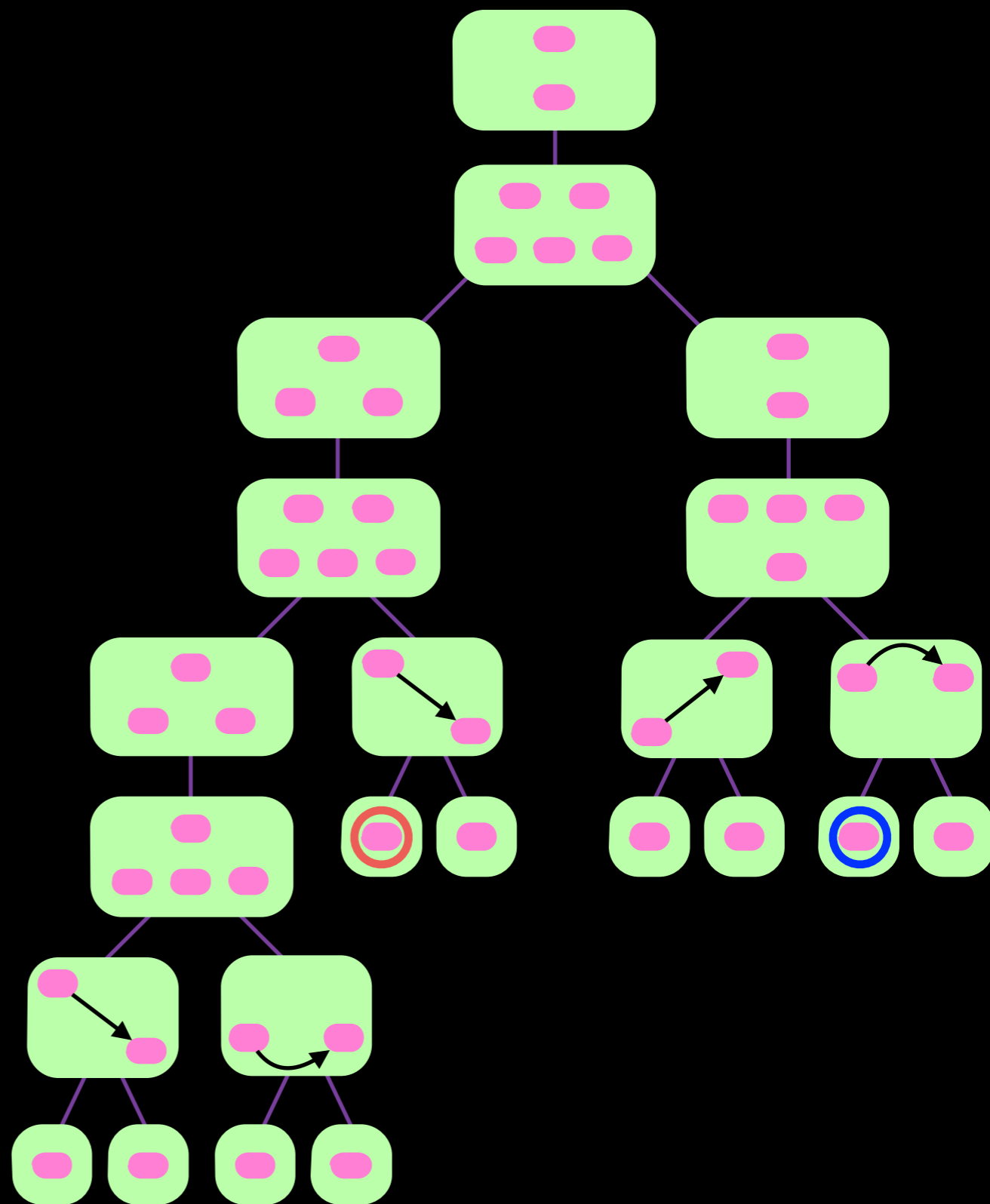
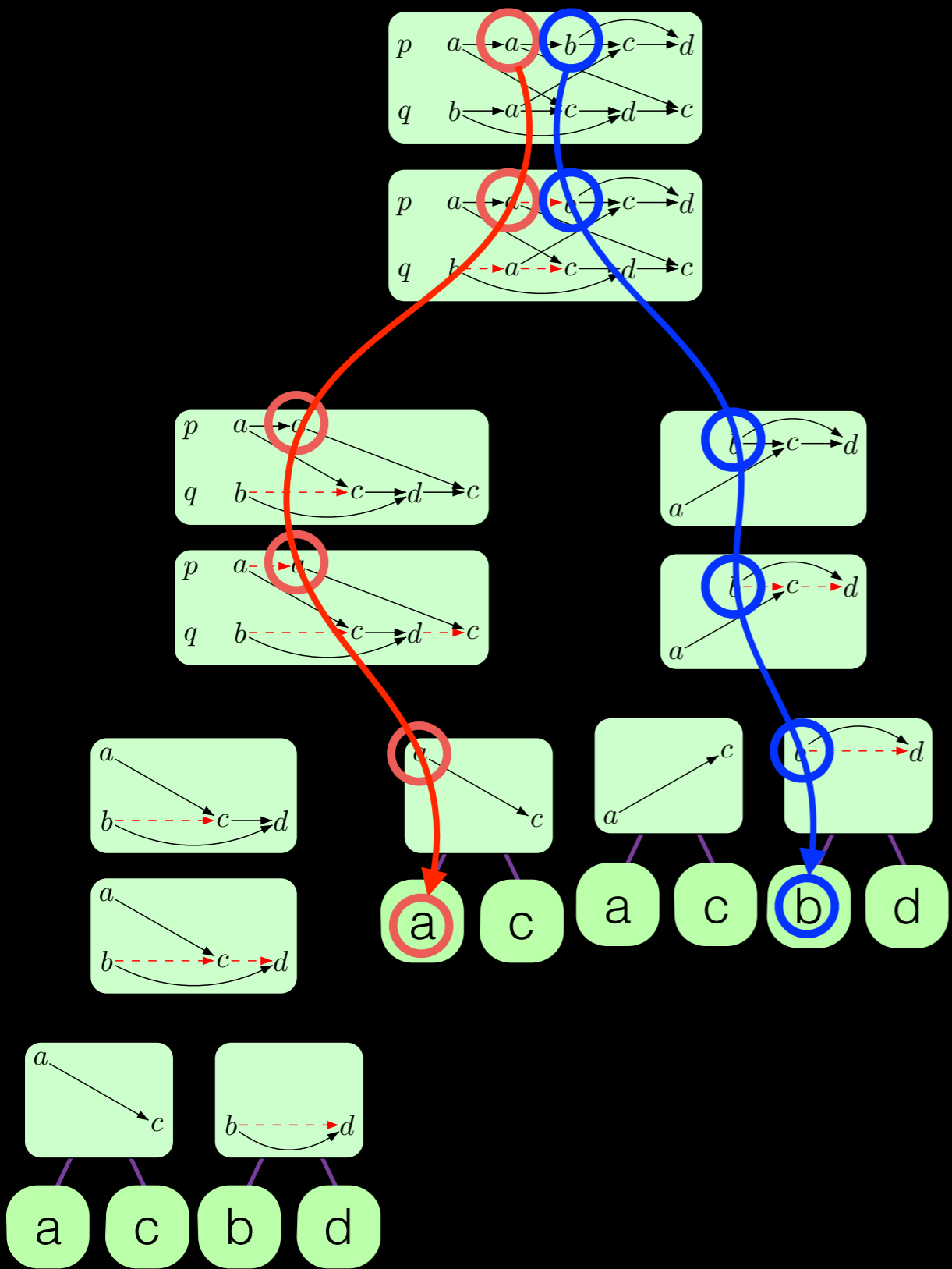
TREE INTERPRETATION



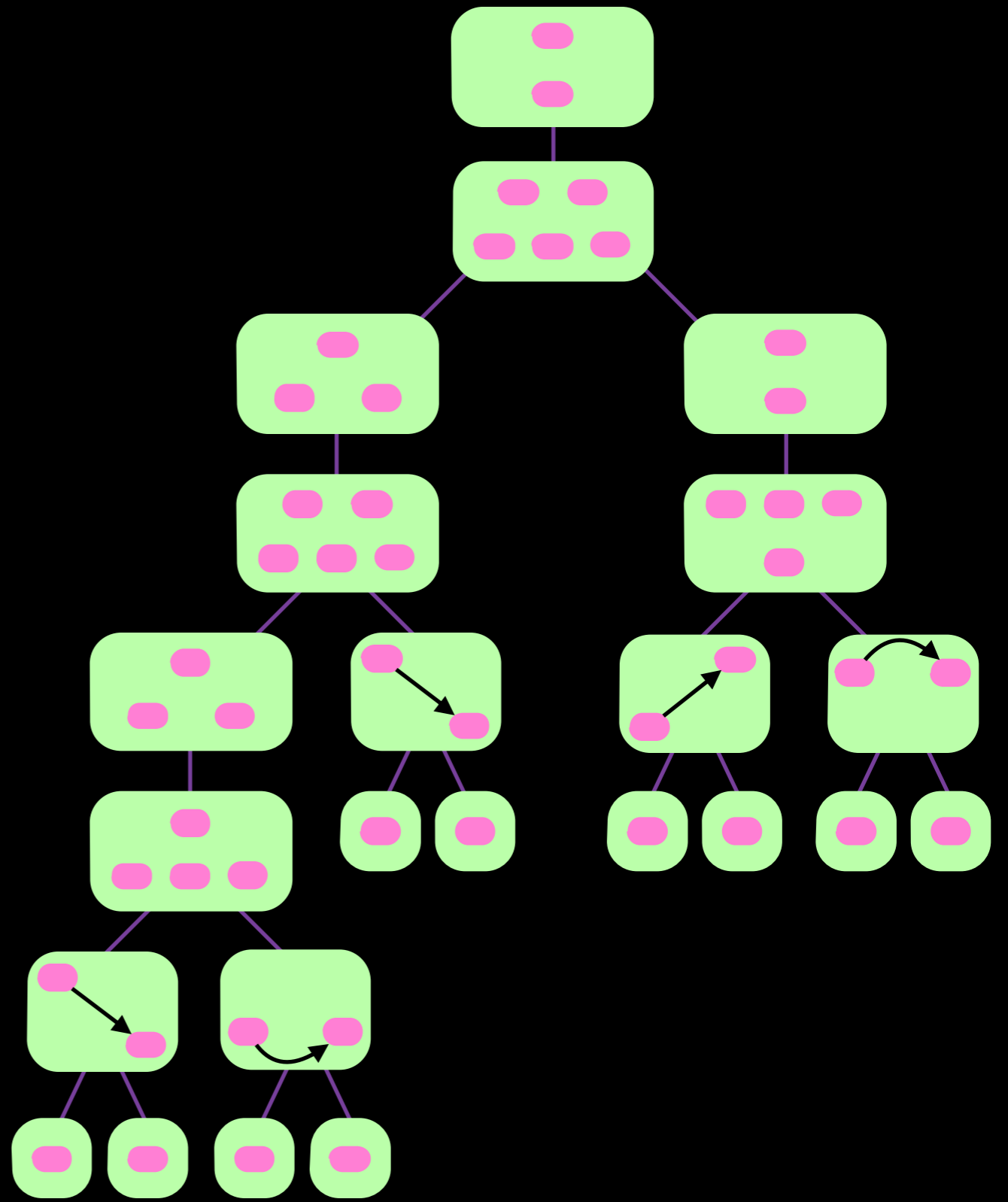
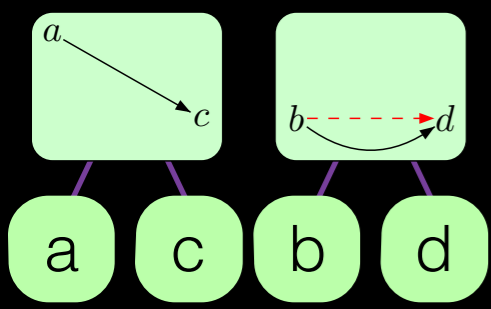
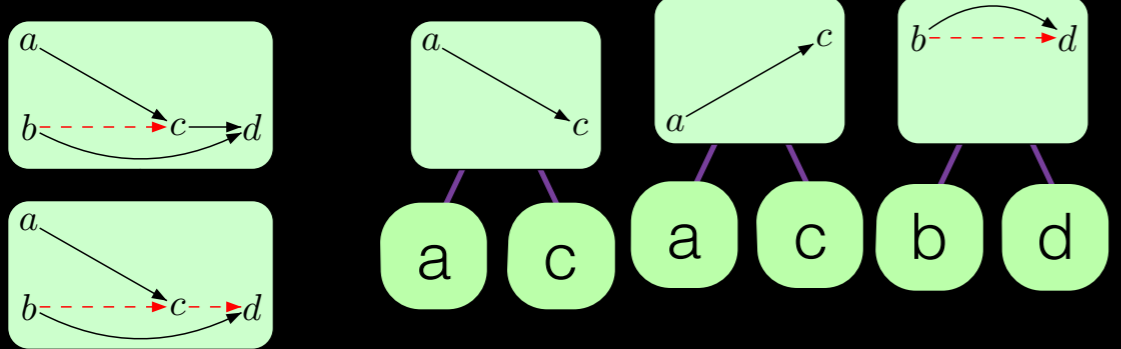
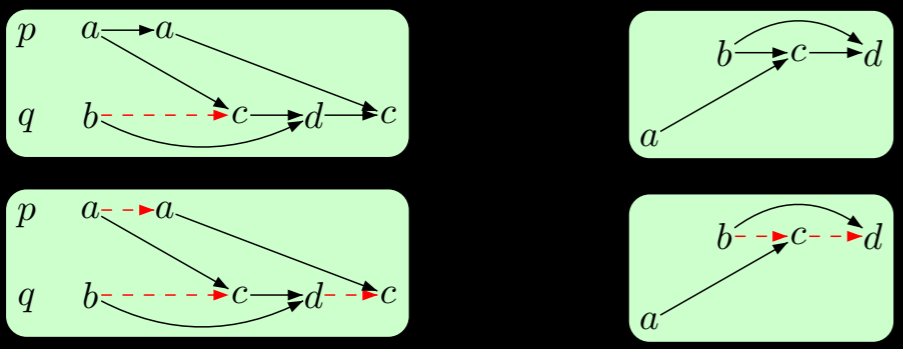
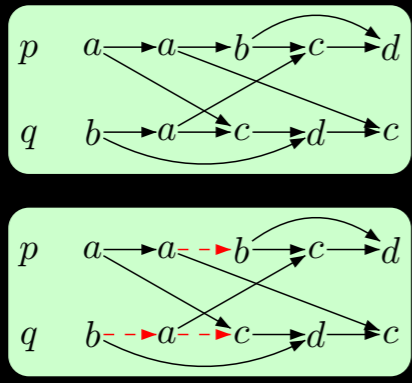
TREE INTERPRETATION



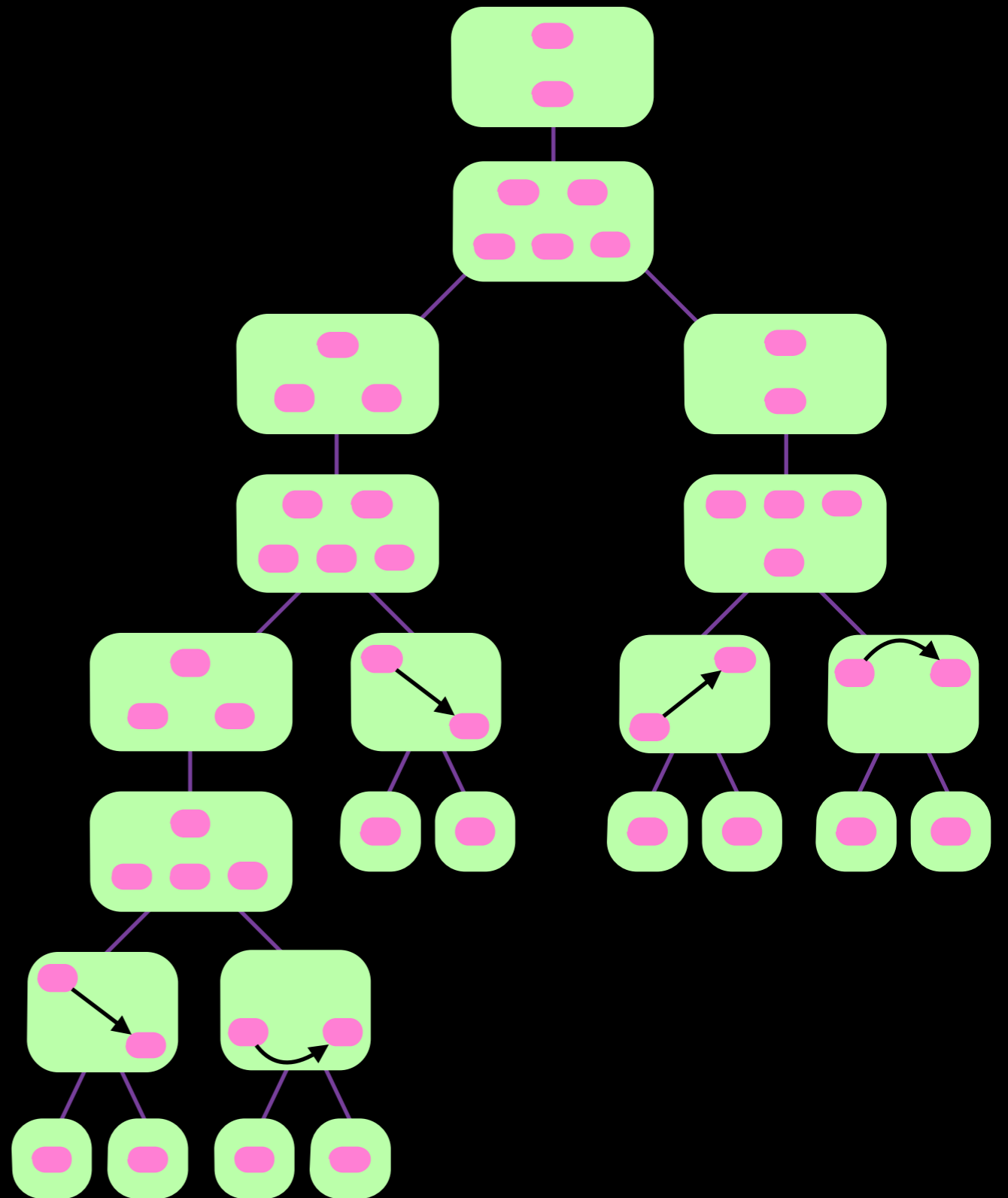
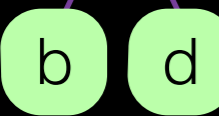
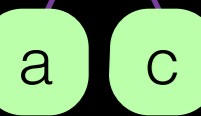
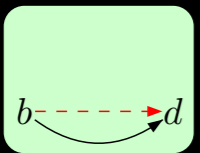
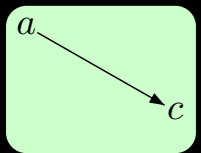
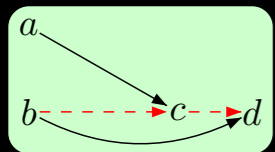
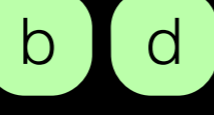
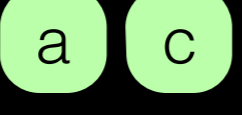
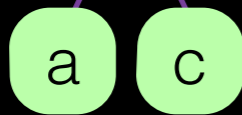
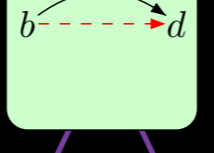
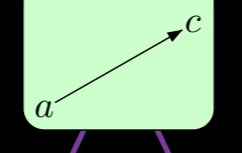
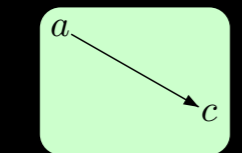
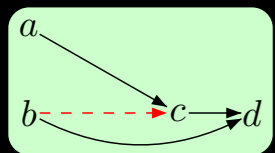
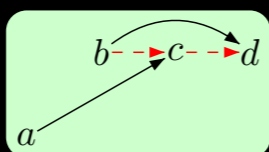
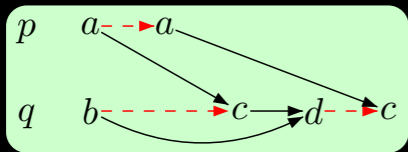
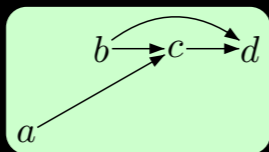
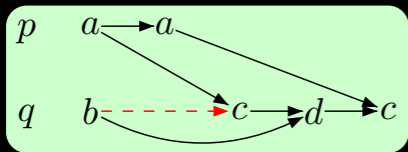
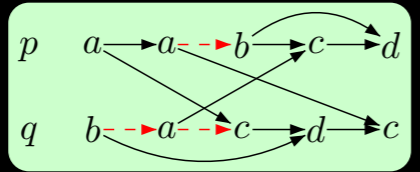
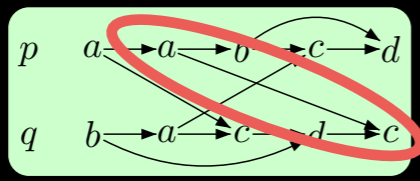
TREE INTERPRETATION



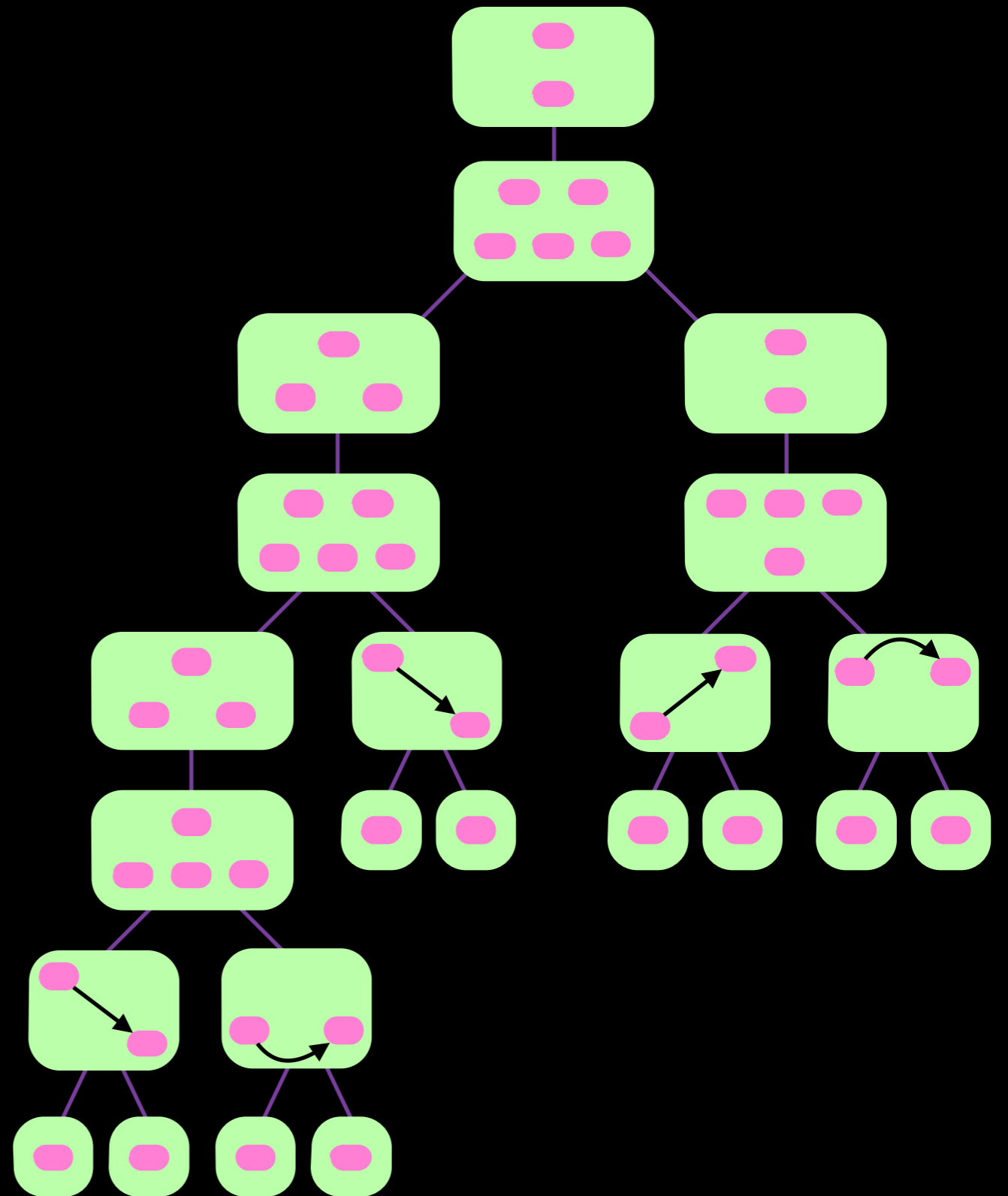
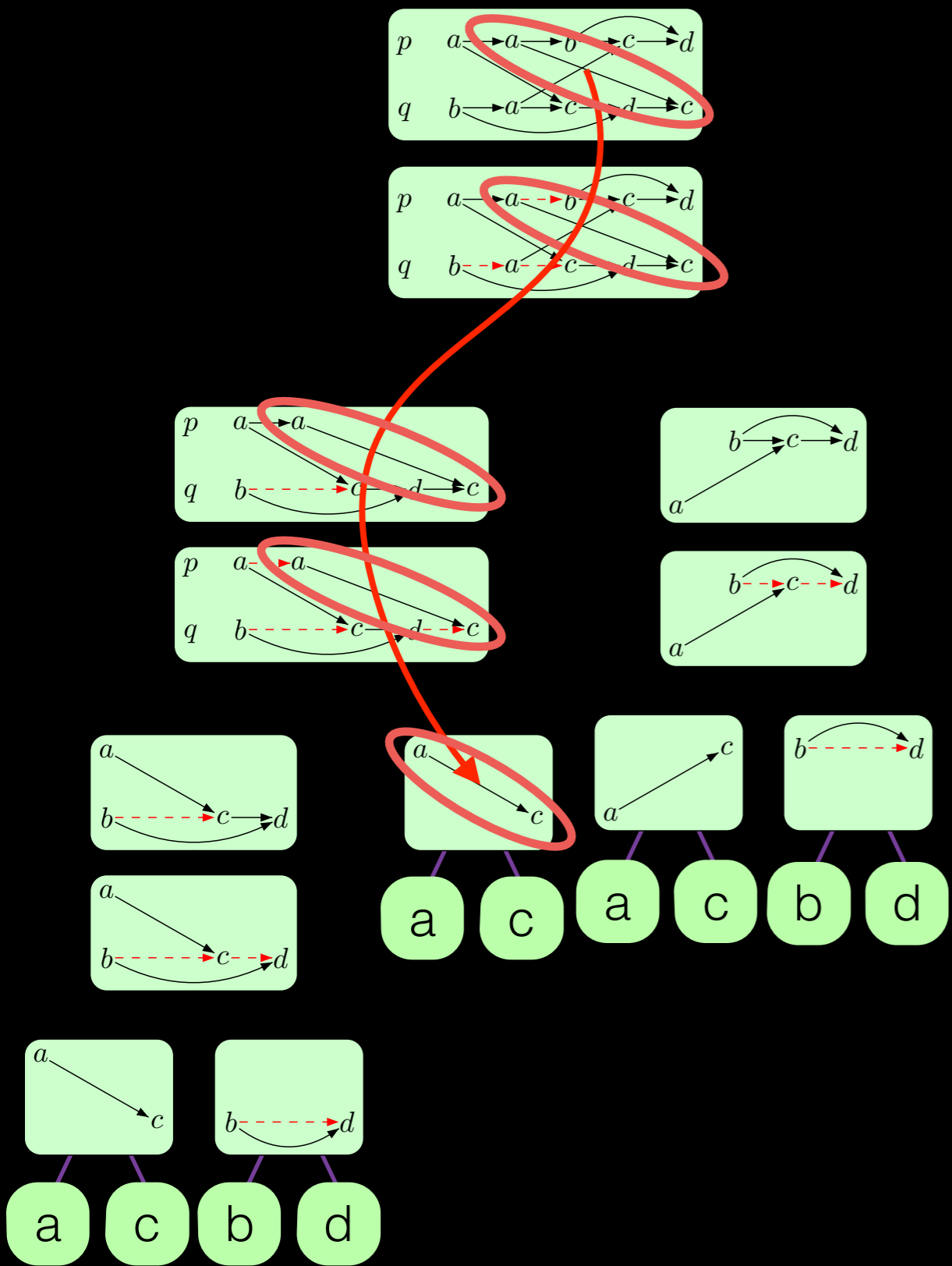
TREE INTERPRETATION



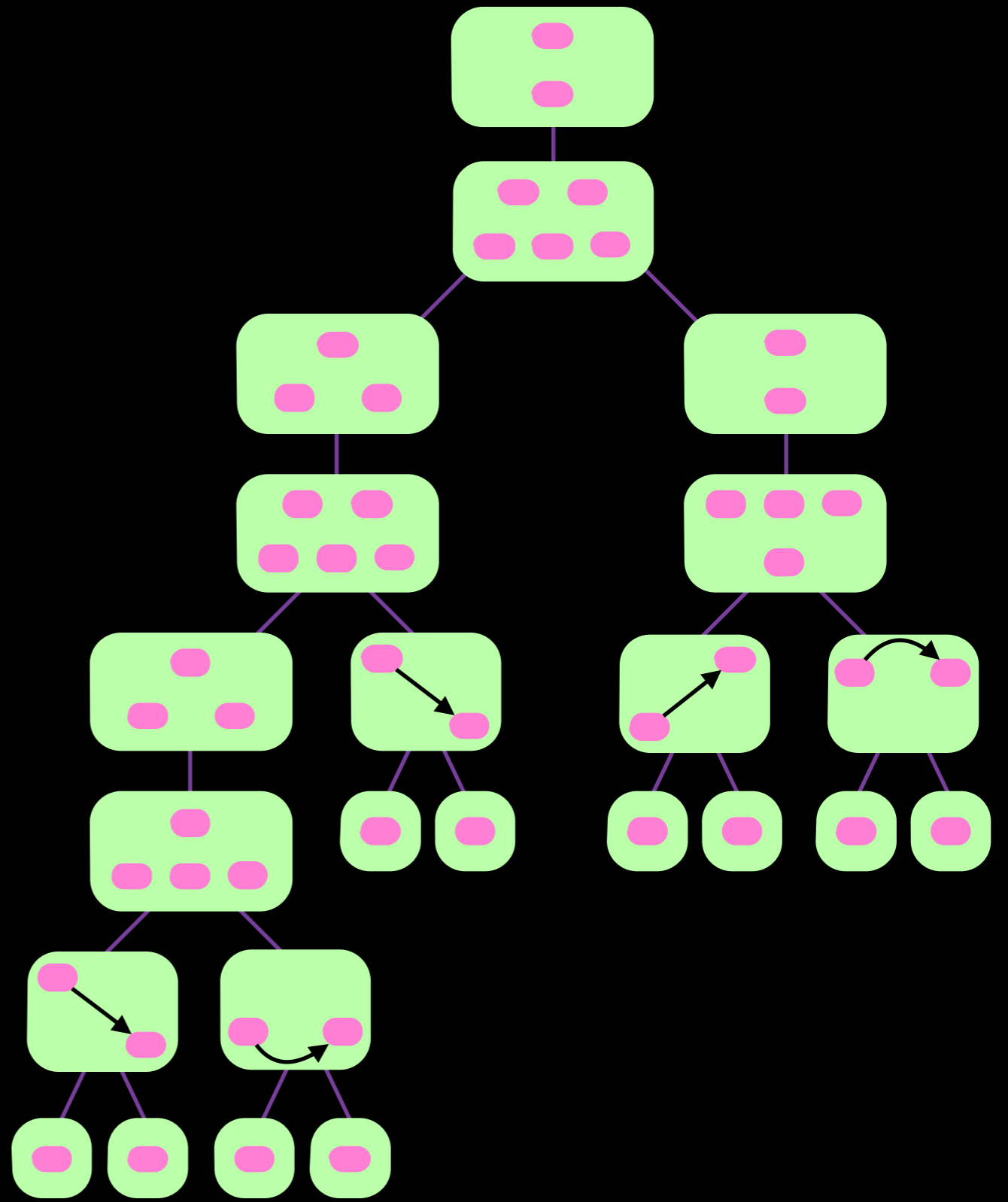
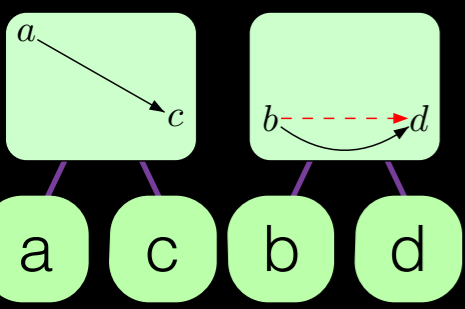
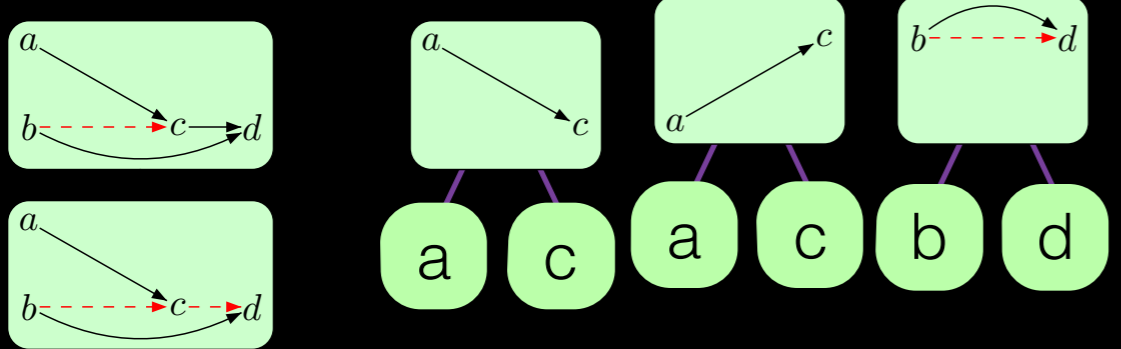
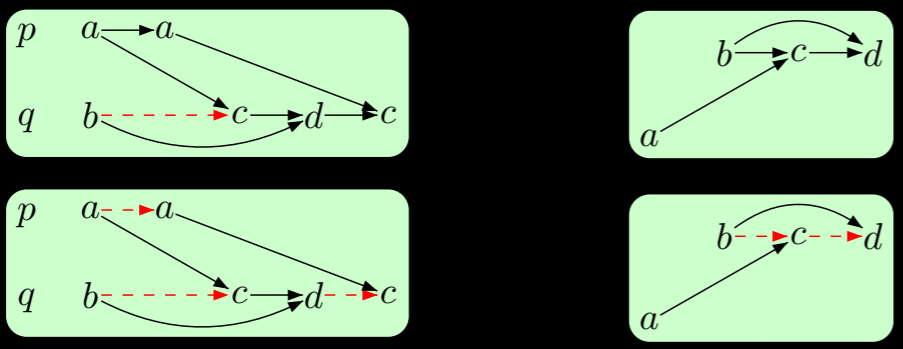
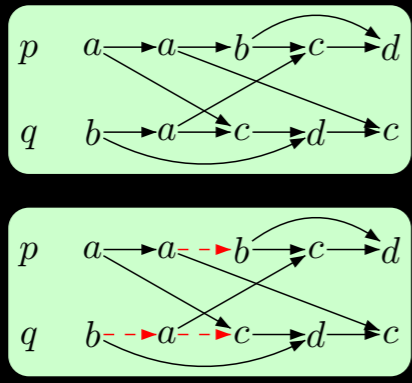
TREE INTERPRETATION



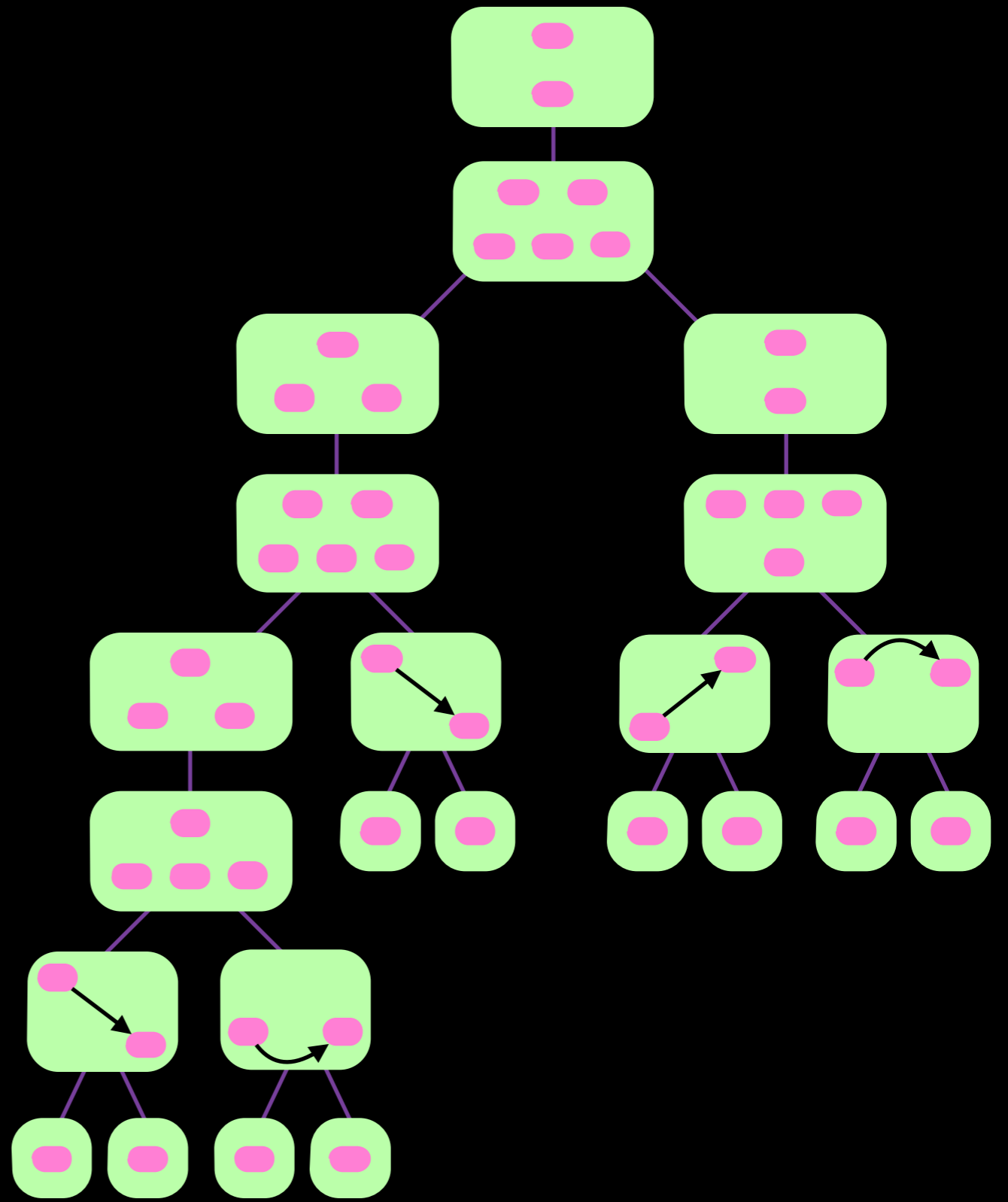
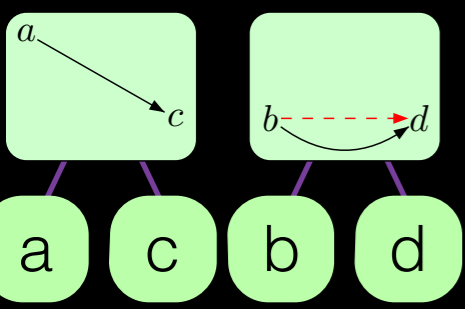
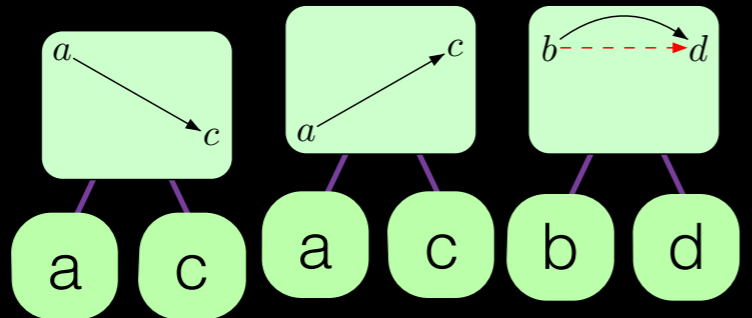
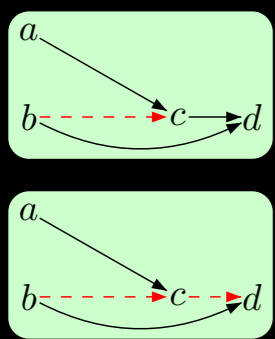
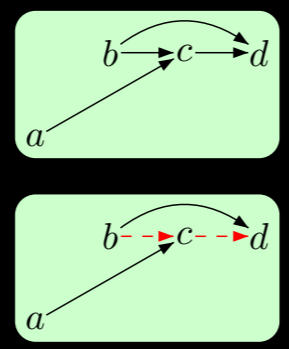
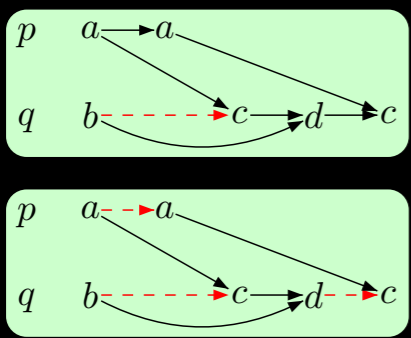
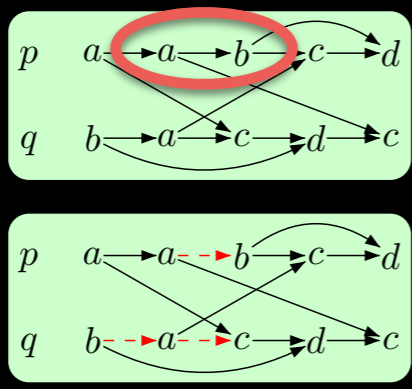
TREE INTERPRETATION



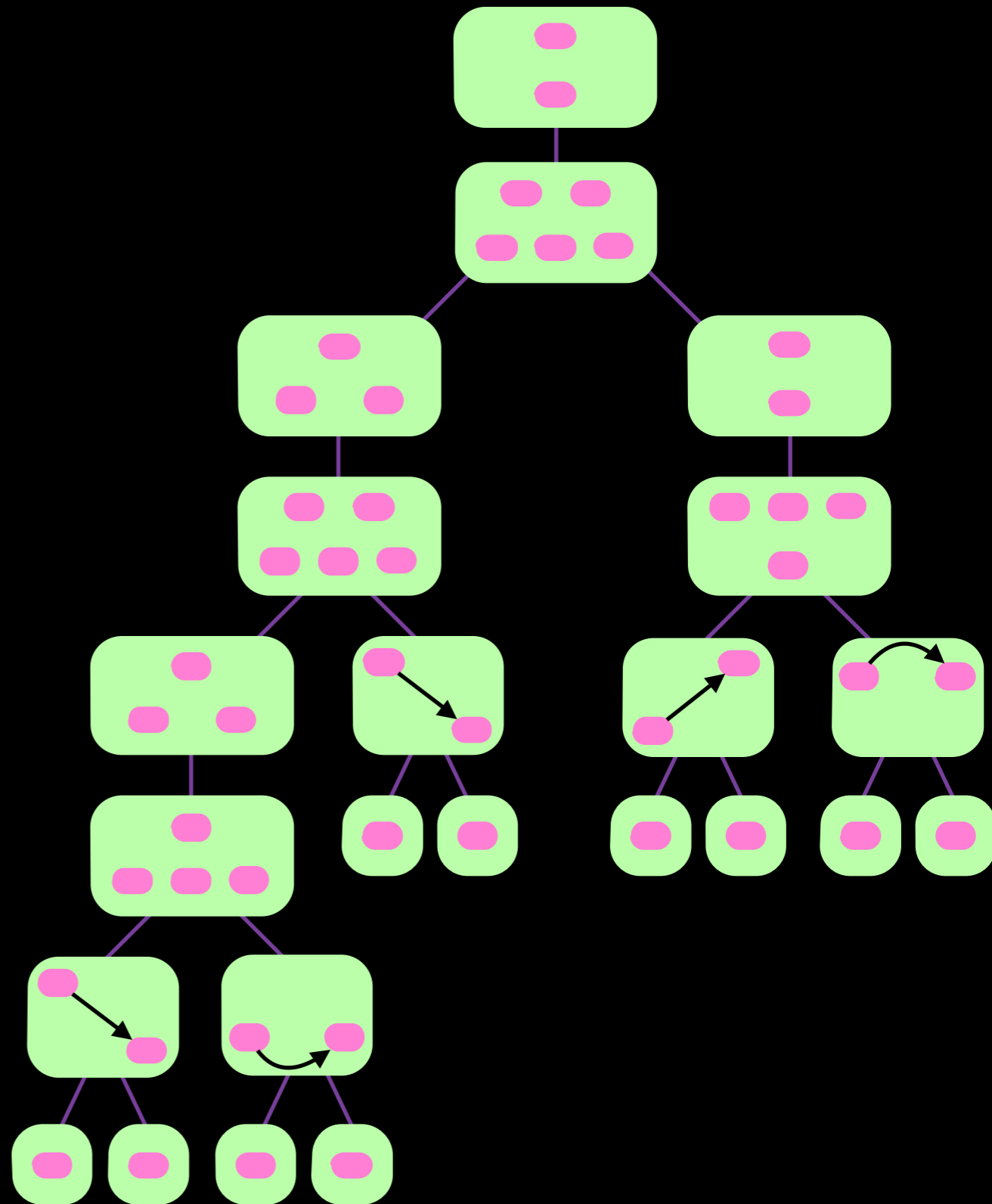
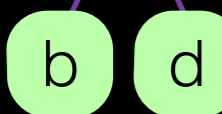
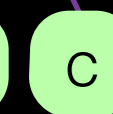
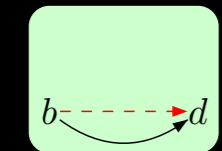
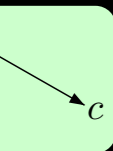
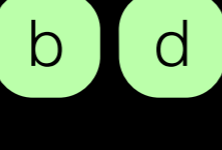
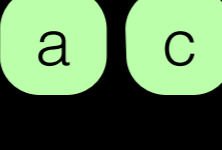
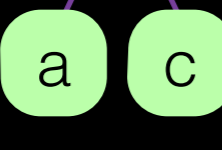
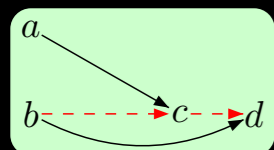
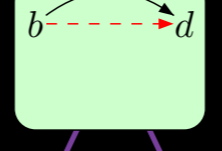
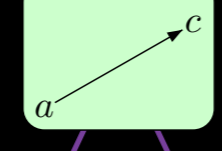
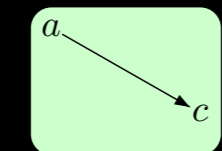
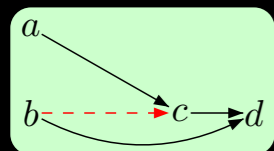
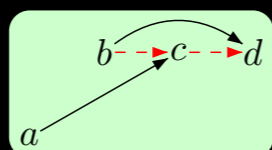
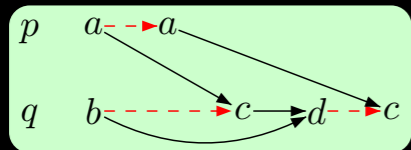
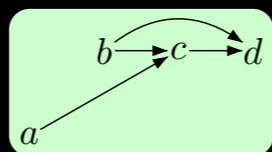
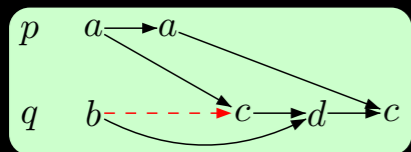
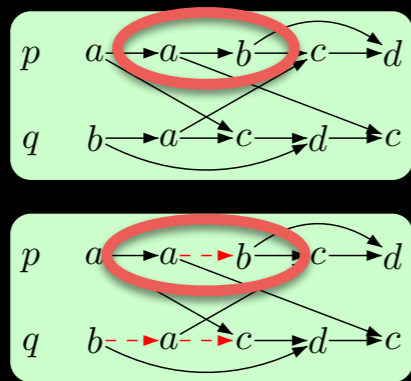
TREE INTERPRETATION



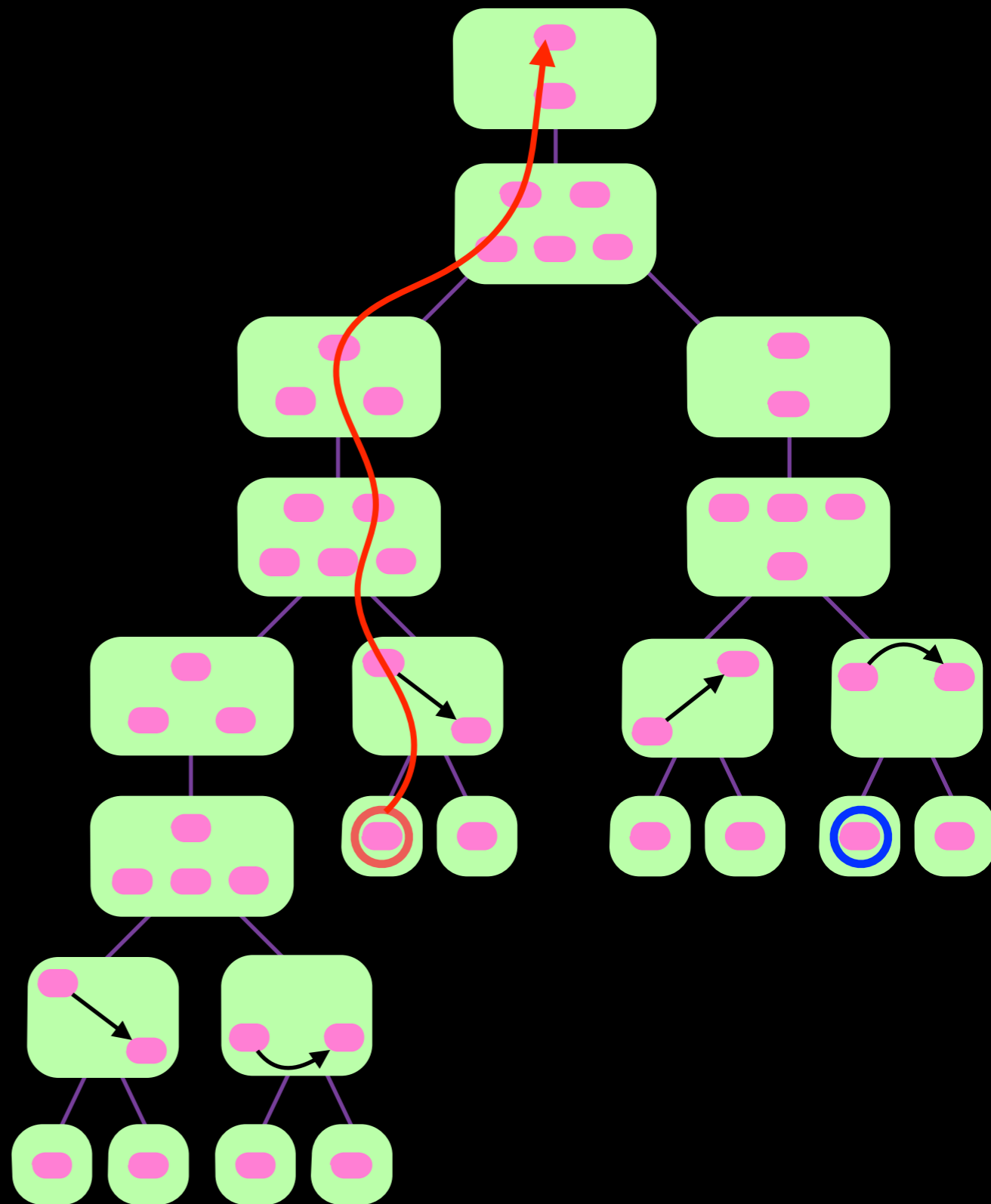
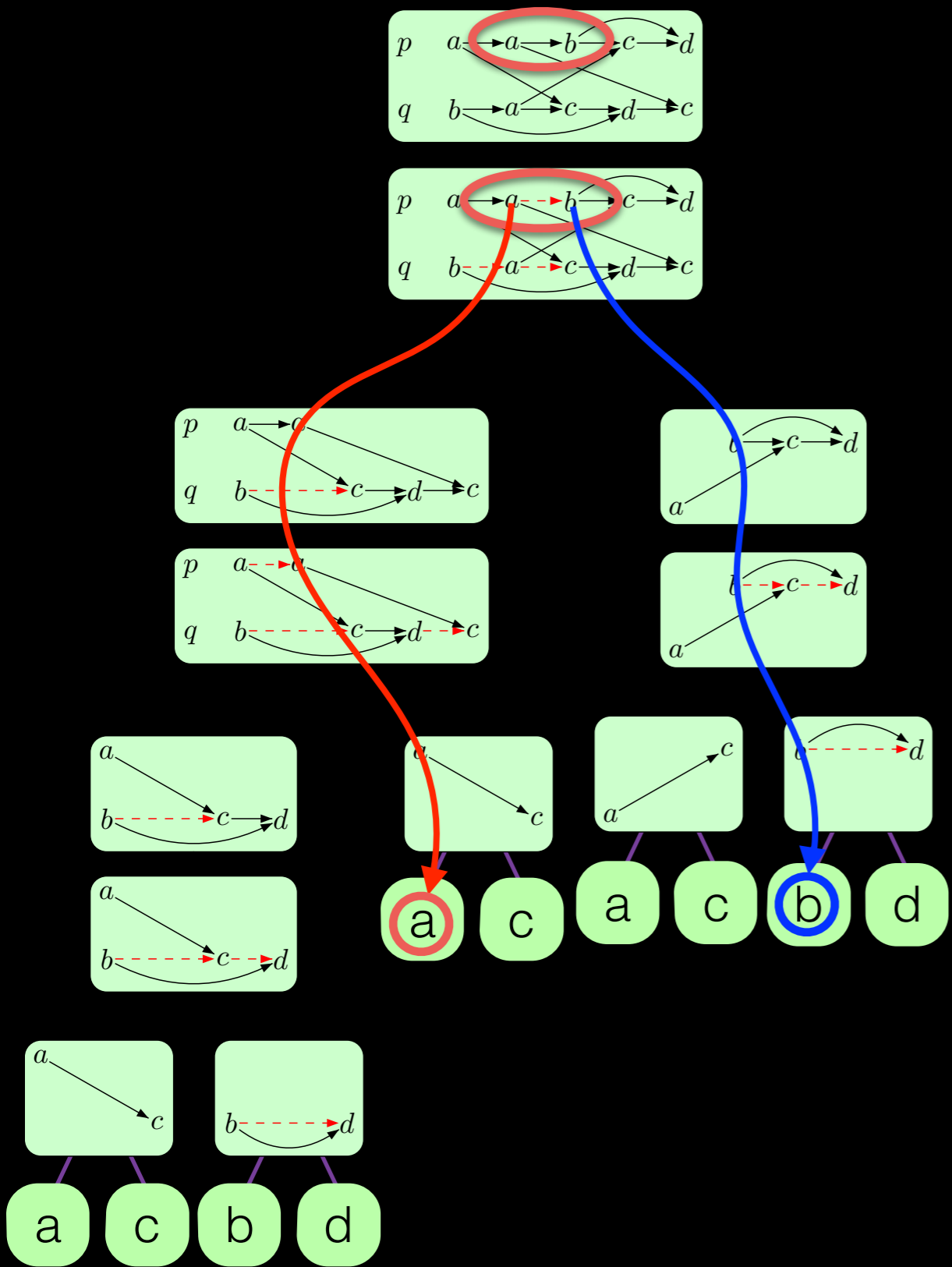
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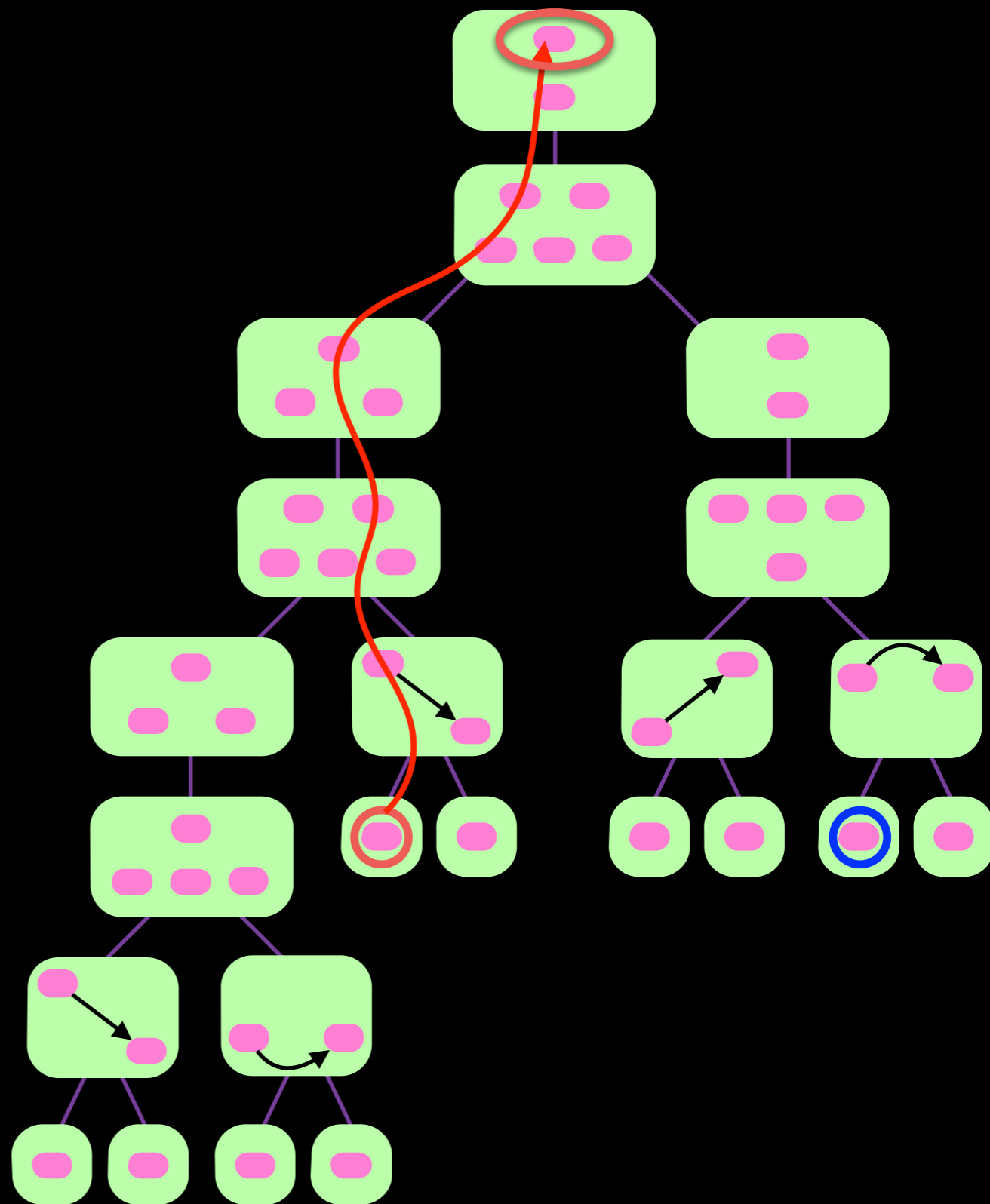
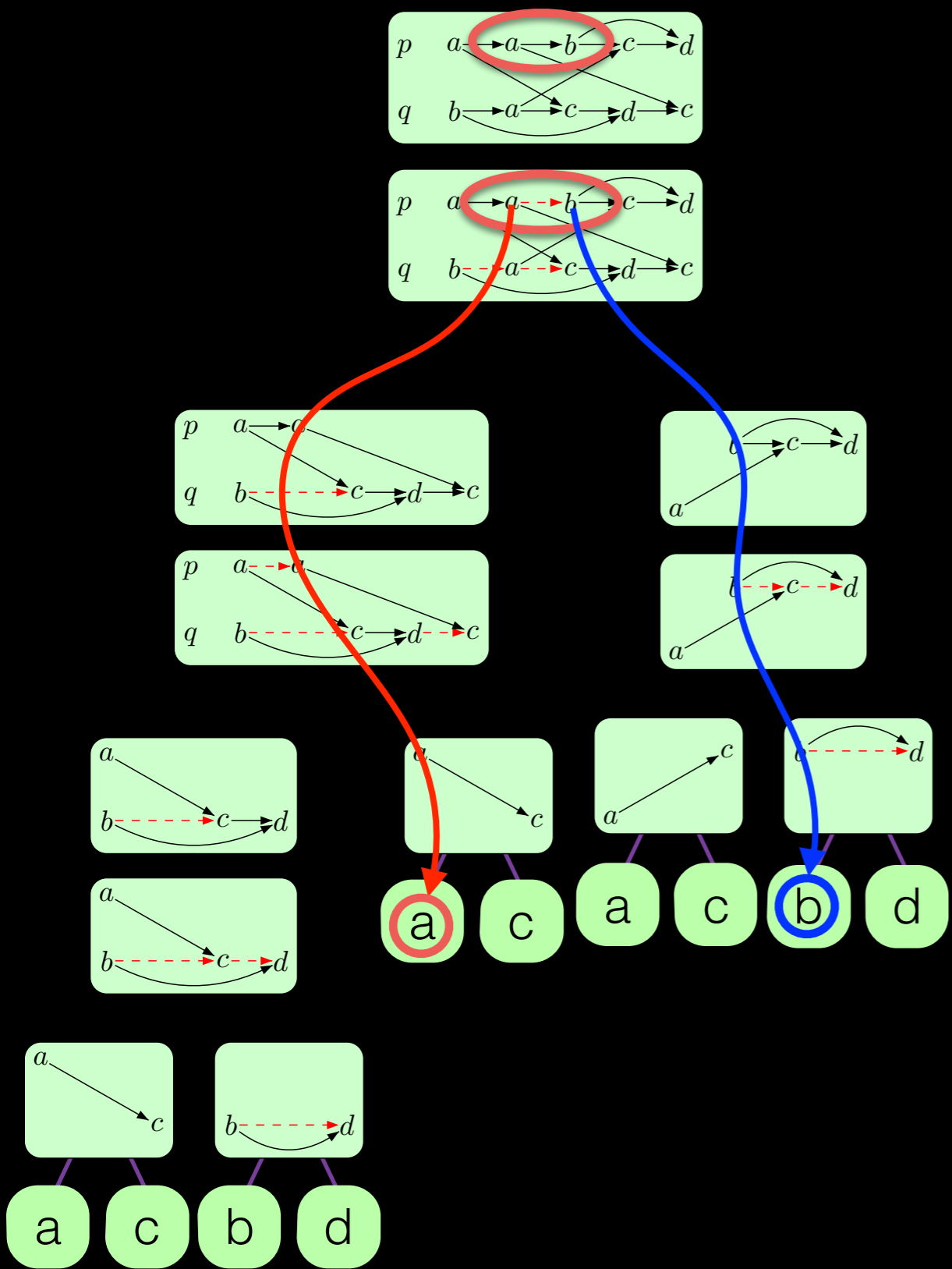
TREE INTERPRETATION



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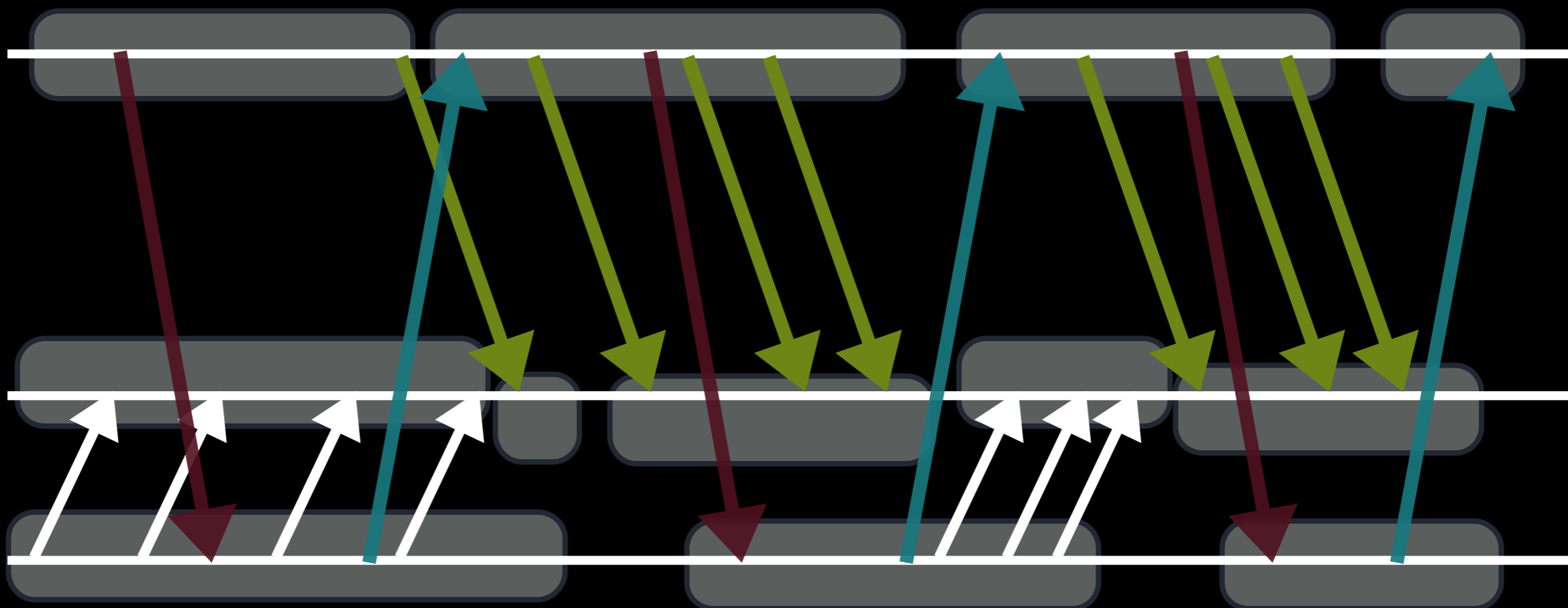


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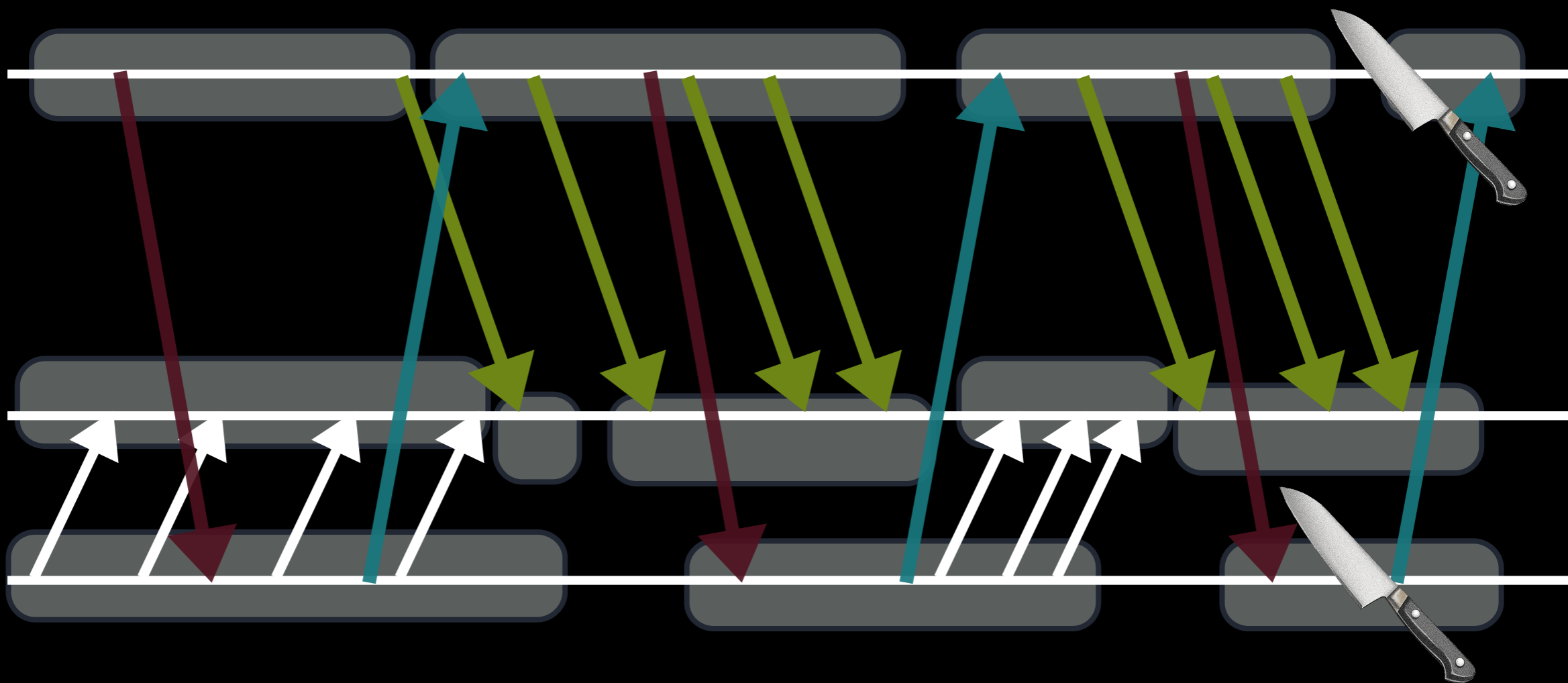
Split-width

Problem	Complexity	
	bound on split-width part of the input (in unary)	bound on split-width fixed
CPDS emptiness	EXPTIME-Complete	P TIME-Complete
CPDS inclusion or universality	2EXPTIME	EXPTIME-Complete
LTL / CPDL satisfiability or model checking	EXPTIME-Complete	
ICPDL satisfiability or model checking	2EXPTIME -Complete	
MSO satisfiability or model checking	Non-elementary	

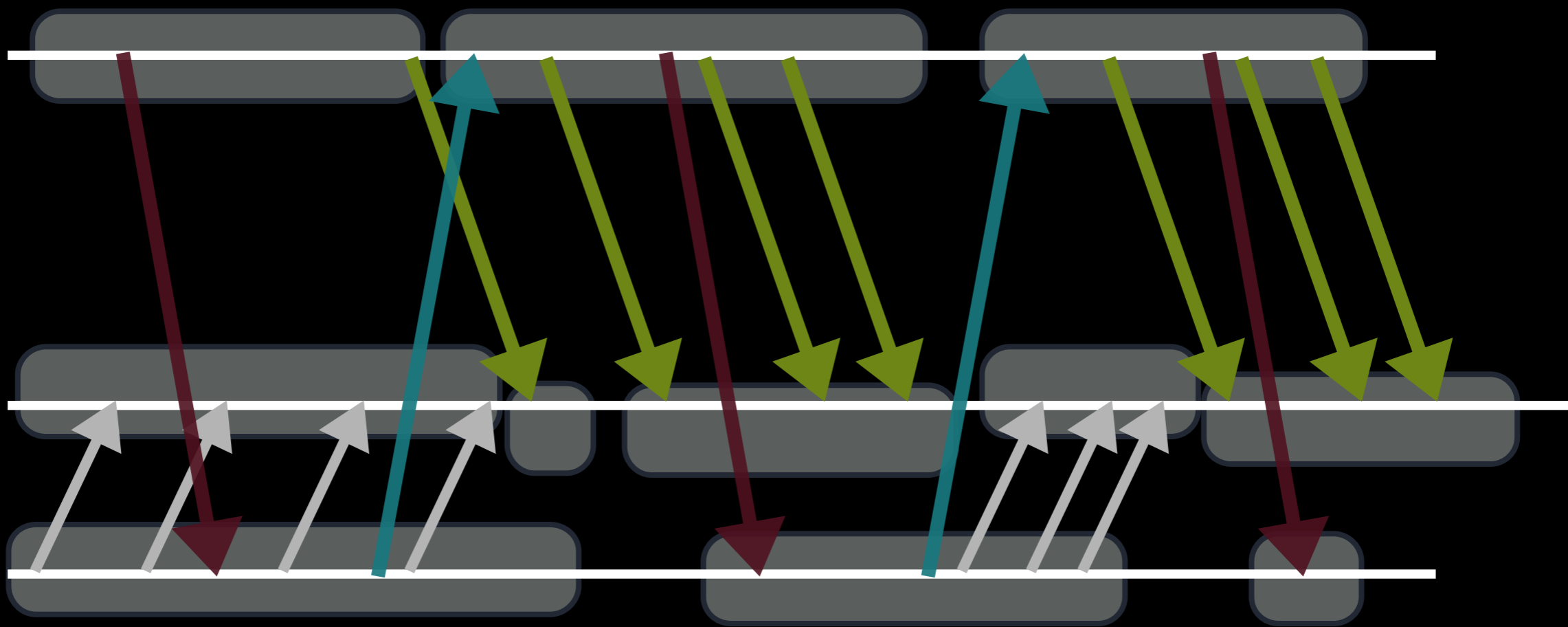
SPLIT-WIDTH



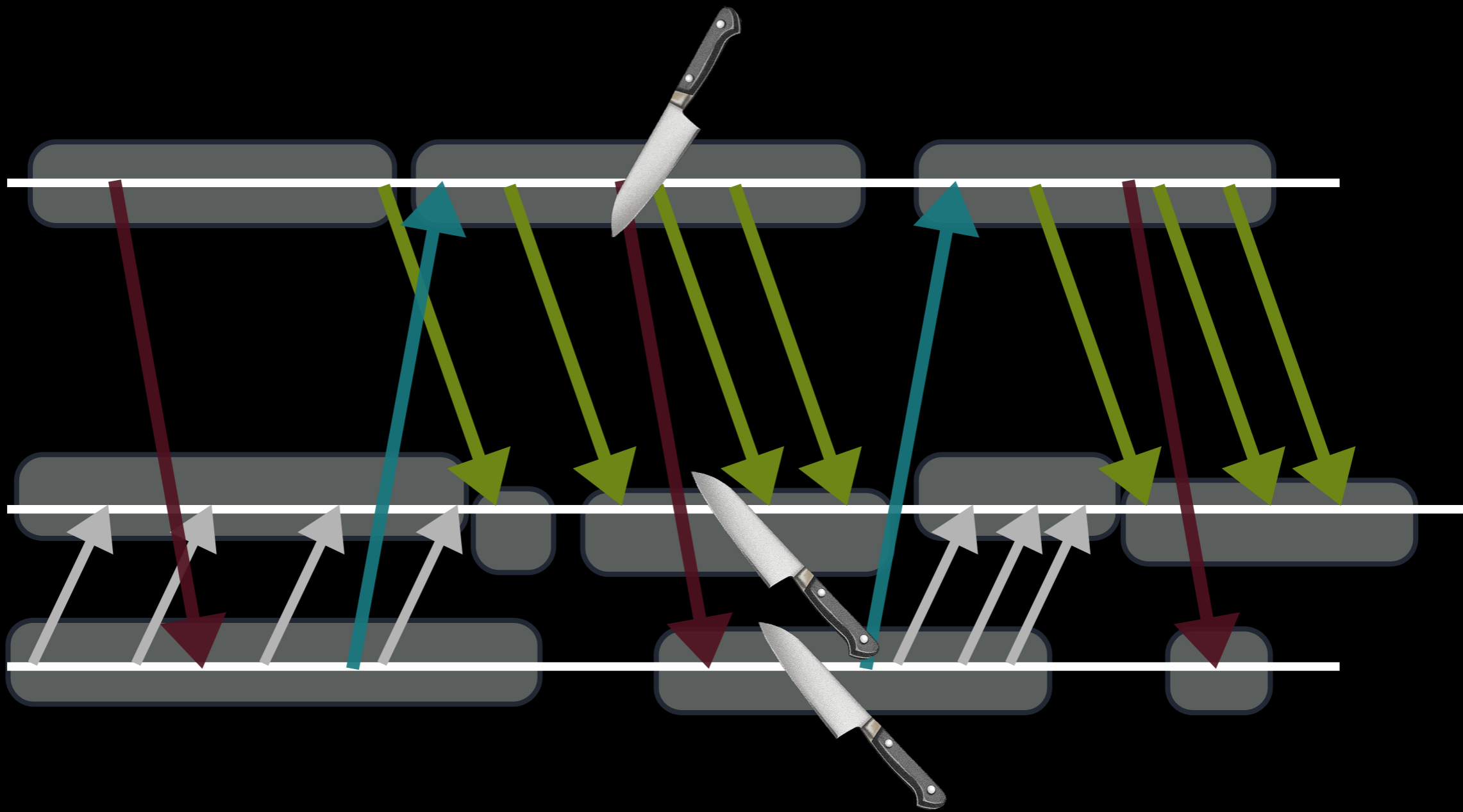
SPLIT-WIDTH



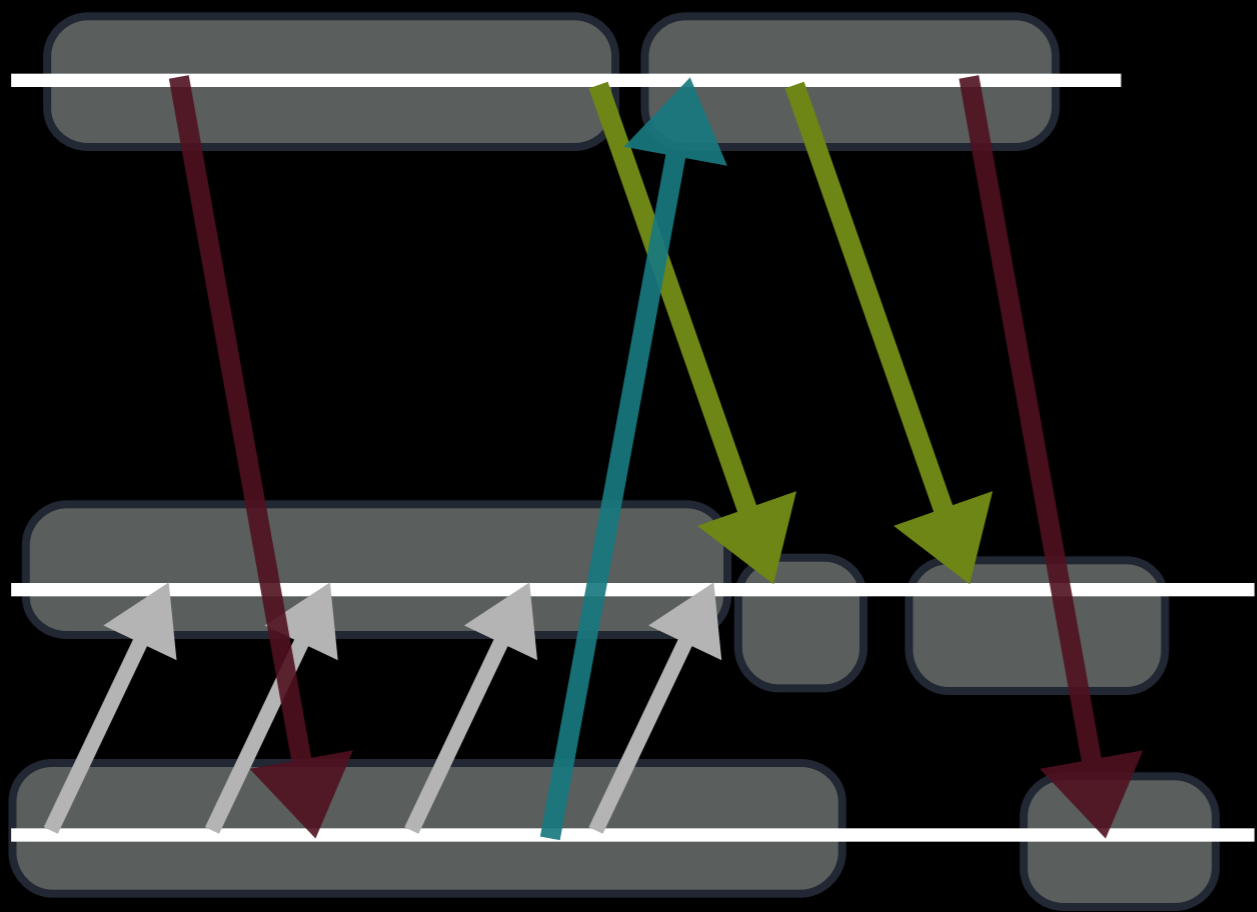
SPLIT-WIDTH 3



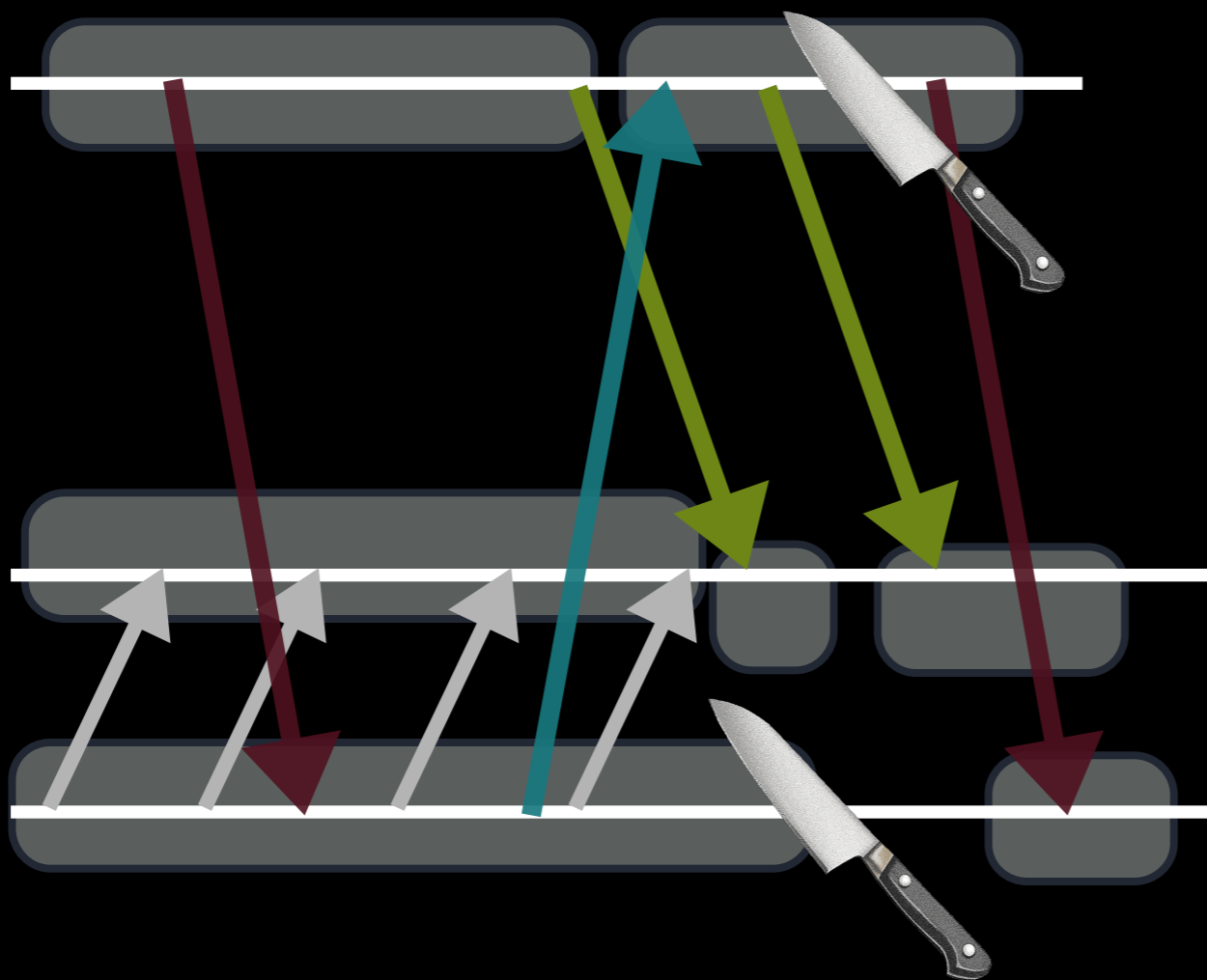
SPLIT-WIDTH 3



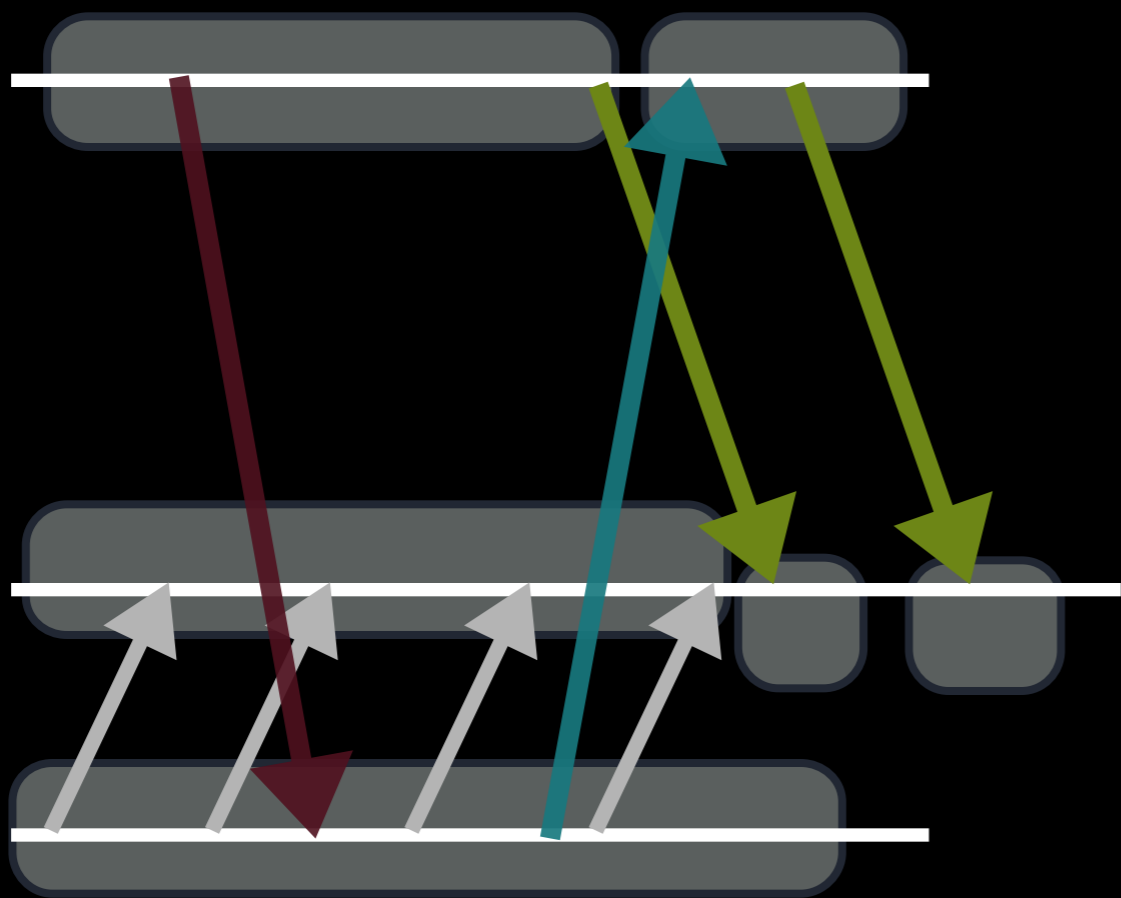
SPLIT-WIDTH 3



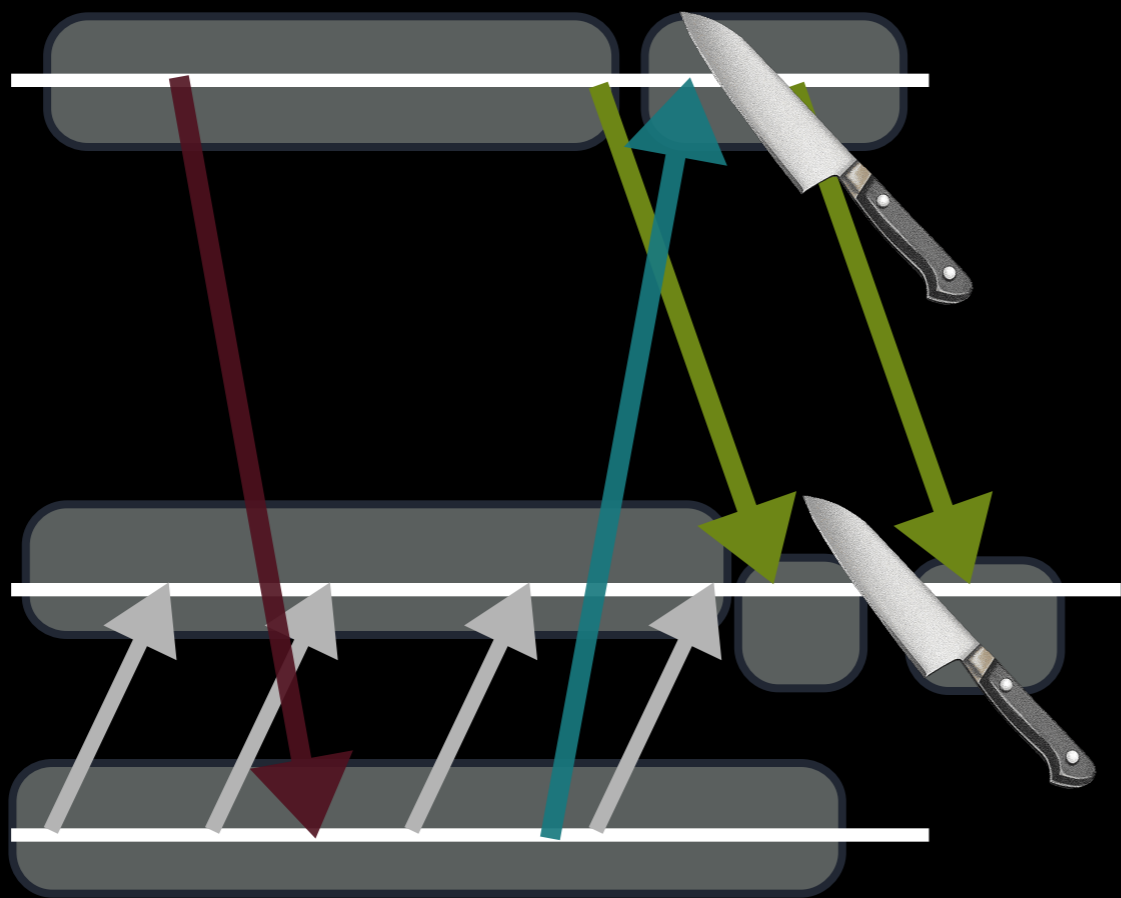
SPLIT-WIDTH 3



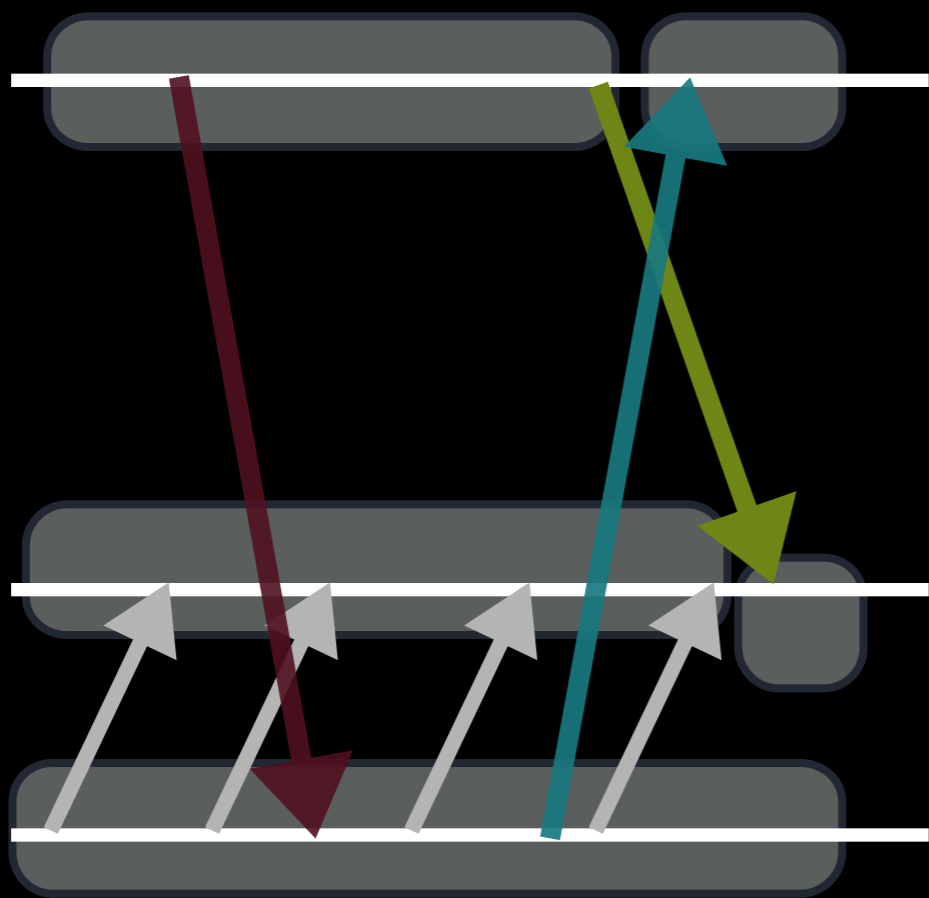
SPLIT-WIDTH 3



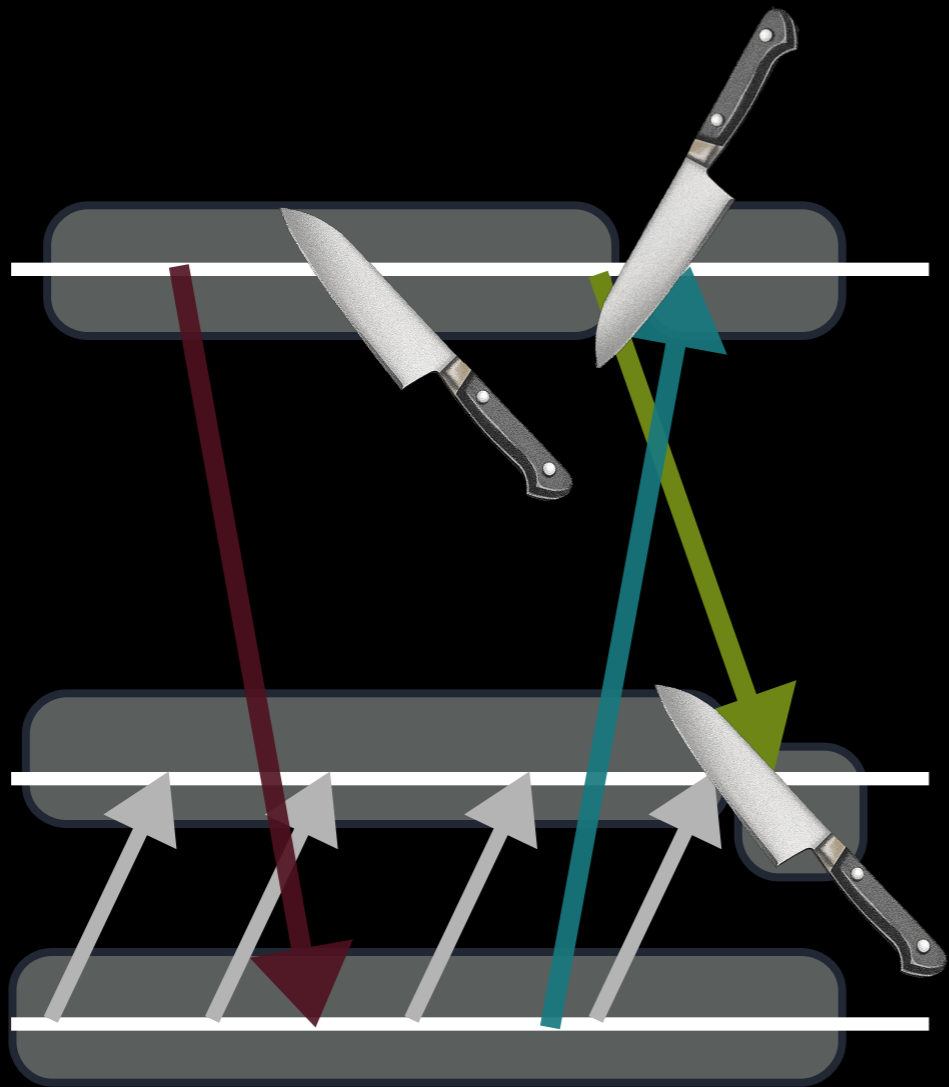
SPLIT-WIDTH 3



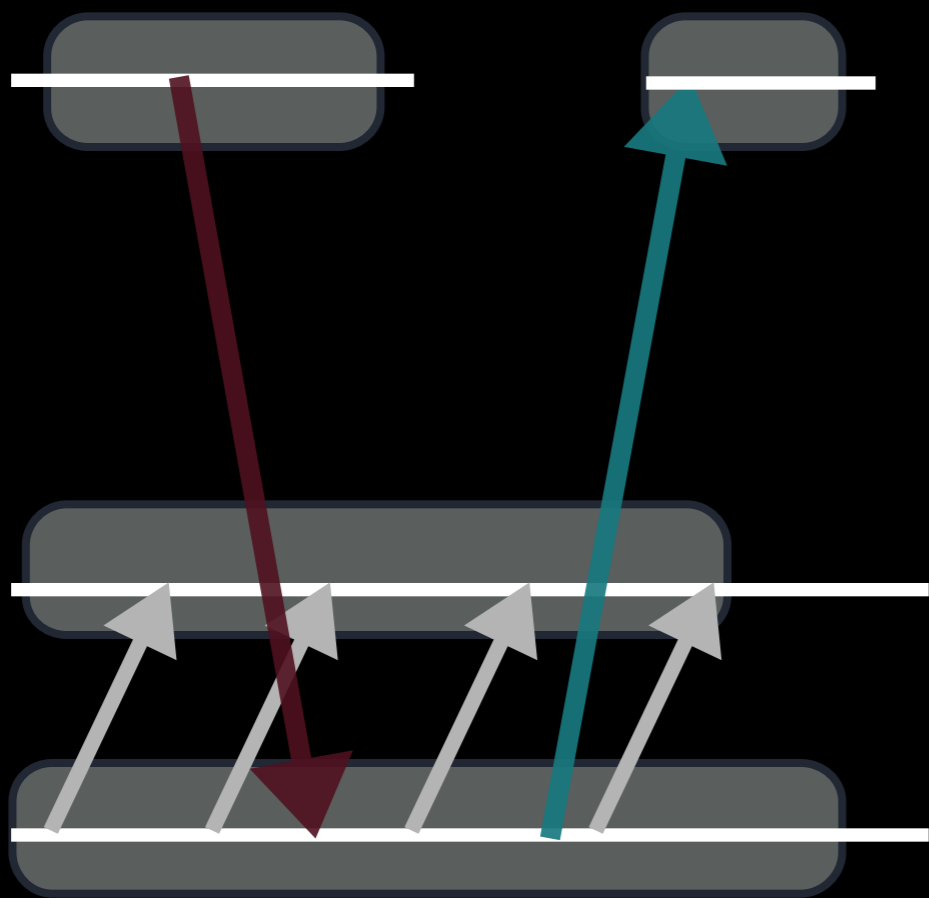
SPLIT-WIDTH 3



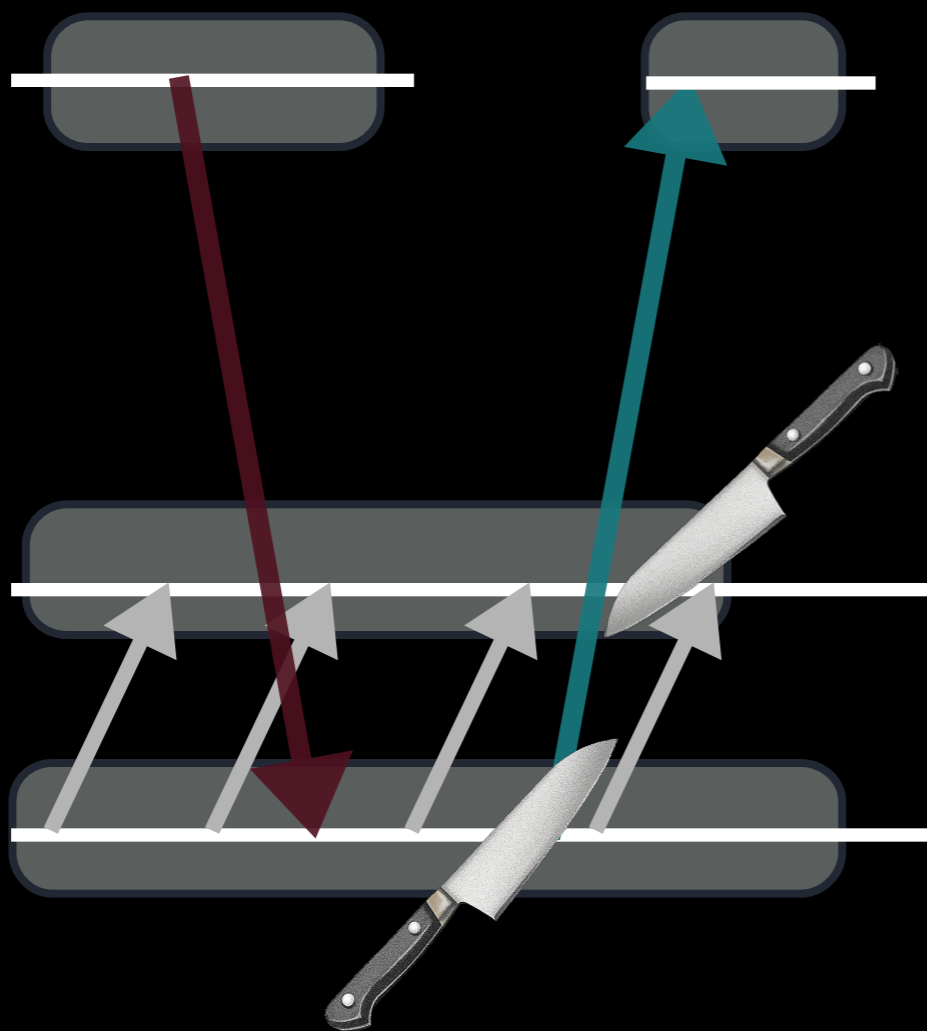
SPLIT-WIDTH 3



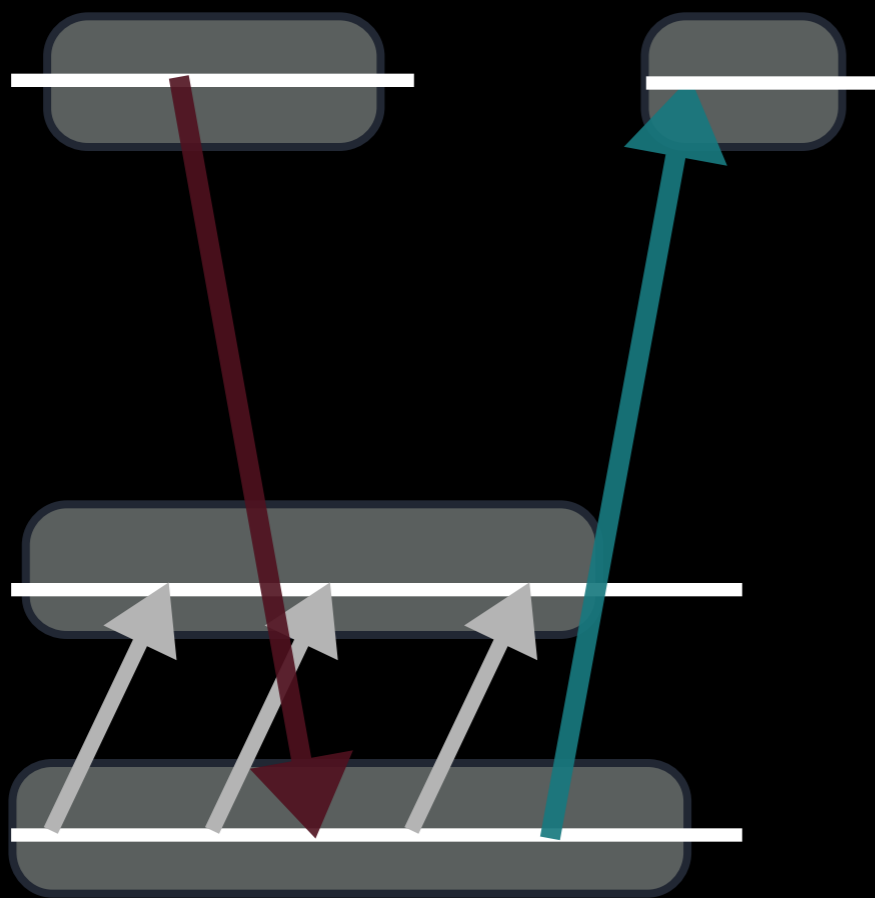
SPLIT-WIDTH 3



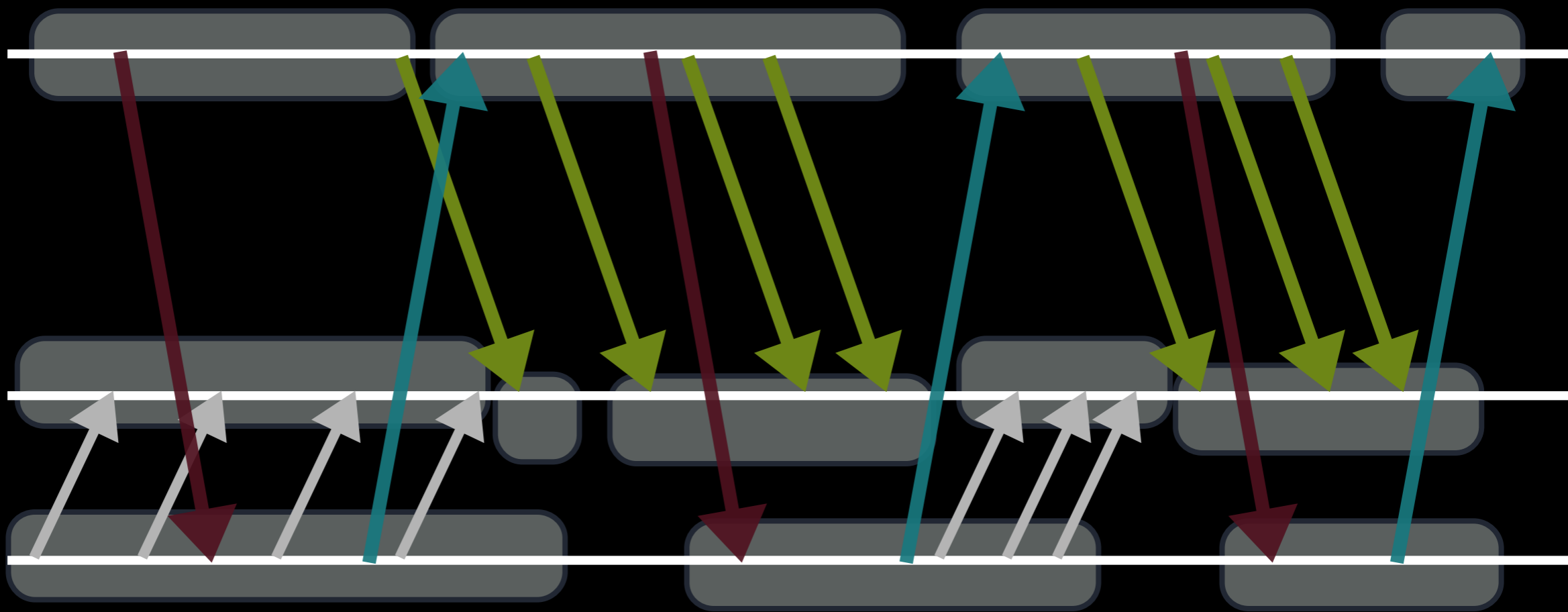
SPLIT-WIDTH 3



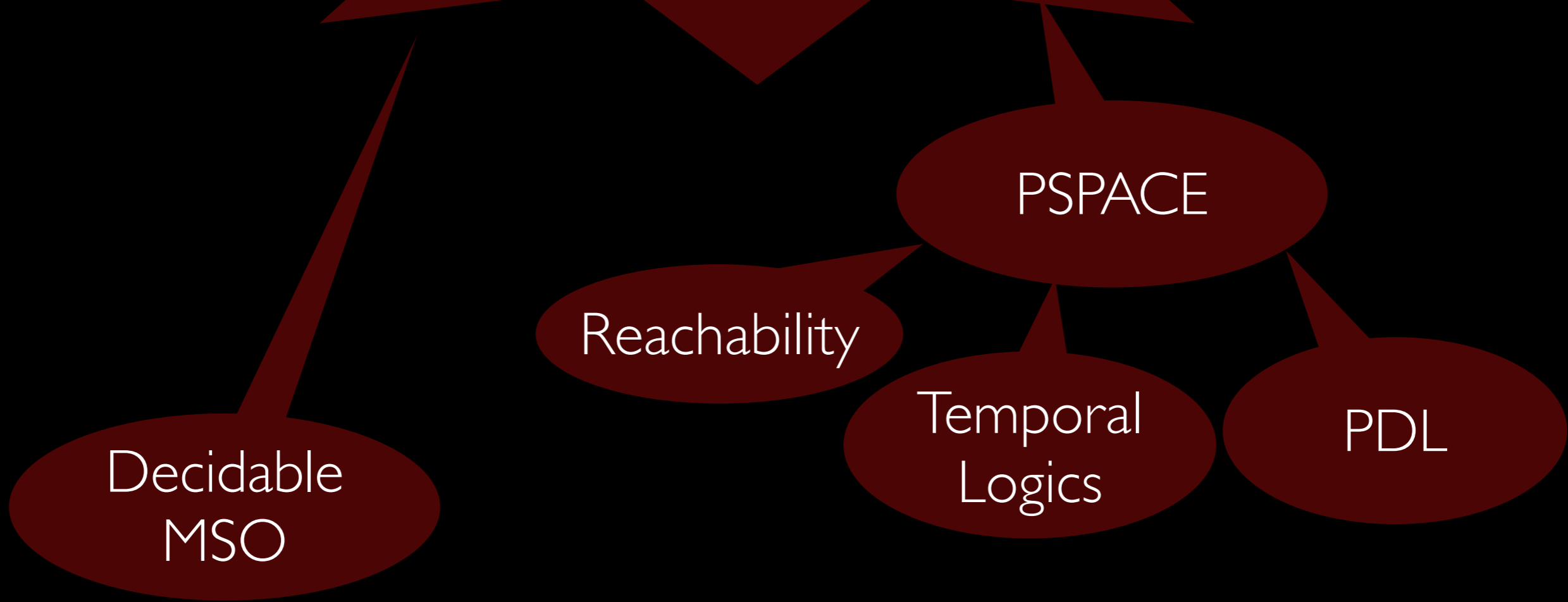
SPLIT-WIDTH 3



SPLIT-WIDTH 3



Polynomial SPLIT-WIDTH



Decidable
MSO

PSPACE

Reachability

Temporal
Logics

PDL

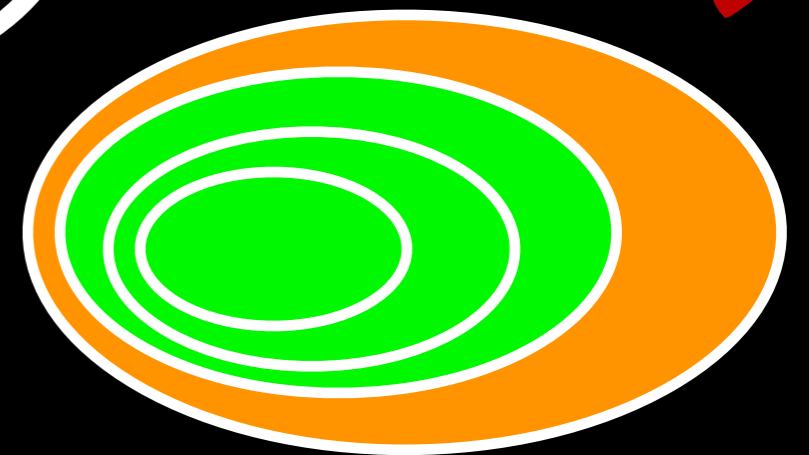
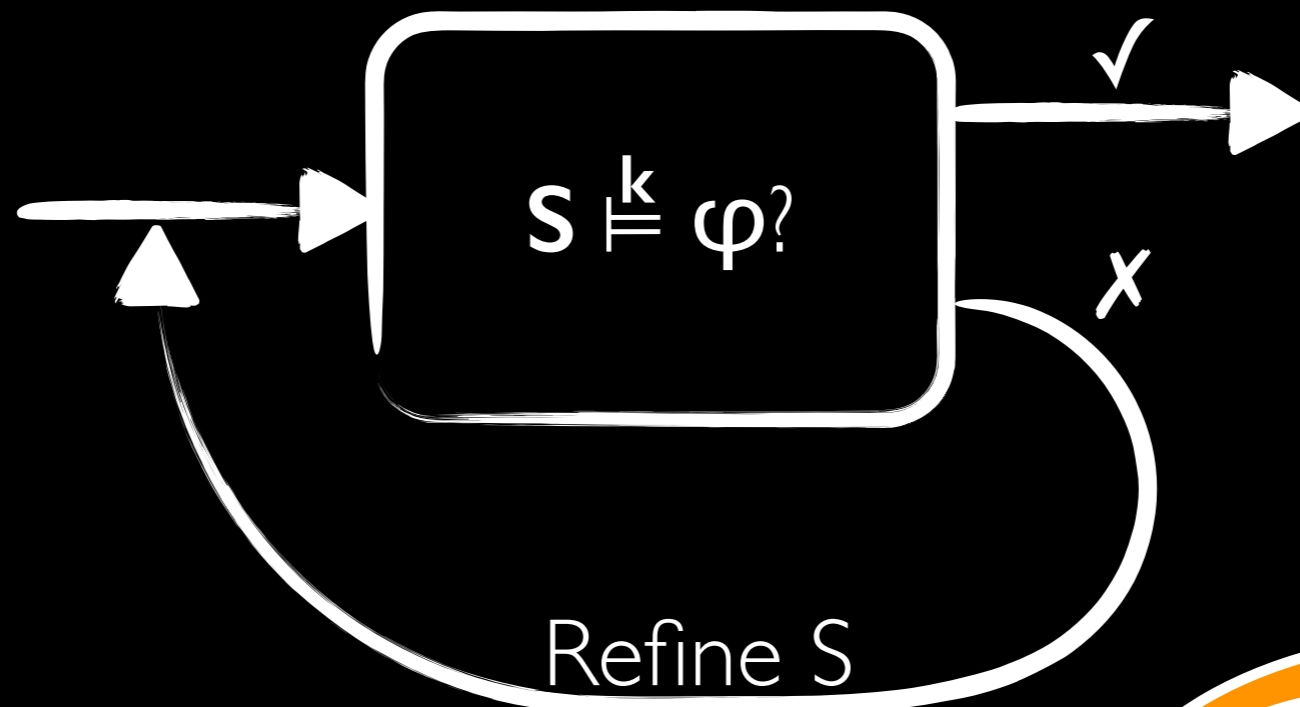
UNDER-APPROXIMATE VERIFICATION

Model Checking



Decidable

System S
Specification φ



OTHER UNDER-APPROXIMATIONS

- * Bounded channel size
- * Existentially bounded [Genest et al.]
- * Acyclic Architectures [La Torre et al., Heußner et al. Clemente et al.]
- * Bounded context switching [Qadeer, Rehof], [LaTorre et al.], ...
- * Bounded phase [LaTorre et al.]
- * Bounded scope [LaTorre et al.]
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Tree-width

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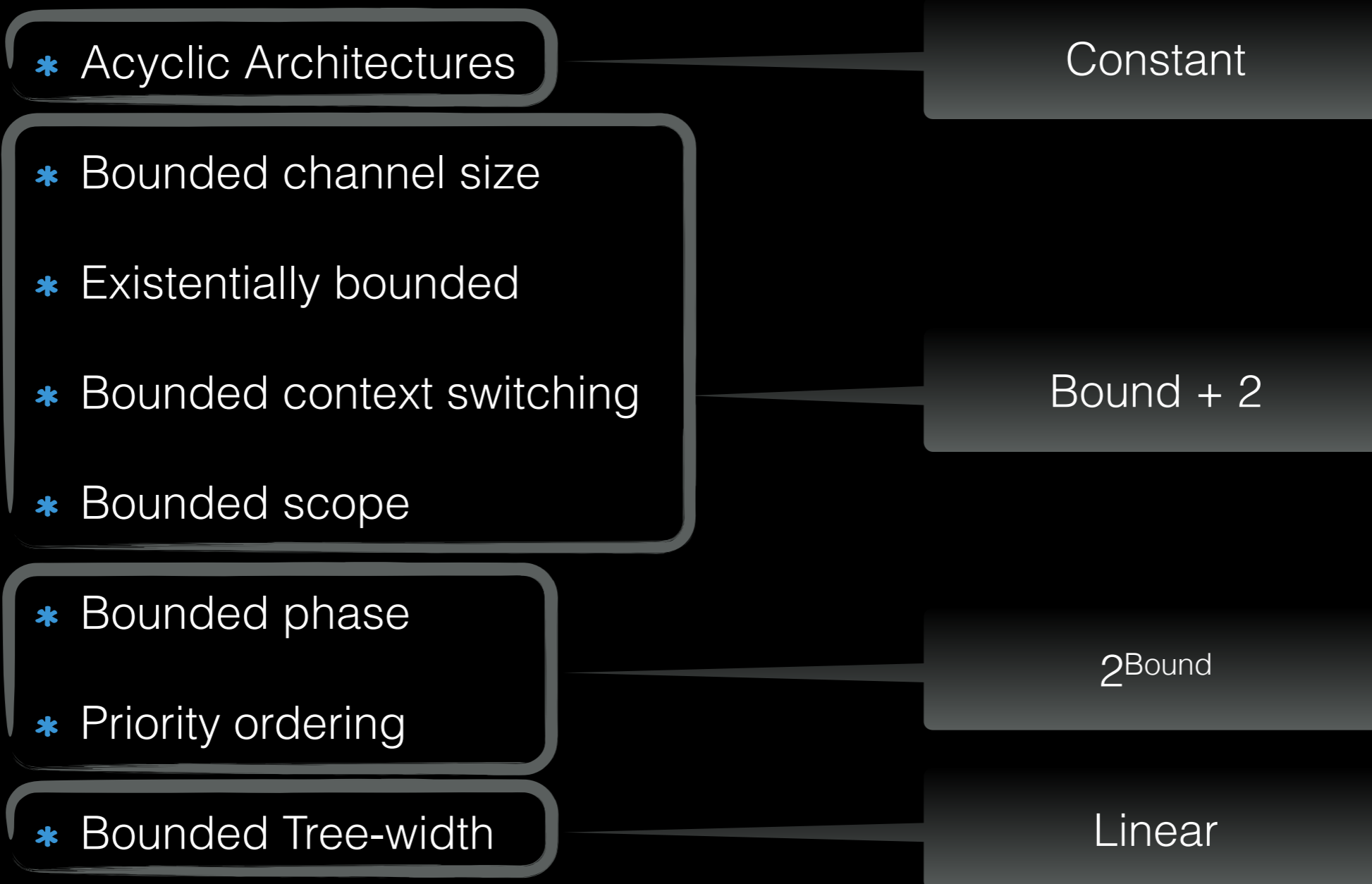
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Tree-width

- * Many of the above classes have bounded tree-width [Parlato, Madhusudhan]

OTHER UNDER-APPROXIMATIONS

Split-width



Width: split vs tree vs clique

Split-Width k

Tree-Width t

Clique-Width c

Let C be a class of **bounded degree MSO definable** graphs.

TFAE

1. C has a decidable MSO theory
2. C can be interpreted in binary trees
3. C has bounded tree-width
4. C has bounded clique-width
5. C has bounded split-width (for concurrent recursive behaviors)

Width: split vs tree vs clique



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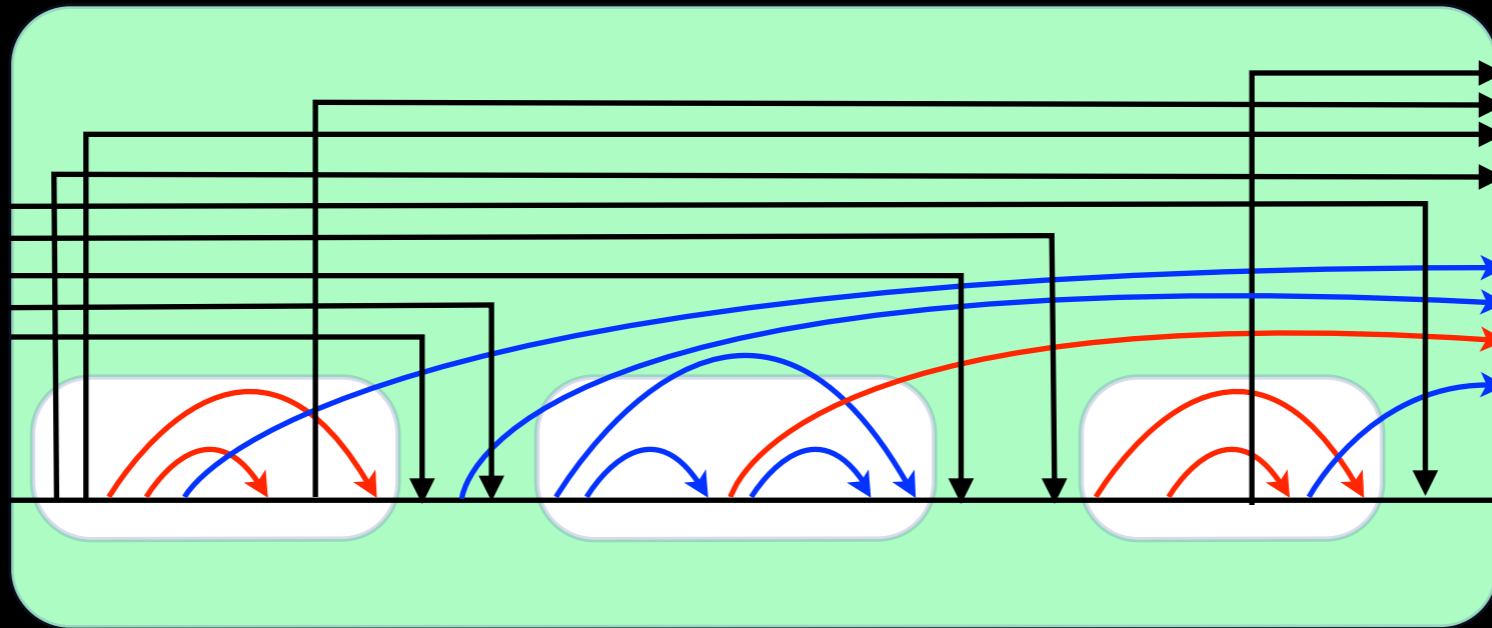
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COMMUNICATING RECURSIVE PROGRAMS: CONTROL AND SPLIT-WIDTH

AUTONOMOUS COMPUTATIONS

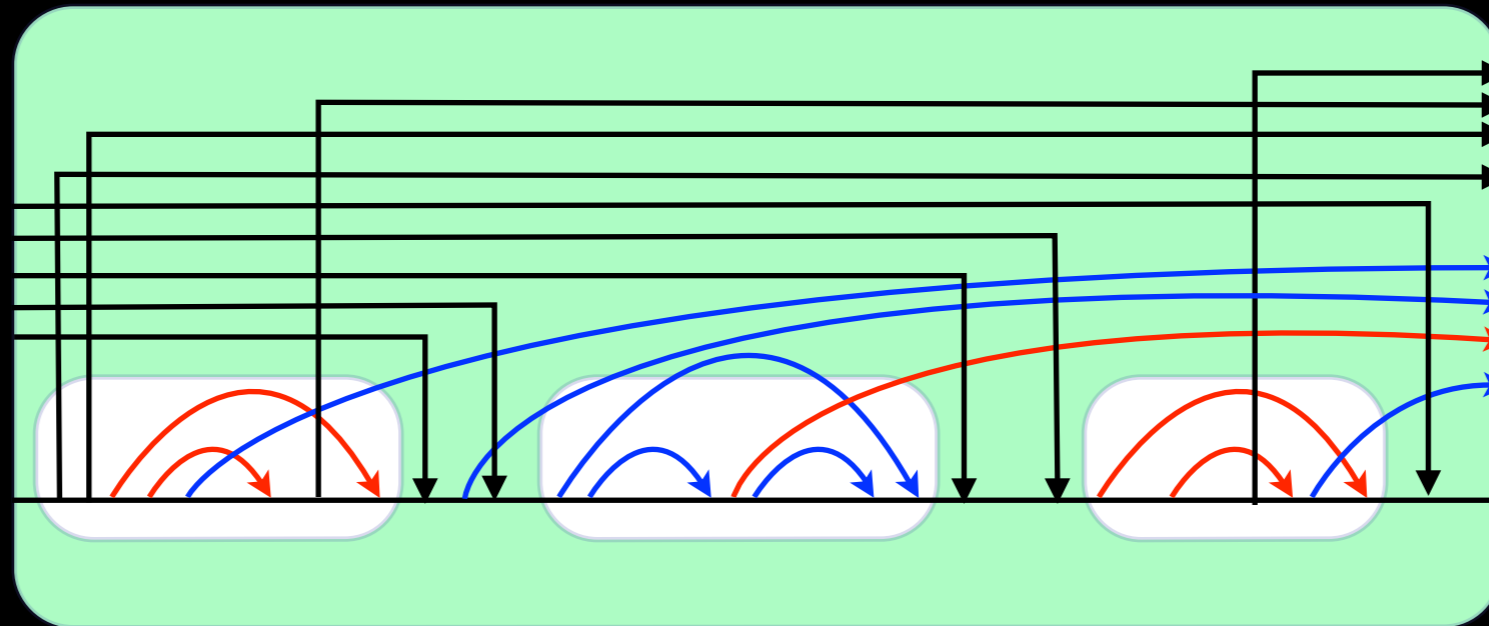
- Recursive computations which does not read from other stacks/queues.
- A stretch of computation in which all incoming edges are on a single stack

AUTONOMOUS COMPUTATIONS

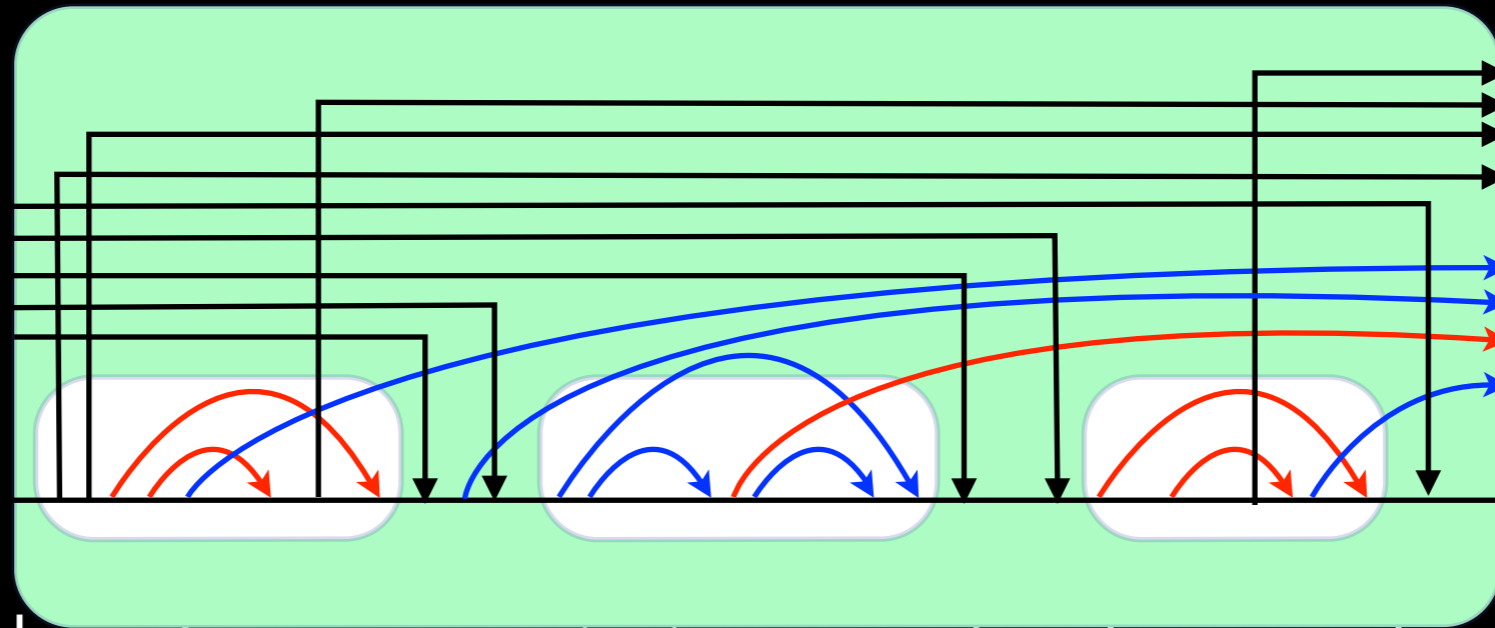


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PHASE

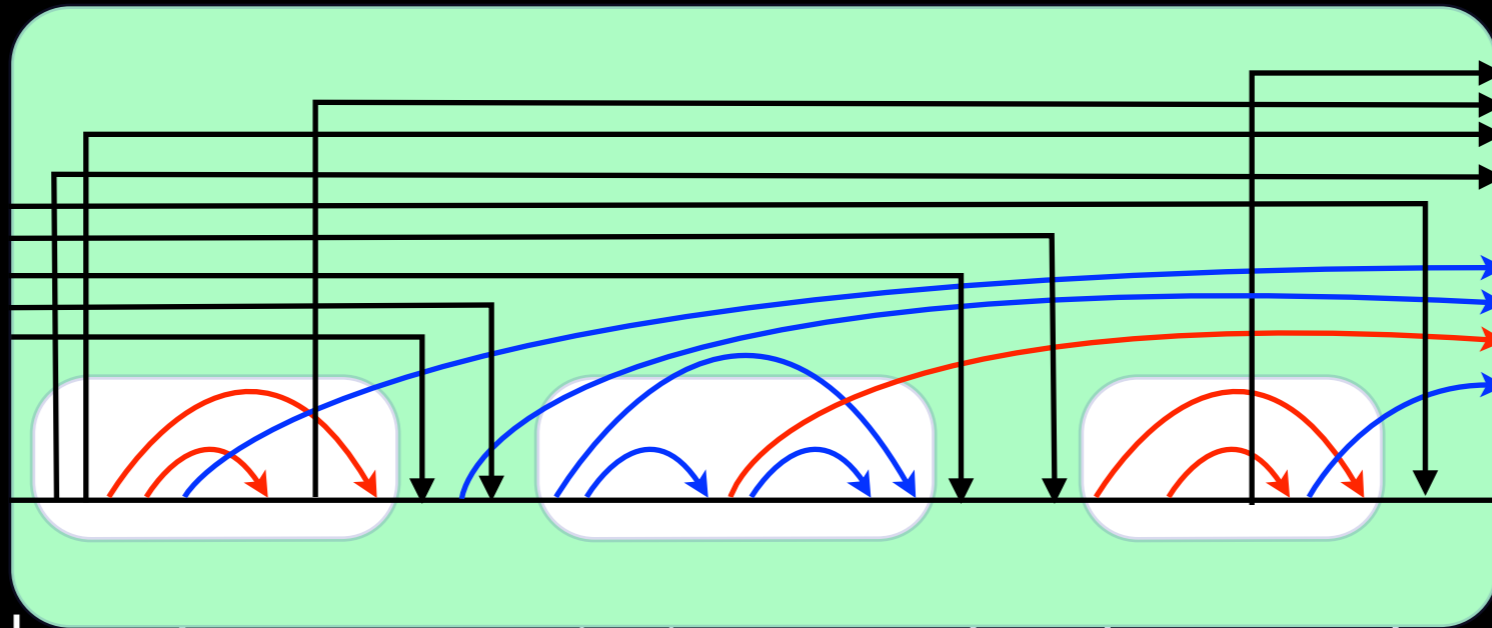


PHASE



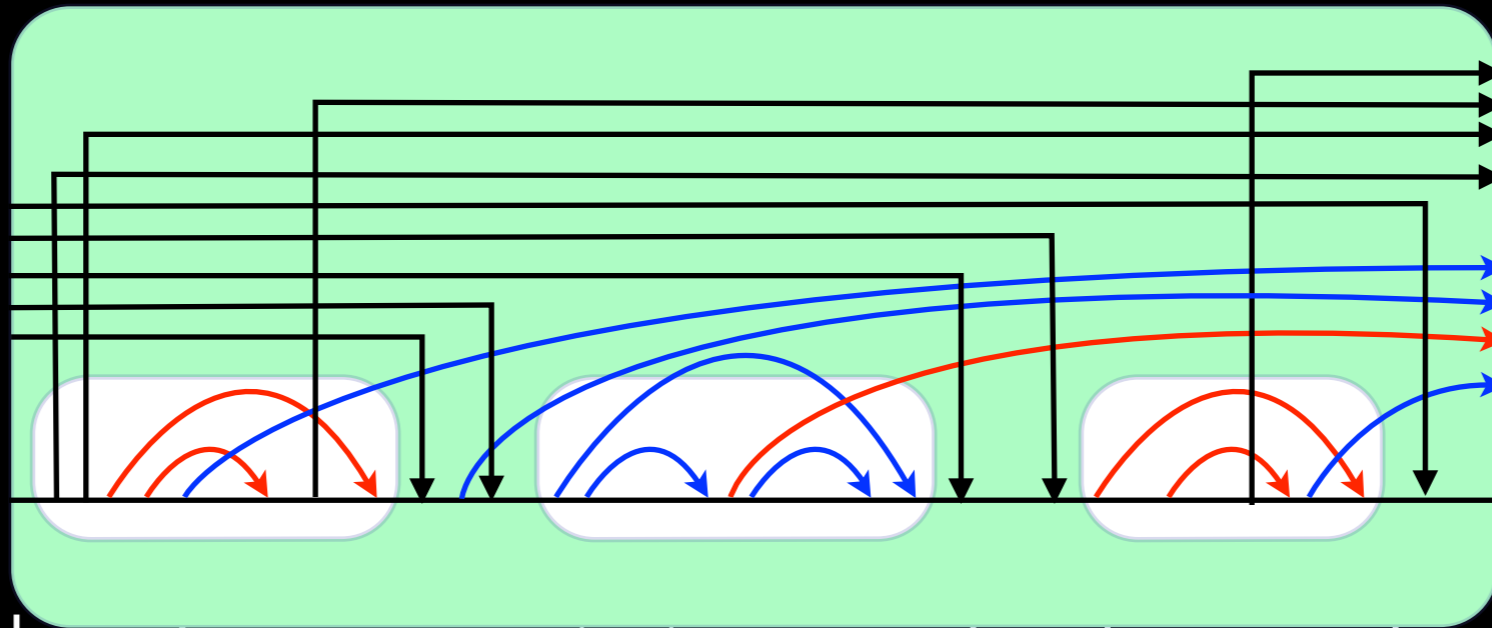
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PHASE



- A stretch of computation which reads from at most one stack/queue
- free (unlimited) autonomous computations

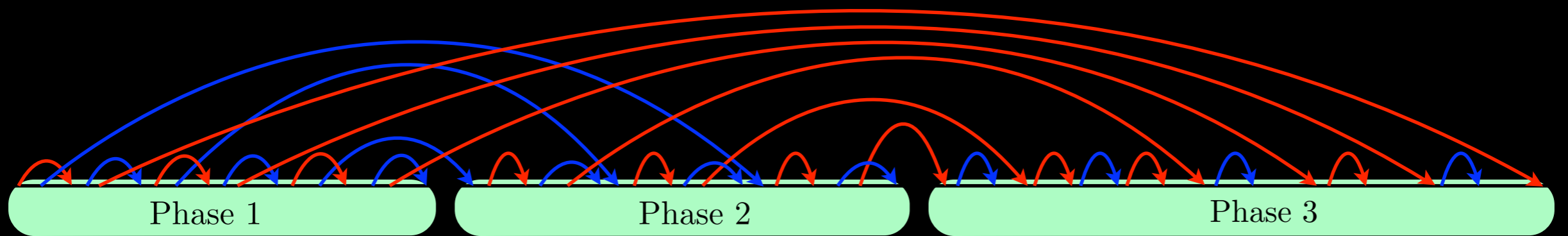
PHASE



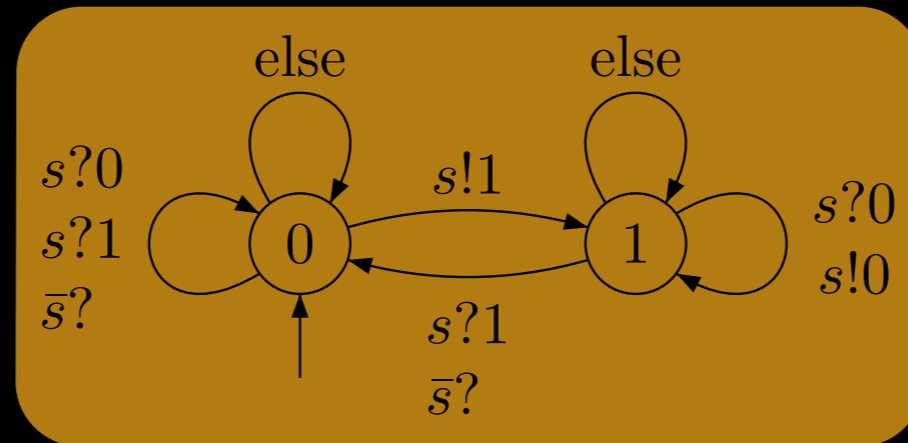
- A stretch of computation which reads from at most one stack/queue
- free (unlimited) autonomous computations
- no loops

K-BOUNDED PHASE

K-BOUNDED PHASE



IDENTIFYING AUTONOMOUS POPS



- Possible by tagging the values on stacks
- Deterministic controller for each stack
- The phase controller simulates one such automaton for each stack.

COMMUNICATING RECURSIVE PROGRAMS: CONTROL AND SPLIT-WIDTH

- C. A., Paul Gastin, and K. Narayan Kumar. Verifying communicating multi pushdown systems via Split-width. In *ATVA 2014*.
- C. A., Paul Gastin, and K. Narayan Kumar. Controllers for the verification of communicating multi-pushdown systems. In *CONCUR 2014*.
- A. C., Paul Gastin, and K. Narayan Kumar. MSO decidability of multi-pushdown systems via Split-width. In *CONCUR 2012*.
- A. C. *Verification of Communicating Recursive Programs via Split-width*. PhD thesis, ENS Cachan, 2014.

